RAppID Initialization for Power Architecture

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Overview & Features

Supported Devices
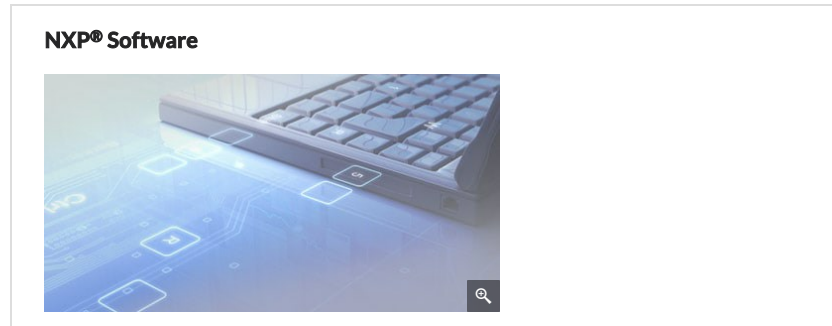
Target Applications

Overview

RAppID is a family of graphical development tools for the MPC5XXX family of Power Architecture® controllers that enables the user to quickly and easily configure the controller PLUS generate complete documentation. It can also be used as a learning tool to gain an understanding of the controller and it's peripherals. RAppID not only generates C code for initializing the registers, but it also provides a system initialization function that brings the controller up in an orderly sequence. Use RAppID to save time and become an expert on the MPC5XXX Family.

Features

- Intuitive, easy-to-use graphical user interface (GUI)
- Comprehensive initialization of the CPU, memory and peripherals
- Automatic DMA register setting from peripherals for basic modes
- Built-in consistency checks to minimize incorrect settings
- Automatic report generation of settings
- Efficient C and assembly code generation for compilers from companies such as Wind River®, Green Hills® and NXP®

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NXP® Software

Supported Devices

- **MPC5534**: 32-bit MCU for Low-End Automotive Powertrain Applications
- **MPC5553**: 32-bit MCU for Automotive Powertrain Applications
- **MPC5554**: 32-bit MCU for Powertrain Applications
- **MPC5561**: 32-bit MCU for Automotive ADAS Applications
- **MPC5566**: 32-bit MCU for Automotive Powertrain Applications
- **MPC5567**: 32-bit MCU for Auto Powertrain Applications

Target Applications

Motor Control

3-Phase AC Induction Motor   Brushless DC (BLDC) Motor