SOMC



Vishay Dale

Thick Film Resistor Networks, Dual-In-Line, Medium Body, Small Outline, Molded DIP, Surface Mount



FEATURES

- Isolated, bussed and terminator dual schematics available
- 14, 16, or 20 terminal package
- Molded case construction
- Thick film resistive elements
- Reflow solderable
- Compatible with automatic surface mounting equipment
- Reduces total assembly costs
- For wave flow soldering contact factory
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Note

This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

STAND	STANDARD ELECTRICAL SPECIFICATIONS								
GLOBAL MODEL	CIRCUIT	POWER RATING ELEMENT P70 °C W	POWER RATING PACKAGE P _{70 °C} W	TOLERANCE ⁽³⁾ ± %	RESISTANCE RANGE Ω	MAXIMUM WORKING VOLTAGE ⁽²⁾ V _{DC}	TEMPERATURE COEFFICIENT ⁽¹⁾ ± ppm/°C		
	01	0.08	1.05	1, 2, 5	10 to 1M	50	100		
SOMC14	03	0.16	1.125	1, 2, 5	10 to 1M	50	100		
	05	0.08	1.05	1, 2, 5	10 to 1M	50	100		
	01	0.08	1.20	1, 2, 5	10 to 1M	50	100		
SOMC16	03	0.16	1.28	1, 2, 5	10 to 1M	50	100		
	05	0.08	1.20	1, 2, 5	10 to 1M	50	100		
	01	0.08	1.52	1, 2, 5	10 to 1M	50	100		
SOMC20	03	0.16	1.60	1, 2, 5	10 to 1M	50	100		
	05	0.08	1.52	1, 2, 5	10 to 1M	50	100		

Notes

DSCC has created series of drawings to support the need for a surface mount gull wing resistor network product. Vishay Dale is listed as a resource on this drawing as follows:

DSCC DRAWING NUMBER	VISHAY DALE MODEL	CIRCUIT	POWER RATING ELEMENT P _{70 °C} W	POWER RATING PACKAGE P _{70°C} W	RESISTANCE RANGE Ω	TOLERANCE ± %	TEMPERATURE COEFFICIENT (0 °C to 70 °C) ± ppm/°C	MAXIMUM WORKING VOLTAGE ⁽²⁾ V _{DC}
87012	SOMC160116 SOMC160317 SOMC160548	01 (B) 03 (A) 05 (J)	0.08 0.16 0.08	1.20	10 to 2.2M	1, 2, 5	100, 300	50
87013	SOMC14016 SOMC140313 SOMC140522	01 (B) 03 (A) 05 (J)	0.08 0.16 0.08	1.00	10 to 2.2M	1, 2, 5	100, 300	50

These drawings can be viewed at: www.landandmaritime.dla.mil/Programs/MilSpec/ListDwgs.aspx?DocTYPE=DSCCdwg.

Power rating depends on the max. temperature at the solder point, the component placement density and the substrate material

Jumper: 0 Ω -resistor on request (100 m Ω)

Packaging: According to EIA; see appropriate catalog or web page

(1) Temperature range: -55 °C to +125 °C

⁽²⁾ Continuous working voltage shall be $\sqrt{P \times R}$ or maximum working voltage, whichever is less

 $^{(3)}$ ± 2 % standard, ± 1 % and ± 5 % available

TECHNICAL SPECIFICATIONS						
UNIT	01 CIRCUIT	03 CIRCUIT	05 CIRCUIT			
W	0.08	0.16	0.08			
V _{DC}		50				
ppm/V	< 50					
V _{DC/AC} peak	200					
Category temperature range °C		-55 / +150				
Ω	> 10 ¹⁰					
ppm/°C	50					
-	UNIT W V _{DC} ppm/V V _{DC/AC} peak °C Ω	UNIT 01 CIRCUIT W 0.08 V _{DC} 0.08 ppm/V 0.08 V _{DC/AC} peak 0.08 °C 0 Ω 0	$\begin{array}{c c c c c c c c } & 01 \ CIRCUIT & 03 \ CIRCUIT \\ \hline W & 0.08 & 0.16 \\ \hline V_{DC} & 50 \\ \hline ppm/V & <50 \\ \hline V_{DC/AC} \ peak & 200 \\ \hline ^{\circ}C & -55 \ / +150 \\ \hline \Omega & > 10^{10} \\ \hline \end{array}$			

Note

⁽¹⁾ Rated voltage: $\sqrt{P \times R}$

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1 For technical questions, contact: ff2aresistors@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT

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GLOBAL P	GLOBAL PART NUMBER INFORMATION								
New Global Pa	rt Numbering:	SOMC16011K0	OGDC (pre	ferred p	art numberi	ing f	ormat)		
S	0 М С	1 6	0 1	1	К	0	0 G	D C	
GLOBAL MODEL	PIN COUNT	SCHEMATIC	RESIST. VAL	-	TOLERAN CODE		PAC	KAGING	SPECIAL
SOMC	14 16 20	01 = bussed 03 = isolated 00 = special	R = K = M = 1 10R0 =	kΩ MΩ	F = ± 1 9 G = ± 2 9 J = ± 5 9 S = spec	% %	EA = lead (Pb)	(Pb)-free, tube -free, tape and reel n / lead, tube	Blank = standard (dash number) (up to 3 digits) from 1 to 999 as
			680K = 6 1M00 = 7 0000 = jump	580 kΩ 1.0 MΩ = 0 Ω	Z = 0 Ω jumper	2	RZ = tin / le	ad, tape and reel	applicable
	Historical Part Number Example: SOMC1601102G (will continue to be accepted)								
SOMC		16	01			102		G	D02
HISTORIC MODEL		I COUNT	SCHEM	IATIC	RESIS V/	STAN ALUE		DLERANCE CODE	PACKAGING
New Global Pa	rt Numbering:	SOMC2005500E	BGRZ (pref	erred p	art numberi	ng fo	ormat)		
S	0 M C	2 0	0 5	5 5	0	0	BG	R Z	
GLOBAL MODEL	PIN COUNT	SCHEMATIC	RESIST	-	TOLERAN CODE	ICE	PAC	KAGING	SPECIAL
SOMC	14 16	05 = dual terminator			$F = \pm 1$ 9 $G = \pm 2$ 9		EJ = lead (Pb)-free, tube EA = lead (Pb)-free, tape and reel		Blank = standard (dash number)
	$\begin{array}{c c c c c c c c c c c c c c c c c c c $						`up to 3 digits´ from 1 to 999 as		
Historical Part	Number Exam	ple: SOMC2005	820131G (v	will cont	inue to be a	acce	pted)		
SOMC	20		05		820		131	G	R61
HISTORICAL MODEL	PIN COL	INT SCH	EMATIC	-	STANCE ALUE 1	R	ESISTANCE VALUE 2	TOLERANCE CODE	PACKAGING

Note

• For additional information on packaging, refer to the Surface Mount Network Packaging document (<u>www.vishay.com/doc?31540</u>)

2

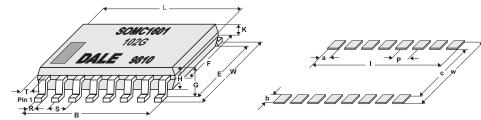
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CIRCUIT APPLICATIONS 01 Schematic 13, 15, or 19 resistors with one pin common Ş Ş Ş The SOMCxx01 circuit provides a choice of 13, 15, or 19 nominally equal resistors, each connected between a common lead (14, 16, or 20) and a discrete PC board pin. Commonly used in the following applications: Ş ì • MOS/ROM pull-up/pull-down • TTL input pull-down Ч • Open collector pull-up Digital pulse squaring SOMC14-• TTL unused gate pull-up • "Wired OR" pull-up SOMC16 -Pin 1 • Power driven pull-up • High speed parallels pull-up SOMC20 03 Schematic 7, 8, or 10 isolated resistors The SOMCxx03 circuit provides a choice of 7, 8, or 10 nominally equal resistors with each resistor isolated from all others and wired directly across. Commonly used in the following applications: • "Wired OR" pull-up • Long-line Impedance balancing • Power driven pull-up LED current limiting SOMC14 • Powergate pull-up ECL output pull-down SOMC16 Pin 1 Line termination • TTL input pull-down SOMC20 05 Schematic TTL dual-line terminator; pulse squaring, 12, 14, or 18 pairs of resistors (R1 resistors are common to leads 14, 16, or 20) (R₂ resistors are common to leads 7, 8, or 10) The SOMCxx05 circuit contains 12, 14, or 18 pairs of resistors. Each pair is connected between ground and a common line. The junctions of these resistor pairs are connected to the input leads. The 05 circuits are designed for TTL dual-line termination and pulse squaring. SOMC14, SOMC16, SOMC20

DIMENSIONS



SOLDER PAD DIMENSIONS in millimeters							
	а	b	с	Ι	р	w	
WAVE	0.64	1.91	5.34	9.53	1.27	9.15	
REFLOW	0.64	1.91	5.34	9.53	1.27	9.15	

Notes

• The dimension shown are for a 16 pin part. For parts with different pin numbers use the same pitch and add or subtract pads as required

Maximum solder reflow temperature +255 °C

DIMEN	DIMENSIONS in millimeters										
PIN NO#	L	W	В	E	F	G	Н	К	R	S	Т
14	9.91	7.62	7.62	6.20	5.59	2.16	2.03	0.914	0.457	1.27	1.14
16	11.18	7.62	8.89	6.20	5.59	2.16	2.03	0.914	0.457	1.27	1.14
20	13.72	7.62	11.43	6.20	5.59	2.16	2.03	0.914	0.457	1.27	1.14
Tol.	± 0.254	± 0.381	± 0.254	± 0.381	± 0.127	± 0.127	± 0.127		± 0.076	± 0.254	

MARKING INFORMATION

1 % parts have 4 digits while 2 % and 5 % parts have 3 digits.

Revision: 18-Apr-17

3

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			-		-
IM	PED	ΔΝ	CF	CO	DES

R ₁ (Ω)	R ₂ (Ω)	CODE	R ₁ (Ω)	R ₂ (Ω)			
82	130	141A	270	270			
120	200	181A	330	390			
130	210	191A	330	470			
160	260	221B	330	680			
180	240	281B	560	560			
180	270	381B	560	1.2K			
180	390	501C	620	2.7K			
220	270	102A	1.5K	3.3K			
220	330	202B	3K	6.2K			
	82 120 130 160 180 180 220	82 130 120 200 130 210 160 260 180 240 180 270 200 200 200 200 200 200	82 130 141A 120 200 181A 130 210 191A 160 260 221B 180 240 281B 180 270 381B 180 390 501C 220 270 102A	82 130 141A 270 120 200 181A 330 130 210 191A 330 160 260 221B 330 180 240 281B 560 180 270 381B 560 180 270 102A 1.5K			

Note

For additional impedance codes, refer to the Dual Terminator Impedance Code Table document (www.vishay.com/doc?31530)

PERFORMANCE						
TEST	CONDITIONS OF TEST	TEST RESULTS (TYPICAL TEST LOTS)				
Power conditioning	MIL-STD-202	± 0.5 %				
Load life at 70 °C	MIL-STD-202	± 0.5 %				
Short time overload	MIL-STD-202	± 0.25 %				
Thermal shock	MIL-STD-202	± 0.5 %				
Moisture resistance	MIL-STD-202	± 0.5 %				
Resistance to soldering heat	MIL-STD-202	± 0.25 %				
Low temperature operation	MIL-STD-202	± 0.25 %				
Vibration	MIL-STD-202	± 0.25 %				
Shock	MIL-STD-202	± 0.25 %				
Terminal strength	MIL-STD-202	± 0.25 %				

MECHANICAL SPECIFICATIONS					
Marking Model number, schematic number, value tolerance, pin 1 indicator, date code					
Marking resistance to solvents Permanency testing per MIL-STD-202, method 215					
Maximum solder reflow temperature	+255 °C				
Solderability Per MIL-STD-202, method 208E					
Terminals	Copper alloy. Solder dipped terminal				
Body	Molded epoxy				



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