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Vishay Sfernice

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COMPLIANT

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(5-2008)

High-Precision Thin Film Chip Resistor Arrays, Sulfur Resistant



LINKS TO ADDITIONAL RESOURCES



PRA arrays can be used in most applications requiring a matched pair (or set) of resistor elements. The networks provide 1 ppm/°C TCR tracking, a ratio tolerance as tight as 0.01 %, and outstanding stability.

They are available in pitch:

- 0.70 mm for PRA073 (based on case 0302)
- 0.70 mm for PRA074 (based on case 0402)
- 1.00 mm for PRA100 (based on case 0603)
- 1.35 mm for PRA135 (based on case 0805)
- 1.82 mm for PRA182 (based on case 1206)

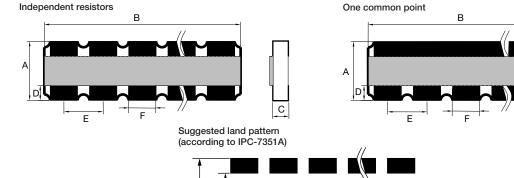
FEATURES

- High-stability passivated nichrome resistive layer 0.02 % on ratio, 1000 h at Pn at +70 °C
- Tight TCR (10 ppm/°C) and TCR tracking (to 1 ppm/°C)
- Very low noise < -35 dB and voltage coefficient < 0.01 ppm/V
- Ratio tolerance to 0.01 % ($R \ge 200R$)
- High-temperature (230 °C) version, see PRA HT
- ESA-qualified version, see PRA HR
- · SMD wraparound chip resistor array
- Thin film technology
- Option to withstand humidity test of AEC-Q200
- Sulfur resistant (per ASTM B809-95 humid vapor test)
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

TYPICAL PERFORMANCE

	ABSOLUTE	TRACKING
TCR	10 ppm/°C	2 ppm/°C
	ABSOLUTE	RATIO
TOL.	0.1 %	0.01 %

DIMENSIONS



DIM.	PRA073 (0302 base)		PRA074 (0402 base)		PRA100 (0603 base)		PRA135 (0805 base)		PRA182 (1206 base)	
	mm	mil	mm	mil	mm	mil	mm	mil	mm	mil
Α	0.75 ± 0152	29.5 ± 6	1.00 ± 0.152	40 ± 6	1.52 ± 0.152	60 ± 6	1.91 ± 0.152	75 ± 6	3.06 ± 0.152	120 ± 6
В	B = N x E (± 0.2 mm) B = N x E (± 8 mil)									
С	0.5 ± 0.127	20 ± 5	0.5 ± 0.127	20 ± 5	0.5 ± 0.127	20 ± 5	0.5 ± 0.127	20 ± 5	0.5 ± 0.127	20 ± 5
D	0.15 ± 0.08	5.9 ± 3	0.25 ± 0.1	10 ± 4	0.38 ± 0.13	15 ± 5	0.38 ± 0.13	15 ± 5	0.4 ± 0.13	16 ± 5
Е	0.7	27.5	0.7	27.5	1	40	1.35	53	1.825	72
F	0.55 ± 0.1	21.5 ± 4	0.55 ± 0.1	21.5 ± 4	0.7 ± 0.1	27.6 ± 4	1.05 ± 0.1	41.4 ± 4	1.525 ± 0.1	6 ± 4
G _{min.}	0.28	11	0.29	11.4	0.49	19.3	0.88	34.5	1.99	78.3
X _{max} .	0.51	20	0.51	20	0.66	26	1.01	39.8	1.49	58.7
Z _{max} .	1.8	70.9	2.05	80.7	2.57	101.2	2.96	116.5	4.11	161.8

Note

• N represents number of resistors

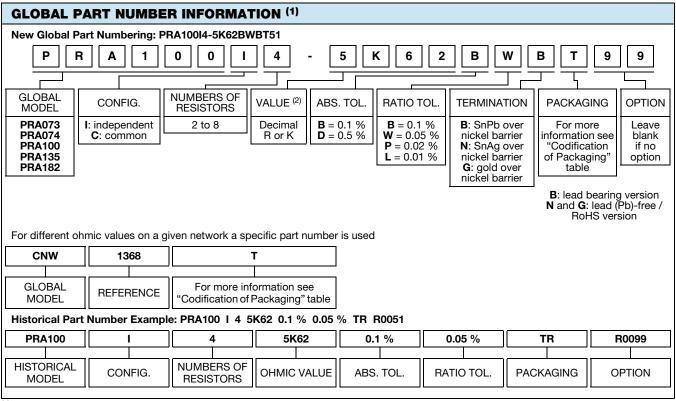
Revision: 04-Mar-2021

1 Document Number: 53033

PRA073, PRA074, PRA100, PRA135, PRA182 (CNW)

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Notes

- (1) Part number can only have 18 digits. Depending on information needed a compromise has to be found. Consult Vishay
- (2) When the last digit(s) of the ohmic value is (are) 0, it (they) must be omitted E.g.:PRA100I4-2K20BWN → must be ordered under PRA100I4-2K2BWN PRA100I4-2K00BWN → must be ordered under PRA100I4-2KBWN

STANDAR	STANDARD ELECTRICAL SPECIFICATIONS								
MODEL	SIZE	RESISTANCE RANGE	POWER RATING PER RESISTOR (1) W	ABSOLUTE TOLERANCE ± %	RATIO TOLERANCE ⁽²⁾ %	ABSOLUTE TCR ⁽³⁾ ± ppm/°C	RATIO TCR ⁽⁴⁾ ± ppm/°C		
PRA073	073	10 to 50K	0.030	0.1, 0.5	0.01, 0.02, 0.05, 0.1	10	1, 2		
PRA074	074	10 to 100K	0.040	0.1, 0.5	0.01, 0.02, 0.05, 0.1	10	1, 2		
PRA100	100	10 to 250K	0.100	0.1, 0.5	0.01, 0.02, 0.05, 0.1	10	1, 2		
PRA135	135	10 to 500K	0.125	0.1, 0.5	0.01, 0.02, 0.05, 0.1	10	1, 2		
PRA182	182	10 to 2M	0.200	0.1, 0.5	0.01, 0.02, 0.05, 0.1	10	1, 2		

Notes

- (1) At +70 °C
- (2) 0.02 % ($R \ge 50$ Ω), 0.01 % ($R \ge 200$ Ω)
- (3) At -40 °C to +125 °C (4) At -40 °C to +125 °C, 1 ppm/°C on request

CLIMATIC SPECIFICATIONS						
Operating temperature range (1) -55 °C to +155 °C						
Note						

For temperature up to 230 °C, se (www.vishay.com/doc?53057) or consult factory (1) For PRA HT see

PERFORMANCE VS. HUMID SULFUR VAPOR					
Test conditions	$50 ^{\circ}\text{C} \pm 2 ^{\circ}\text{C}, 85 \% \pm 4 \% \text{RH},$ exposure time 500 h				
Test results	Resistance drift $<$ (0.05 % R + 0.05 Ω), no corrosion products observed				

PERFORMANCES					
TEST	SPECIFICATIONS				
Noise	≤ -35 dB				
Voltage coefficient	≤ 0.01 ppm/V				
	PRA073	20 V			
	PRA074	40 V			
Limiting voltage	PRA100	50 V			
	PRA135	100 V			
	PRA182	150 V			

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MECHANICAL SPECIFICATIONS					
Substrate	Alumina				
Technology	Thin film				
Film	Nickel chromium with mineral passivation				
B type: SnPb over nickel barrier					
Terminations	N type: SnAg over nickel barrier				
	G type: Gold over nickel barrier				

SPECIAL FEATURES

Resistance values can be different on a given network (R max./R min. as high as 300). Tooling charges might be required depending on the ohmic values in the same network. Please, consult Vishay Sfernice for ohmic values, tolerances and also temperature coefficient (e.g. \pm 1 ppm/°C) outside the standard range.

AEC-Q200 OPTION: 0058

Vishay Sfernice offers a part compliant to AEC-Q200 specification.

PACKAGING

Several types of packaging are available: Waffle-pack and tape and reel.

		NUMBER OF PIECES PER PACKAGE				
0175	1400	WAFFI F DAOK MAY QUANTITY DED DOY	TAPE AN	D REEL (1)		
SIZE	MOQ	WAFFLE PACK MAX. QUANTITY PER BOX	MIN.	MAX.		
PRA073 x 2		400				
PRA073 x 3		100				
PRA073 x 4		140				
PRA073 x 5	100	140				
PRA073 x 6		60				
PRA073 x 7		60				
PRA073 x 8		60				
PRA074 x 2		400				
PRA074 x 3		100				
PRA074 x 4		140	100	4000		
PRA074 x 5	100	140				
PRA074 x 6		60				
PRA074 x 7		60				
PRA074 x 8		60				
PRA100 x 2		100	100	4000		
PRA100 x 3		140	100	4000		
PRA100 x 4		60	100	4000		
PRA100 x 5	100	50				
PRA100 x 6		50	100	3000		
PRA100 x 7		50				
PRA100 x 8		28	100	4000		
PRA135 x 2		140	100	4000		
PRA135 x 3		60				
PRA135 x 4		60	100	4000		
PRA135 x 5	100	50				
PRA135 x 6		28	100	4000		
PRA135 x 7		24				
PRA135 x 8		24				
PRA182 x 2		60	100	2000		
PRA182 x 3		60	100	4000		
PRA182 x 4		50	100	2000		
PRA182 x 5	100	21	100	1500		
PRA182 x 6		24				
PRA182 x 7		24				
PRA182 x 8		20				

Note

⁽¹⁾ Other sizes upon request

CODIFICATION OF PACKAGING					
CODE 18	PACKAGING				
WAFFLE PACK					
W	100 min., 1 mult.				
PLASTIC TAPE (Standard	for all sizes.)				
Т	100 min., 1 mult.				
TA	100 min., 100 mult.				
TB	250 min., 250 mult.				
TC	500 min., 500 mult.				
TD	1000 min., 1000 mult.				
TE	2500min., 2500 mult.				
TF	Full tape (quantity depending on size of chips)				

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PRA073, PRA074, PRA100, PRA135, PRA182 (CNW)

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PACKAGING RULES

Waffle Pack

Can be filled up to maximum quantity indicated in the table here above, taking into account the minimum order quantity. When quantity ordered exceeds maximum quantity of a single waffle pack, the waffle packs are stacked up on the top of each other and closed by one single cover.

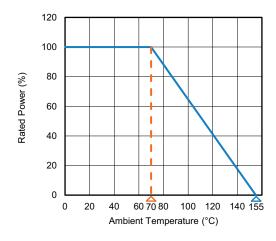
To get "not stacked up" waffle pack in case of ordered quantity > maximum number of pieces per package: Please consult Vishay Sfernice for specific ordering code.

Tape and Reel

Can be filled up to maximum quantity indicated in the table here above, taking into account the minimum order quantity. When quantity ordered is between the MOQ and the maximum reel capacity, only one reel is provided.

When several reels are needed for ordered quantity within MOQ and maximum reel capacity: Please consult Vishay Sfernice for specific ordering code.

POWER RATING



MARKING (1)

On the primary package, printed information includes Vishay S.A. trademark series and model, schematic number of resistors, ohmic value, absolute tolerance, ratio tolerance, type of termination: B tinned over nickel barrier.

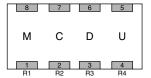
Marking on parts:

All resistors inside network have same ohmic value:

If number of resistors inside network < or = 3



For instance ohmic value 13K: Coded 1302: M = 1, C = 3, D = 0, U = 2If number of resistors inside networks > 3



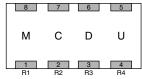
E.g.: 4 resistors in the network:

Ohmic value 13K: Coded 1302: M = 1, C = 3, D = 0, U = 2 Resistors inside the network have different ohmic value, a

<u>CNW number is assigned by Vishay Sfernice</u> If number of resistors inside network < or = 3



E.g.: CNW1538: M = 1, C = 5, D = 3, U = 8 If number of resistors inside networks > 3



E.g.: 4 resistors in the network:

E.a.: CNW1314: M = 1, C = 3, D = 1, U = 4

Note

(1) PRA073 and PRA074 are NOT marked. For CNW of size 073 and 074, only a "dot" is marked to identify R1

PERFORMANCE						
	CONDITIONS	DRII	FTS			
TESTS	CECC REQUIREMENTS	ABSOLUTE PER (Typical Values)	RATIO			
Overload	2.5 Un/2 s	0.05 % Rn + 0.05 Ω	0.01 % Rn			
Climatic sequences	-55 °C to +155 °C/5 moisture cycles	0.1 % Rn + 0.05 Ω	0.01 % Rn			
Thermal shock	-55 °C to +155 °C/5 cycles 30'	0.05 % Rn + 0.05 Ω	0.01 % Rn			
Load life	1000 h/Pn at 70 °C	$0.1~\%~$ Rn + $0.05~\Omega$	0.02 % Rn			
Resistance to solder heat	260 °C/10 s	0.05 % Rn + 0.05 Ω	0.01 % Rn			
Moisture resistance	0.01 Pn at + 40 °C 93 % RH	0.1 % Rn + 0.05 Ω	0.01 % Rn			
High temperature storage	1000 h/no load at +155 °C	0.1 % Rn + 0.05 Ω	0.02 % Rn			

Note

• Rn: nominal resistance

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