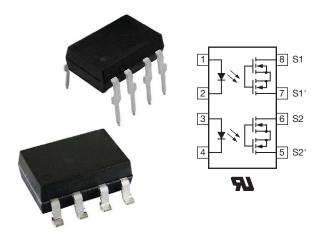


Vishay Semiconductors

## **Dual 1 Form A Solid-State Relay (Normally Open)**



#### DESCRIPTION

The LH1505 contains two normally open switches (dual 1 Form A) that can be used as two independent SPST relays or as one DPST relay. It can replace electromechanical relays in many applications. They are constructed using a GaAlAs LED for actuation control and MOSFET switches for the output. In addition, the LH1505 SSRs employ current-limiting circuitry when overvoltage protection is provided.

#### **FEATURES**

- Isolation test voltage 5300 V<sub>RMS</sub>
- Current limit protection
- Typical R<sub>ON</sub> 12 Ω
- Load voltage 250 V
- Load current 120 mA
- Clean bounce free switching
- Low power consumption
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### APPLICATIONS

- General telecom switching
- Security equipment
- Instrumentation
- Industrial controls
- Automatic test equipment

### AGENCY APPROVALS

• UL1577, file no. E52744

ORDERING INFORMATION	
L H 1 5 0 5 A	# # T R DIP SMD
PART NUMBER ELECTR. VARIATION	PACKAGE TAPE AND CONFIG. REEL
PACKAGE	UL
SMD-8, tape and reel	LH1505AACTR
SMD-8, tube	LH1505AAC
DIP-8, tube	LH1505AB





1

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### **Vishay Semiconductors**

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	CONDITION	SYMBOL	VALUE	UNIT
INPUT				
IRED continuous forward current		I <sub>F</sub>	50	mA
IRED reverse voltage		V <sub>R</sub>	5	V
Input power dissipation		P <sub>diss</sub>	80	mW
OUTPUT				
DC or peak AC load voltage		VL	250	V
Continuous DC load current at 25 °C, one channel		١L	130	mA
Continuous DC load current at 25 °C, two channels		۱ <sub>L</sub>	120	mA
SSR output power dissipation		P <sub>diss</sub>	550	mW
SSR				
Ambient temperature range		T <sub>amb</sub>	-40 to +85	°C
Storage temperature range		T <sub>stg</sub>	-40 to +150	°C
Soldering temperature	t = 10 s max.	T <sub>sld</sub>	260	٦°

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = 25 \degree C$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
IRED forward current, switch turn-on	I <sub>L</sub> = 100 mA, t = 10 ms	I <sub>Fon</sub>	-	0.4	2	mA
IRED forward current, switch turn-off	$V_L = \pm 200 V$	I <sub>Foff</sub>	0.05	0.35	-	mA
IRED forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>	1.15	1.36	1.45	V
IRED reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	10	μA
OUTPUT	·			•	•	
On-resistance	$I_{\rm F} = 5 \text{ mA}, I_{\rm L} = 50 \text{ mA}$	R <sub>ON</sub>	6	12	20	Ω
Off-resistance	$I_{\rm F} = 0 \text{ mA}, V_{\rm L} = \pm 100 \text{ V}$	R <sub>OFF</sub>	0.5	5000	-	GΩ
	$I_{\rm F} = 0 \text{ mA}, V_{\rm L} = \pm 100 \text{ V}$	Ι <sub>Ο</sub>	-	< 1	200	nA
Off-state leakage current	$I_{\rm F} = 0 \text{ mA}, V_{\rm L} = \pm 200 \text{ V}$	Ι <sub>Ο</sub>	-	< 1	1000	nA
	$I_F = 0 \text{ mA}, V_L = 1 \text{ V}, 1 \text{ MHz}$	Co	-	39	-	pF
Output capacitance	I <sub>F</sub> = 0 mA, V <sub>L</sub> = 50 V, 1 MHz	Co	-	6	-	pF
Current limit AC/DC	$I_F = 5 \text{ mA}, t = 5 \text{ ms}, V_L = \pm 6 \text{ V}$	I <sub>limit</sub>	300	440	550	mA
TRANSFER	·			•	•	•
Capacitance (input to output)	V <sub>IO</sub> = 1 V	C <sub>IO</sub>	-	0.4	-	pF

#### Note

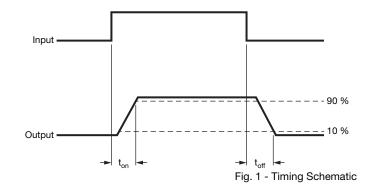
 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements

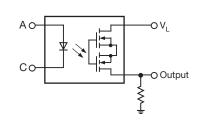
SWITCHING CHARACTERISTICS ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	I <sub>F</sub> = 5 mA, I <sub>L</sub> = 50 mA	t <sub>on</sub>	-	0.20	4	ms
Turn-off time	$I_{\rm F} = 5  {\rm mA},  I_{\rm L} = 50  {\rm mA}$	t <sub>off</sub>	-	0.03	4	ms

2



**Vishay Semiconductors** 

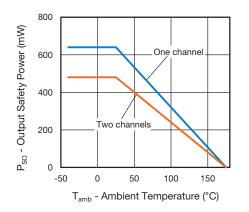


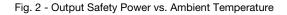


PARAMETER	CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		40 / 85 / 21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group IIIa	CTI	175	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	VISO	5300	V <sub>RMS</sub>
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	8000	Vpeak
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	VIORM	890	V <sub>peak</sub>
Insulation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
Insulation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
Output safety power	One channel	D	640	mW
	Two channels	P <sub>SO</sub>	480	
Input safety current	One channel	I	240	mA
input salety current	Two channels	I <sub>SI</sub>	200	
Safety temperature		T <sub>S</sub>	175	°C
Creepage distance			≥7	mm
Clearance distance			≥7	mm
Insulation thickness		DTI	≥ 0.4	mm
Input to output test voltage, method B	$V_{IORM} x 1.875 = V_{PR}$ , 100 % production test with t <sub>M</sub> = 1 s, partial discharge < 5 pC	V <sub>PR</sub>	1669	V <sub>peak</sub>
Input to output test voltage, method A	$V_{IORM} \times 1.6 = V_{PR}$ , 100 % sample test with t <sub>M</sub> = 10 s, partial discharge < 5 pC	V <sub>PR</sub>	1424	V <sub>peak</sub>

Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits





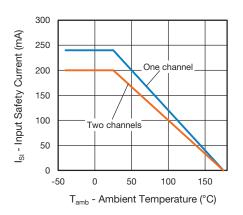


Fig. 3 - Input Safety Current vs. Ambient Temperature

3



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### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

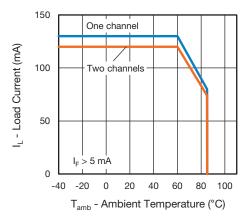


Fig. 4 - Load Current vs. Ambient Temperature

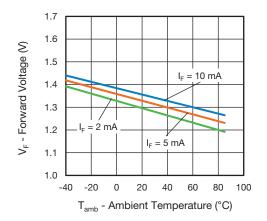


Fig. 5 - Forward Voltage vs. Ambient Temperature

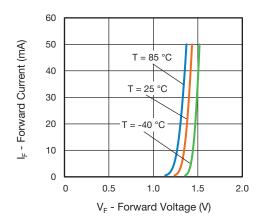


Fig. 6 - Forward Current vs. Forward Voltage

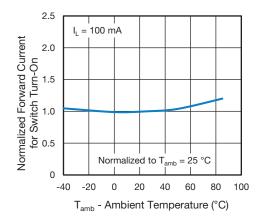


Fig. 7 - Normalized Forward Current vs. Ambient Temperature

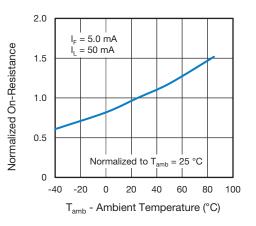


Fig. 8 - Normalized On-Resistance vs. Ambient Temperature

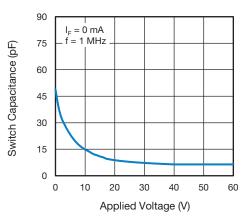


Fig. 9 - Switch Capacitance vs. Load Voltage

Rev. 1.7, 05-Jul-2018

4 For technical questions, contact: <u>optocoupleranswers@vishay.com</u> Document Number: 83809

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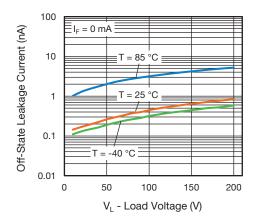


Fig. 10 - Leakage Current vs. Load Voltage

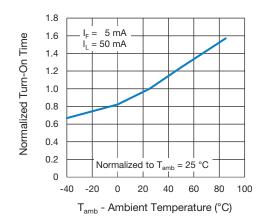


Fig. 11 - Normalized Turn-On Time vs. Ambient Temperature

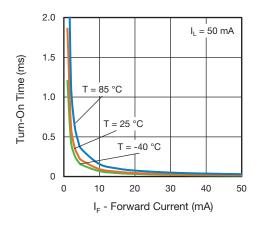


Fig. 12 - Turn-On Time vs. Forward Current

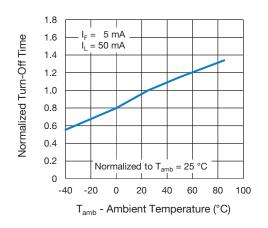


Fig. 13 - Normalized Turn-Off Time vs. Ambient Temperature

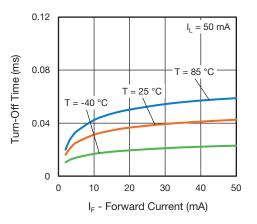


Fig. 14 - Turn-Off Time vs. Forward Current

5

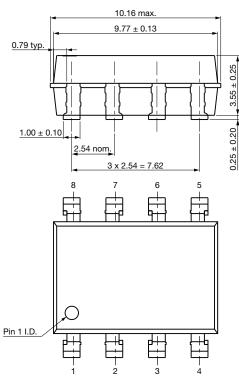
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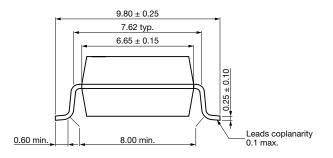


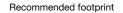
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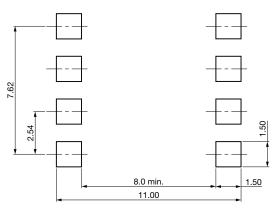
### **PACKAGE DIMENSIONS** in millimeters

SMD-8





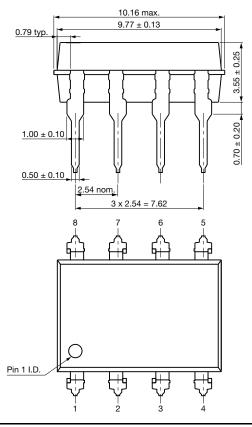




7.62 typ.

 $6.65 \pm 0.15$ 

#### DIP-8



Rev. 1.7, 05-Jul-2018

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 $6.09 \pm 0.25$ 

 $0.25 \pm 0.10$ 

 $3.04 \pm 0.25$ 

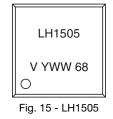
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<u>3° to 9°</u>



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### **PACKAGE MARKING** (example)



#### Note

• Tape and reel suffix (TR) is not part of the package marking

#### **PACKING INFORMATION** (in millimeters)

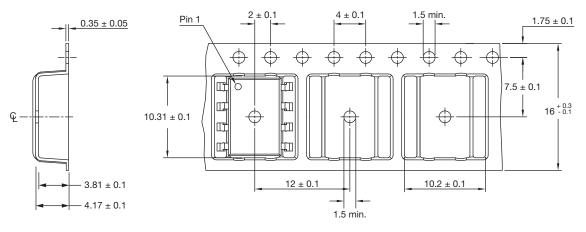


Fig. 16 - Tape and Reel Packing

TAPE AND REEL PACKING	
ТҮРЕ	UNITS/REEL
SMD-8	1000

TUBE PACKING			
ТҮРЕ	UNITS/TUBE	TUBES/BOX	UNITS/BOX
SMD-8	50	40	2000
DIP-8	50	40	2000



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### SOLDER PROFILES

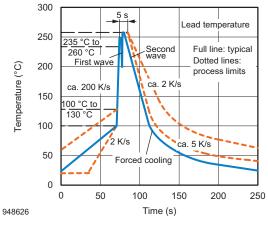


Fig. 17 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices

#### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2

Floor life: unlimited

Conditions: T<sub>amb</sub> < 30 °C, RH < 60 %

Moisture sensitivity level 1, according to J-STD-020

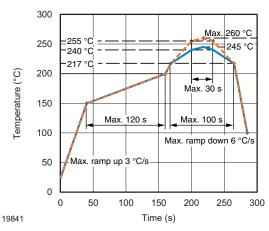


Fig. 18 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices



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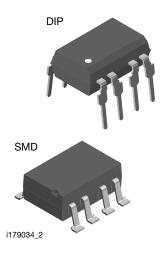
### **Footprint and Schematic Information** for LH1505AAC, LH1505AACTR, LH1505AB

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC	
LH1505AAC	www.snapeda.com/parts/LH1505AAC/Vishay/view-part	
LH1505AACTR	www.snapeda.com/parts/LH1505AACTR/Vishay/view-part	
LH1505AB	www.snapeda.com/parts/LH1505AB/Vishay/view-part	

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