

MECHANICAL DATA

Module Dimension

Viewing Area Dot Size

Mounting Hole

Character Size

Dot Pitch

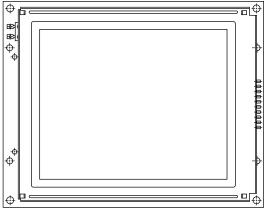
ITEM

Vishay

RoHS

COMPLIANT

160 x 128 Graphic LCD



STANDARD VALUE

129.0 x 102.0

101.0 x 82.0

0.54 x 0.54

0.58 x 0.58

122.0 x 96.2

N/a

FEATURES

• Type: Graphic

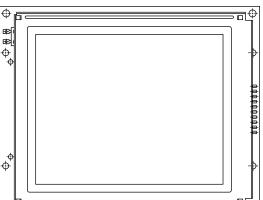
• Display format: 160 x 128 dots • Built-in controller: RA6963

• Duty cycle: 1/128 · Optional N.V.

• + 5 V power supply

• View angle 12° horizontal only

• Material categorization: For definitions of compliance please see www.vishav.com/doc?99912



BSOLUTE MAXIMUM RATINGS				
CYMPOL	STAN	IDARD V	ALUE	UNI.
STIVIBUL	MIN.	TYP.	MAX.	UNI
V _{DD} to V _{SS}	4.75	5.0	5.25	V
VI	- 0.3	-	V_{DD}	
	SYMBOL	SYMBOL STAN MIN. V _{DD} to V _{SS} 4.75	SYMBOL STANDARD V. MIN. TYP. V _{DD} to V _{SS} 4.75 5.0	STANDARD VALUE MIN. TYP. MAX. V _{DD} to V _{SS} 4.75 5.0 5.25

Note

UNIT

mm

• $V_{SS} = 0 \text{ V}, V_{DD} = 5.0 \text{ V}$

ELECTRICAL CHARACTERISTICS							
ITEM	SYMBOL	CONDITION	ST	LINUT			
		CONDITION	MIN.	TYP.	MAX.	UNIT	
Input Voltage	V_{DD}	-	4.75	5.0	5.25	V	
Supply Current	I _{DD}	V _{DD} = + 5 V	-	45.0	50.0	mA	
Recommended LC Driving		- 20 °C	19.9	21.0	22.1		
Voltage for Normal Temperature	V _{DD} to V ₀	25 °C	18.6	19.1	19.6	V	
Version Module		70 °C	11.6	9.1	12.8		
CCFL Starting Voltage	V _{FLS}	25 °C	-	-	-	V _{RMS}	
CCFL Driving Voltage	V _{FLD}	25 °C	-	256	560	V _{RMS}	
CCFL Driving Current	I _{FLD}	$V_{FQ} = 450 V_{RMS}$, 30 kHz	-	-	5.0	mA	
LED Forward Voltage	V _F	25 °C	-	4.6	4.6	V	
LED Forward Current	I _F	25 °C	-	-	500	mA	
EL Power Supply Current	I _{EL}	V _{EL} = 110 V _{AC} , 400 Hz	-	-	5.0	mA	

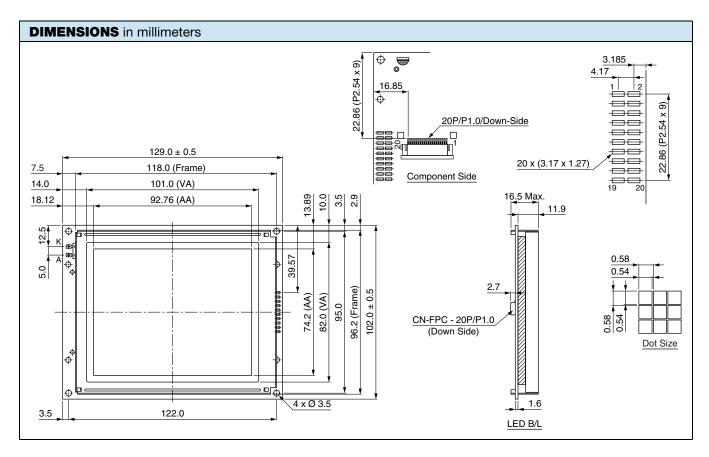
OPTION	S								
PROCESS COLOR				BACKLIGHT					
TN	STN GRAY	STN YELLOW	STN BLUE	FSTN B&W	STN COLOR	NONE	LED	EL	CCFL
	х	х	х	х		х	х	х	х

For detailed information, please see the "Product Numbering System" document.



www.vishay.com

INTERFACE P	NTERFACE PIN FUNCTION				
PIN NO.	SYMBOL	FUNCTION			
1	FG	Frame ground			
2	V _{SS}	Power supply (Ground)			
3	V _{DD}	Power supply (+ 5 V)			
4	V _{ADJ}	Contrast adjustment			
5	V _{EE}	Negative voltage output			
6	WR	Data write			
7	RD	Data read			
8	CE	Chip enable			
9	C/D	Command/data read/write			
10	HALT	Clock operating stop signal			
11	Reset	Reset signal			
12	DB0	Data bus line			
13	DB1	Data bus line			
14	DB2	Data bus line			
15	DB3	Data bus line			
16	DB4	Data bus line			
17	DB5	Data bus line			
18	DB6	Data bus line			
19	DB7	Data bus line			
20	NC	No connection			



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