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RoHS

COMPLIANT

# Low Profile, High Current Inductors



STANDARD ELECTRICAL SPECIFICATIONS								
L₀ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (µH)	DCR 25 °C (mΩ)		HEAT RATING CURRENT DC I <sub>DC</sub> (A) <sup>(3)</sup>		SATURATION CURRENT DC I <sub>SAT</sub> (A) <sup>(4)</sup>			
(µn)	TYP.	MAX.	TYP.	MAX.	TYP.	MAX.		
0.47	47	57	2.60	2.34	2.85	2.56		
1.0	87	107	1.70	1.50	2.00	1.80		
1.5	137	164	1.60	1.44	1.65	1.50		
2.2	192	230	1.35	1.22	1.45	1.31		
3.3	243	292	1.05	0.95	1.05	0.95		
4.7	322	387	0.95	0.85	0.95	0.80		
6.8	610	732	0.62	0.56	0.80	0.72		
10	932	1119	0.47	0.42	0.62	0.55		
22	2365	2838	0.37	0.33	0.45	0.40		

#### Notes

 $^{(1)}\,$  All test data is referenced to 25 °C ambient

<sup>(2)</sup> Operating temperature range -55 °C to +125 °C

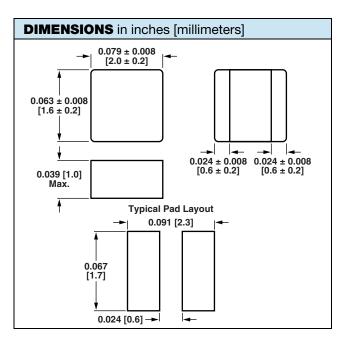
- <sup>(3)</sup> DC current (A) that will cause an approximate  $\Delta T$  of 40 °C
- <sup>(4)</sup> DC current (A) that will cause  $L_0$  to drop approximately 30 %
- (5) The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application

### **FEATURES**

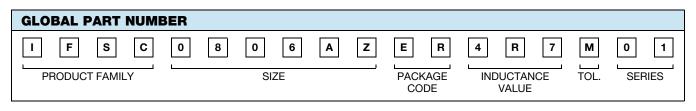
- Shielded construction
- Frequency range up to 5.0 MHz
- · Handles high transient current spikes without saturation
- HALOGEN FREE • Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- PDA / notebook / desktop / server applications
- High current POL converters
- · Low profile, high current power supplies
- DC/DC converters in distributed power systems
- DC/DC converter for field programmable gate array (FPGA)



DESCRIPTION								
IFSC-0806AZ-01	4.7 μH	± 20 %	ER	e3				
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC <sup>®</sup> LEAD (Pb)-FREE STANDARD				



Revision: 04-Jan-2021

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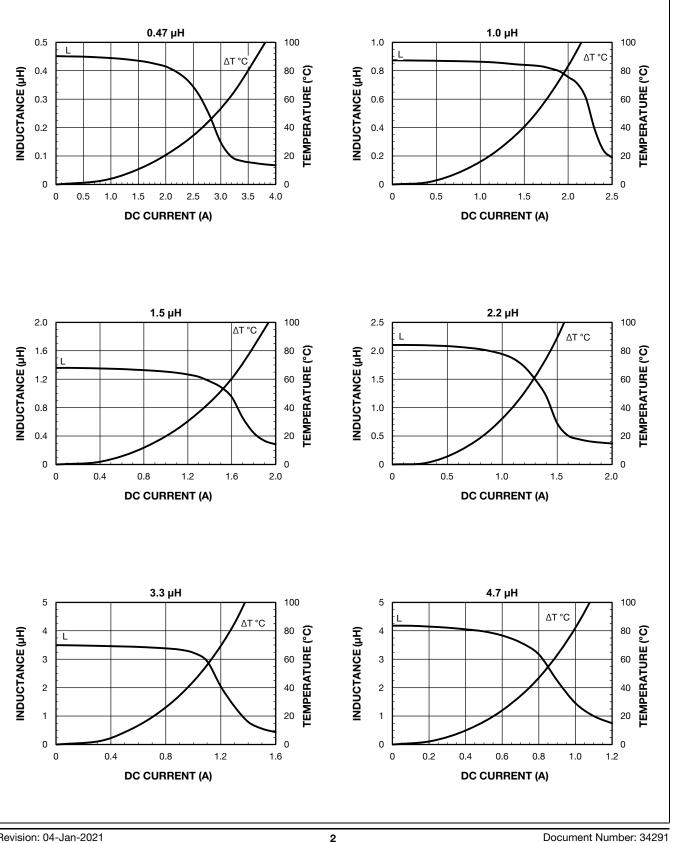
1 For technical questions, contact: magnetics@vishay.com

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#### **PERFORMANCE GRAPHS**



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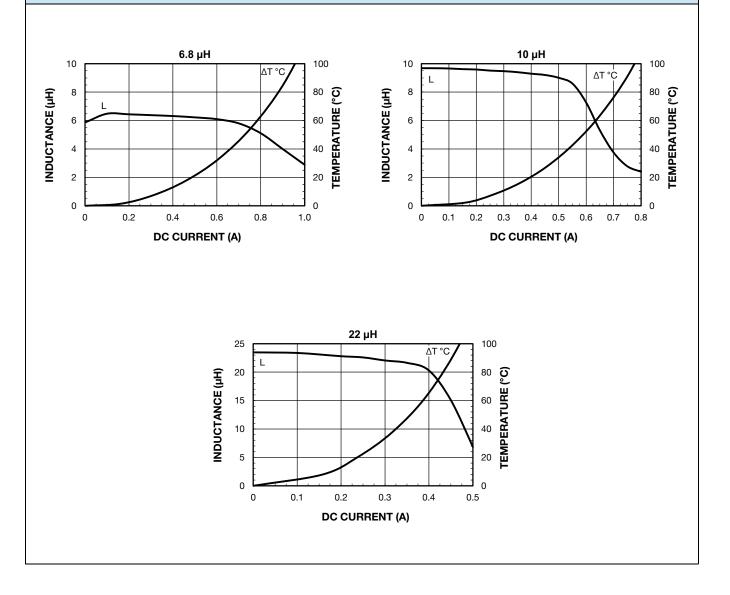
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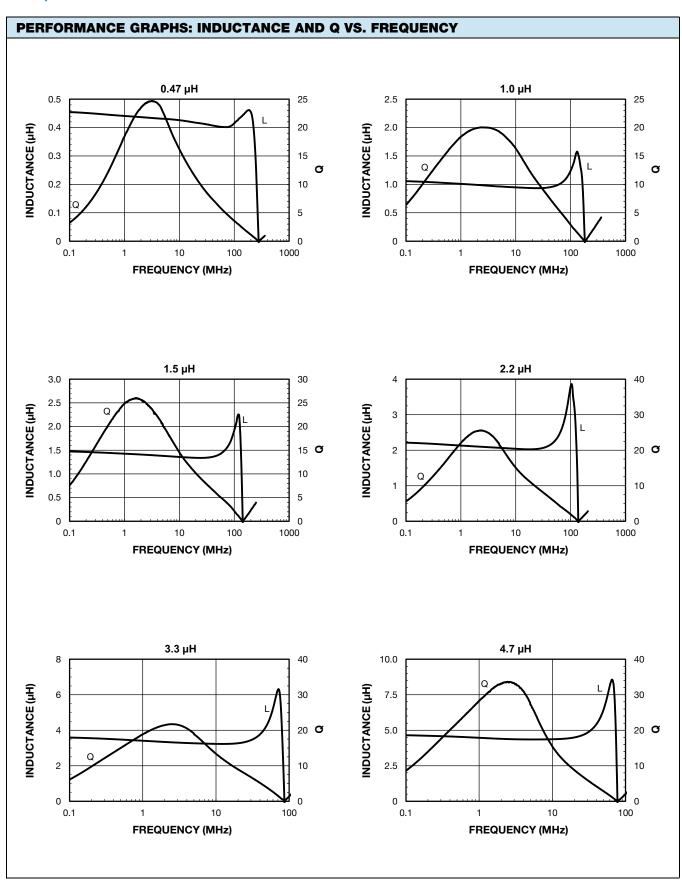


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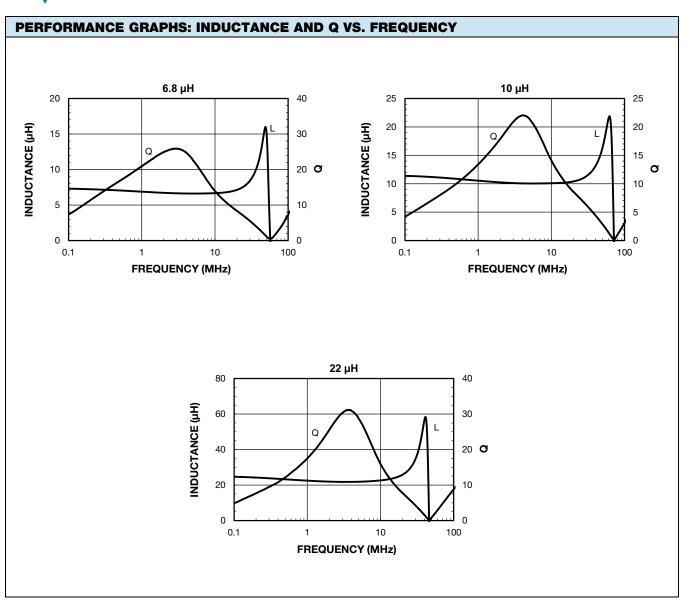
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