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Vishay Dale

# **Ultra Low DCR Inductors, High Current, Vertical Mount Series**



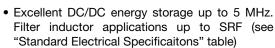
STANDARD ELECTRICAL SPECIFICATIONS					
L <sub>0</sub> INDUCTANCE AT 100 kHz, 0.1 V, 0 A	DCR ± 7 % 25 °C	HEAT RATING CURRENT DC TYP.	SATURATION CURRENT DC TYP. (A)		SRF TYP.
(μĤ)	(mΩ)	(A) <sup>(1)</sup>	(2)	(3)	(MHz)
0.12	0.24	90	91	130	112

#### **Notes**

- All test data is referenced to 25 °C ambient
- Operating temperature range -40 °C to +155 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- $^{(1)}\,$  DC current (A) that will cause an approximate  $\Delta T$  of 40 °C
- $^{(2)}\,$  DC current (A) that will cause  $L_0$  to drop approximately 20 %
- (3) DC current (A) that will cause L<sub>0</sub> to drop approximately 30 %

### **FEATURES**

- High temperature rating, up to 155 °C
- · Shielded construction



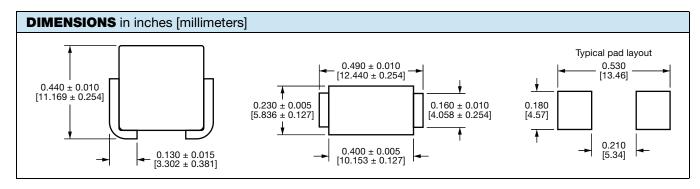


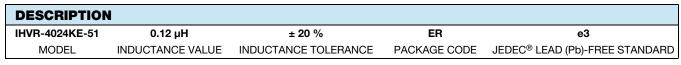
ROHS

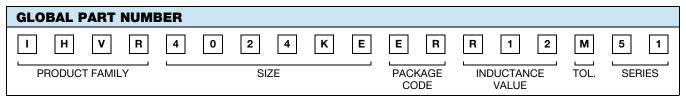
- Lowest DCR/µH, in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- Patent pending
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

### **APPLICATIONS**

- Desktop / server applications
- High current POL converters
- · Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)

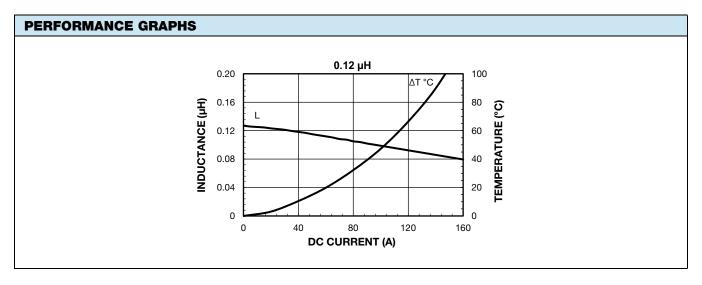


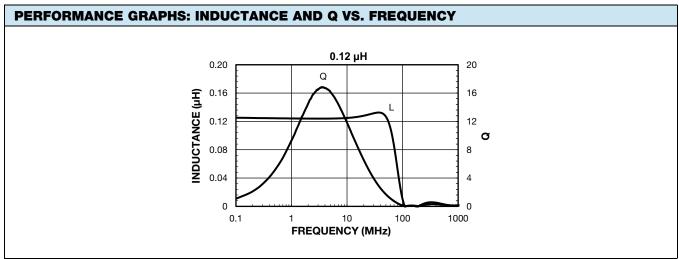




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