# IHCL-4040DZ-5A 4.7 μH

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Vishay Dale

RoHS COMPLIANT

HALOGEN

FREE

**GREEN** 

(5-2008)

## Low-Profile, High-Current Coupled Inductor



### **DESIGN SUPPORT TOOLS**

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Models Available

Design Tools Available

STANDARD ELECTRICAL SPECIFICATIONS								
L <sub>0</sub> INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH)	-		HEAT RATING CURRENT DC TYP. (A) <sup>(1)</sup>	SATURATION CURRENT DC TYP. (A) <sup>(2)</sup>				
4.7	30.5	33	6.1	7.2				
4.7	30.5	33	6.1	9.2				
19	61	65	4	3				
0.6	61	65	4	See note <sup>(3)</sup>				
4.7	15.25	16.2	10	8				
0.0	15.25	16.2	10	See note <sup>(3)</sup>				
	L <sub>0</sub> INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH) 4.7 4.7 19 0.6 4.7	L₀ DCR   INDUCTANCE DCR   ± 20 % AT NOM.   100 kHz, 25 °C   0.25 V, 0 A (µH)   4.7 30.5   4.7 30.5   19 61   0.6 61   4.7 15.25	L₀ DCR DCR   ± 20 % AT DCR MAX.   100 kHz, 25 °C (mΩ)   0.25 V, 0 A 30.5 33   4.7 30.5 33   19 61 65   0.6 61 65   4.7 15.25 16.2	L₀ L₀ HEAT   iNDUCTANCE DCR DCR MAX   100 kHz, DCR DCR MAX   0.25 V, 0 A 25 °C (mΩ) MAX   4.7 30.5 33 6.1   4.7 30.5 33 6.1   19 61 65 4   0.6 61 65 4   4.7 15.25 16.2 10				

#### Notes

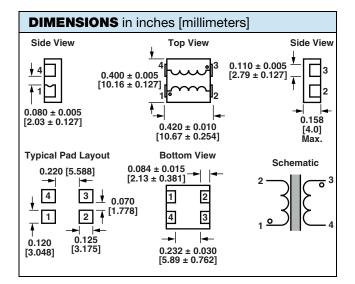
- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +155 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 50 V
- SEPIC operation can generate up to 2x the input or output voltage across the inductor. Please limit  $V_{IN}$  and  $V_{OUT}$  to 25 V max. for SEPIC operation
- $^{(1)}\,$  DC current (A) that will cause an approximate  $\Delta T$  of 40  $^{\circ}C$
- <sup>(2)</sup> DC current (A) that will cause  $L_0$  to drop approximately 20 % (3)
- In this configuration, current flowing opposite directions through coils cancels and the 0.6 µH inductance is very stable with varying current. Observe the heat rating current to avoid excessive temperature rise in this configuration

## **FEATURES**

- High temperature, up to 155 °C
- Shielded construction
- Frequency range up to 5.0 MHz
- Lowest DCR/µH in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- Coupling is > 90 % optimized for SEPIC converters
- AEC-Q200 gualified
- Patent pending
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

### APPLICATIONS

- SEPIC converters
- DC/DC converters
- · Common mode applications
- LED lighting



DESCRIPTION								
IHCL-4040DZ-5A	4.7 µH	± 20 %	ER	e3				
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	IDUCTANCE TOLERANCE PACKAGE CODE		JEDEC <sup>®</sup> LEAD (Pb)-FREE STANDARD			
GLOBAL PAR	T NUMBER							
ІНС	L 4 0	4 0 D Z	E R 4	R 7 M	5 A			
PRODUCT FAM	MILY	SIZE	PACKAGE CODE	INDUCTANCE TOL VALUE	SERIES			
Revision: 09-Feb-18		1		Docume	ent Number: 34356			
For technical questions, contact: magnetics@vishav.com								

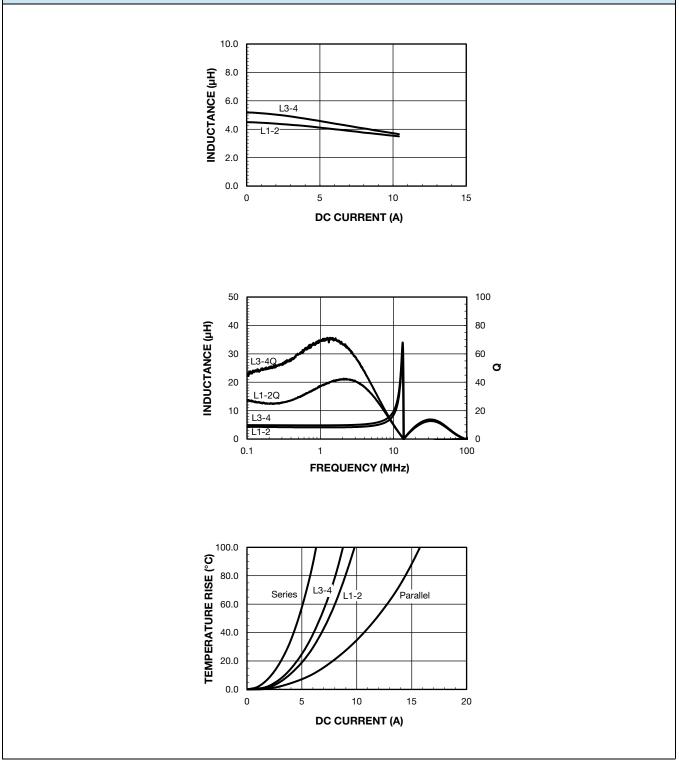
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2 For technical questions, contact: <u>magnetics@vishay.com</u>

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