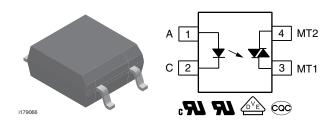


Vishay Semiconductors

Optocoupler, Phototriac Output, Non-Zero Crossing, 0.5 kV/µs dV/dt, 600 V



DESCRIPTION

The VOM160 series phototriac consist a AlGaAs infrared emitting diode (IRED) optically coupled to a photosensitive non-zero crossing TRIAC packaged in a SOP-4 package. It has a IRED trigger current of 5 mA, 7 mA, and 10 mA.

The VOM160 series phototriac isolate low-voltage logic from 120 V_{AC} , 240 V_{AC} , and 380 V_{AC} lines to control resistive, inductive, or capacitive loads including motors, solenoids, high current thyristors or TRIAC and relays.

FEATURES

- High static dV/dt > 0.5 kV/µs
- Input sensitivity I_{FT} = 5 mA, 7 mA, and 10 mA
- On-state RMS current I_{T(RMS)} = 70 mA
- 600 V peak off-state blocking voltage
- Isolation test voltage 3750 V_{RMS}
- Material categorization:
 For definitions of compliance please see www.vishay.com/doc?99912





COMPLIANT

GREEN

(5-2008)

APPLICATIONS

- Consumer appliances
- Triac drives
- · Solid-state relays
- Motor controls
- Office equipment

AGENCY APPROVALS

- UL1577, file no. E52744, double protection
- cUL-file no. E52744, equivalent to CSA bulletin 5A
- VDE 0884-5, DIN EN 60747-5-5
- CQC: GB8898, GB4943

ORDERING INFORMATION								
	V	0	М	1 6 PART NUMBER	0	х	T	SOP-4 7.21 mm
AGENCY CERTIFIED/PACKAGE				TRIGGER CURRENT IFT				
AGEN	NCY CERT	TIFIED/PAC	CKAGE		Т	RIGGER CU	RRENT I _{FT}	
	UL, CQC	TIFIED/PAC	CKAGE	5 mA	Т	RIGGER CU 7 m		10 mA
	UL, CQC	TIFIED/PAC	CKAGE	5 mA VOM160NT	Т		A	10 mA VOM160RT
UL, cl	UL, CQC		CKAGE		Т	7 m	A 60PT	7 7 7 7 7

Notes

- For additional information on the available options refer to option information.
- The product is available only on tape and reel.



www.vishay.com

Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT				
INPUT									
Reverse voltage			V_R	6	V				
Forward current			I _F	60	mA				
Peak surge current	100 μs, 200 pps		I _{FSM}	0.5	Α				
Power dissipation			P _{diss}	100	mW				
OUTPUT									
Peak off-state voltage			V_{DRM}	600	V				
RMS on-state current			I _{T(RMS)}	70	mA				
Peak non-repetitive surge current	PW = 100 ms, 120 pps		I _{TSM}	1	Α				
Power dissipation			P _{diss}	200	mW				
COUPLER									
Isolation test voltage	t = 1 min		V _{ISO}	3750	V_{RMS}				
Power dissipation			P _{tot}	300	mW				
Storage temperature range			T _{stg}	- 55 to + 150	°C				
Ambient temperature range			T _{amb}	- 40 to + 100	°C				
Soldering temperature (1)			T _{sld}	260	°C				

Notes

- Stresses in excess of the Absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to Absolute
 Maximum Ratings for extended periods of the time can adversely affect reliability.
- (1) Wave soldering three cycles are allowed. Also refer to "Assembly Instructions" for surface mounted devices (www.vishay.com/doc?80054).

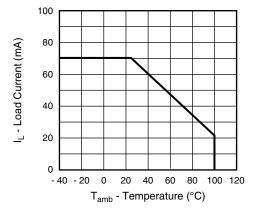


Fig. 1 - Recommended Operating Condition



Vishay Semiconductors

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT	INPUT								
Forward voltage	I _F = 10 mA		V _F		1.2	1.5	V		
Reverse current	V _R = 6 V		I _R			10	μΑ		
Input capacitance	$V_F = 0 V$, $f = 1 MHz$		Cı		25		pF		
OUTPUT									
Off-state current	$V_D = V_{DRM}$		I _{DRM}			100	nA		
On-state voltage	I _T = 100 mA		V _{TM}			2.8	V		
Critical rate of rise off-state voltage	$V_D = 0.67 \ V_{DRM}, \ T_J = 25 \ ^{\circ}C$		dV/dt _{cr}	500			V/µs		
Critical rate of rise of voltage at current commutation			dV/dt _{crq}		0.13		V/µs		
COUPLER									
	V _D = 3 V	VOM160N	I _{FT}			5	mA		
LED trigger current, current required to latch output		VOM160P	I _{FT}			7	mA		
Cancer required to later carput		VOM160R	I _{FT}			10	mA		
Capacitance (input - output)	f = 1 MHz, V _{IO} = 0 V		C _{IO}		0.8		pF		
Peak off-state voltage	I _C = 100 μA		V_{DRM}	600			V		
Holding current			I _{hold}		0.3		mA		

Note

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.

SAFETY AND INSULATION RATINGS							
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification (according to IEC 68 part 1)			40/100/21				
Pollution degree (DIN VDE 0109)			2				
Comparative tracking index	CTI	175		399			
Peak transient overvoltage	V _{IOTM}			6000	V _{peak}		
Peak insulation voltage	V _{IORM}			707	V _{peak}		
Isolation resistance at T _{amb} = 100 °C, V _{DC} = 500 V	R _{IO}	10 ¹¹			Ω		
Isolation resistance at T _{amb} = 25 °C, V _{DC} = 500 V	R _{IO}	10 ¹²			Ω		
Safety rating - power rating	P _{SO}			400	mW		
Safety rating - input current	I _{SI}			150	mA		
Safety rating - temperature	T _{SI}			165	°C		
Creepage distance		5			mm		
Clearance distance		5			mm		
Insulation thickness		0.4			mm		



TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

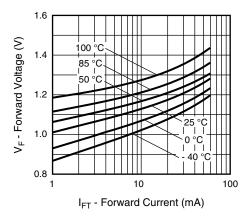


Fig. 2 - Forward Current vs. Forward Voltage

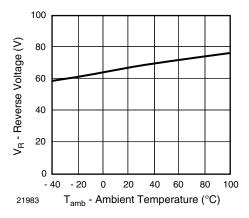


Fig. 3 - Reverse Voltage vs. Ambient Temperature

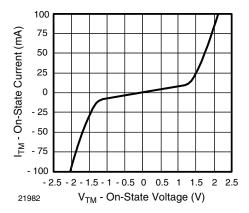


Fig. 4 - On-State Current vs. On-State Voltage

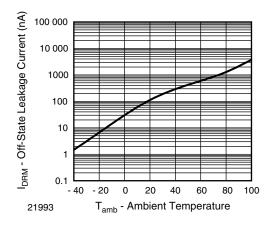


Fig. 5 - Off-State Leakage Current vs. Ambient Temperature

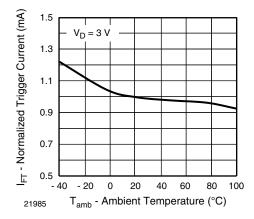


Fig. 6 - Normalized Trigger Current vs. Ambient Temperature

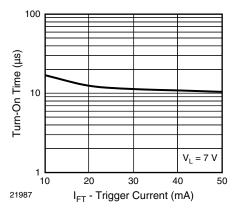


Fig. 7 - Trigger Current vs. Turn-On Time

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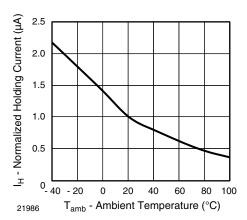


Fig. 8 - Normalized Holding Current vs. Ambient Temperature

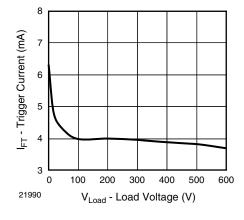


Fig. 11 - Trigger Current vs. Load Voltage

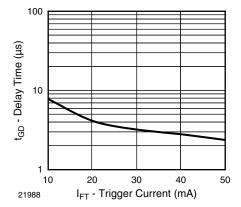


Fig. 9 - Trigger Current vs. Delay Time

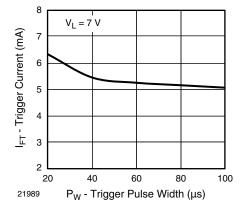
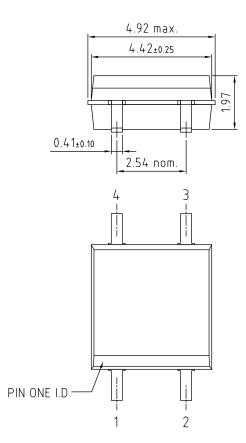
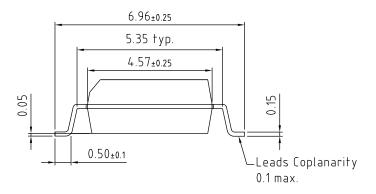


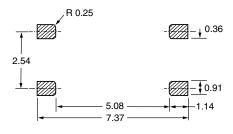
Fig. 10 - Trigger Current vs. Trigger Pulse Width

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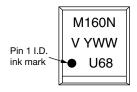
PACKAGE DIMENSIONS in millimeters







PACKAGE MARKING (example)



TAPE AND REEL PACKAGING

Dimensions in millimeters

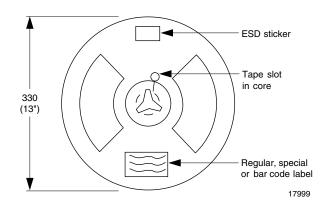


Fig. 12 - Tape and Reel Shipping Medium, 2000 units per reel

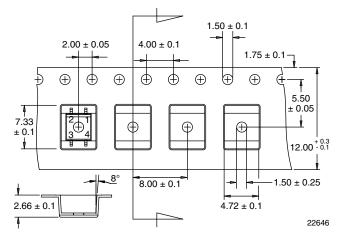


Fig. 13 - Tape Dimensions

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