



# **Surface Mount Transformers/Inductors, Gapped and Ungapped, Custom Configurations Available**



### **FEATURES**

• Compliant to RoHS directive 2002/95/EC

### **ELECTRICAL SPECIFICATIONS**

(Multiple winds are connected in parallel)

Inductance Range: 10 µH to 150 000 µH, measured RoHS at 0.10 V<sub>RMS</sub> at 10 kHz without DC current, using an <sup>COMPLIANT</sup> HP 4263A or HP 4284A impedance analyzer

**DC Resistance Range:** 0.02  $\Omega$  to 46.2  $\Omega$ , measured at + 25 °C ± 5 °C

Rated Current Range: 3.20 A to 0.17 A

Dielectric Withstanding Voltage: 500 V<sub>RMS</sub>, 60 Hz, 5 s

STANDARD ELECTRICAL SPECIFICATIONS						
	IND.	IND.	SCHEMATIC	DCR MAX.	MAX. RATED DC CURRENT	SATURATING CURRENT
MODEL	(µH)	TOL.	LETTER	<b>(</b> Ω <b>)</b>	(A) <sup>(1)</sup>	(A) <sup>(2)</sup>
LPE6855ER151NU	150	± 30 %	Α	0.28	0.84	N/A
LPE6855ER221NU	220	± 30 %	Α	0.34	0.76	N/A
LPE6855ER331NU	330	± 30 %	Α	0.41	0.69	N/A
LPE6855ER471NU	470	± 30 %	Α	0.49	0.63	N/A
LPE6855ER681NU	680	± 30 %	Α	0.59	0.57	N/A N/A
LPE6855ER102NU	1000	± 30 %	Α	0.72	0.52	N/A
LPE6855ER152NU	1500	± 30 %	Α	0.88	0.47	N/A
LPE6855ER222NU	2200	± 30 %	Α	1.07	0.43	N/A
LPE6855ER332NU	3300	± 30 %	Α	1.31	0.39	N/A
LPE6855ER472NU	4700	± 30 %	Α	1.56	0.35	N/A N/A N/A N/A N/A N/A N/A N/A N/A
LPE6855ER682NU	6800	± 30 %	Α	1.88	0.32	N/A
LPE6855ER103NU	10 000	± 30 %	Α	7.17	0.16	N/A
LPE6855ER153NU	15 000	± 30 %	Α	8.78	0.15	N/A
LPE6855ER223NU	22 000	± 30 %	Α	10.6	0.14	N/A
LPE6855ER333NU	33 000	± 30 %	Α	13.0	0.12	N/A
LPE6855ER473NU	47 000	± 30 %	Α	15.5	0.11	IN/A
LPE6855ER683NU	68 000	± 30 %	Α	18.7	0.10	N/A
LPE6855ER104NU	100 000	± 30 %	Α	37.7	0.07	N/A
LPE6855ER154NU	150 000	± 30 %	Α	46.2	0.06	N/A
LPE6855ER100MG	10	± 20 %	В	0.02	3.21	3.375
LPE6855ER150MG	15	± 20 %	B B	0.03	2.90	2.790
LPE6855ER220MG	22	± 20 %	В	0.04	2.64	2.325
LPE6855ER330MG	33	± 20 %	В	0.05	2.12	1.910 1.610
LPE6855ER470MG	47	± 20 %	B B B	0.08	1.73	
LPE6855ER680MG	68	± 20 %		0.12	1.41	1.350
LPE6855ER101MG	100	± 20 %	В	0.15	1.28	1.350 1.120 0.915 0.757
LPE6855ER151MG	150	± 20 %	С	0.23	1.02	0.915
LPE6855ER221MG	220	± 20 %	D	0.35	0.83	0.757
LPE6855ER331MG	330	± 20 %	D	0.55	0.67	0.620
LPE6855ER471MG	470	± 20 %	D E	0.82	0.54	0.520
LPE6855ER681MG	680	± 20 %		1.23	0.45	0.433
LPE6855ER102MG	1000	± 20 %	E	1.89	0.36	0.620 0.520 0.433 0.358 0.292
LPE6855ER152MG	1500	± 20 %	E	2.90	0.29	0.202
LPE6855ER222MG	2200	± 20 %	E	4.50	0.23	0.242
LPE6855ER332MG	3300	± 20 %	E E E	5.50	0.21	0.197
LPE6855ER472MG	4700	± 20 %	E	8.30	0.17	0.166

(1) DC current that will create a maximum temperature rise of 30 °C when applied at + 25 °C ambient.
(2) DC current that will typically reduce the initial inductance by 20 %.

6

8

SIZE

5

5

UNGAPPED MODELS: Highest possible inductance with the lowest DCR and highest Q capability. Beneficial in filter, impedance matching and line coupling devices.

GAPPED MODELS: Capable of handling large amounts of DC current, tighter inductance tolerance with better temperature stability than ungapped models. Beneficial in DC/DC converters or other circuits carrying DC currents or requiring inductance stability over a temperature range.

DESCRIPTION						
LPE	6855	1000 μH	± 30 %	Α	ER	e2
MODEL	SIZE	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	CORE	PACKAGE CODE	JEDEC LEAD (Pb)-FREE STANDARD
GLOBAL PART NUMBER						

Ε

PACKAGE CODE

R

1

0

INDUCTANCE VALUE

2

Ν

TOL

#### Note

Series is also available with SnPb terminations by using package code RY for tape and reel (in place of ER) or SM for bulk (in place of EB).

Ρ

PRODUCT FAMILY

Ε

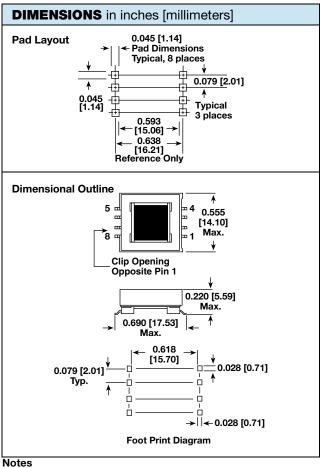
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CORE

## Vishay Dale

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- Pad layout guidelines per MIL-STD-275E (printed wiring for electronic equipment).
- Tolerances:  $xx \pm 0.01$ " [ $\pm 0.25$  mm];  $xxx \pm 0.005$ " [ $\pm 0.12$  mm].

SCHEMATIC (top view)					
Schematic A	Schematic B		Schematic C		
5 0 0 4	5 @	<u>~~</u> °₀ 4	5 0 0 4		
6 0	6 9	~~~°₀ 3	6 0		
7 0	7 9	~~~° <sub>0</sub> 2	7 0		
80 "101	8 @	~~°₀ 1	8 9		
Schematic	D	Schematic E			
5	© <b>4</b>	5 ⊖	<b>0 4</b>		
6 ⊕	<b>9 3</b>	6 0	⊕ 3		
7 9—777777		7 0	° 2		
8 @	<b>℃</b> 1	8 6—	° 1		

Schematic A is for ungapped LPE series

ENVIRONMENTAL PERFORMANCE			
TEST	CONDITIONS		
Thermal Cycling	Withstands - 55 °C to + 125 °C		
Operating Temperature	- 55 °C to + 125 °C <sup>(1)</sup>		
High Humidity	85 %		
Soldering Heat	Tested to + 230 °C		
Mechanical Shock	Per MIL-STD-202, method 213 (100G)		
Vibration	Per MIL-STD-202, method 204 (20G)		
Solderability	Per industry standards		

#### Note

(1) Must be checked in end use application

#### **PART MARKING**

- Vishay Dale
- Date code
- Marking code (suffix of model #)
- Pin 1 indicator

### **PACKAGING**

### **TAPE SPECIFICATIONS:**

Carrier Tape Type: Conductive Cover Tape Type: Anti-static

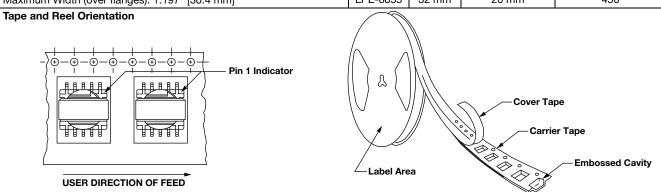
Cover Tape Adhesion to Carrier: 40 g ± 30 g

### **REEL SPECIFICATIONS:**

Diameter (flange): 13" [330.2 mm]
Maximum Width (over flanges): 1.197" [30.4 mm]

STANDARDS: All embossed carrier tape packaging will be accomplished in compliance with latest revision of EIA-481 Surface ' Mount Components for Automatic "Taping Placement'

MODEL	TAPE WIDTH	COMPONENT PITCH	UNITS PER 13" REEL	
LPE-6855	32 mm	20 mm	450	



Top view shown with cover tape removed

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