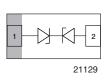


Bidirectional Symmetrical (BiSy) Single Line ESD-Protection Diode in LLP0603-2L





MARKING (example only)

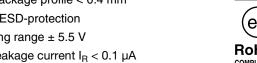


Bar = pin 1 marking

Y = type code (see table below)

FEATURES

- Ultra compact LLP0603-2L package
- Low package profile < 0.4 mm
- 1-line ESD-protection
- Working range ± 5.5 V
- Low leakage current I_R < 0.1 μA
- Low load capacitance C_D = 14 pF
- ESD-protection acc. IEC 61000-4-2 ± 30 kV contact discharge
 - ± 30 kV air discharge
- Pin plating NiPdAu (e4) no whisker growth • e4 - precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





- X = date code

ADDITIONAL RESOURCES



ORDERING INFORMATION					
DEVICE NAME	ICE NAME ORDERING CODE		MINIMUM ORDER QUANTITY		
VCUT05F1-HD0	VCUT05F1-HD0-G4-08	15k	15k		

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VCUT05F1-HD0	LLP0603-2L	Α	0.22 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C

ABSOLUTE MAXIMUM RATINGS VCUT05F1-HD0						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5; t _p = 8/20 μs; single shot	I _{PPM}	4	А		
Peak pulse power	Pin 1 to pin 2 acc. IEC 61000-4-5; $t_p = 8/20 \mu s$; single shot	P _{PP}	60	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses		± 30	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	± 30	kV		
Operating temperature	Junction temperature	TJ	-40 to +125	°C		
Storage temperature		T _{stg}	-55 to +150	°C		

ELECTRICAL CHARACTERISTICS VCUT05F1-HD0 (pin 1 to pin 2 or pin 2 to pin1) (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N _{channel}	-	-	1	lines	
Reverse stand-off voltage	Max. reverse working voltage	V_{RWM}	-	-	5.5	V	
Reverse voltage	at I _R = 0.1 μA	V _R	5.5	-	-	V	
Reverse current	at V _{RWM} = 5.5 V	I _R	-	-	0.1	μA	
Reverse breakdown voltage	at I _R = 1 mA	V _{BR}	7	-	9	V	
Reverse clamping voltage	at I _{PP} = 1 A		-	9	12	V	
	at I _{PP} = I _{PPM} = 4 A	V _C	-	10.8	14	V	
Capacitance	at V _R = 0 V; f = 1 MHz		-	14	16	pF	
	at V _R = 2.5 V; f = 1 MHz	- C _D	-	11	-	pF	

CUT THE SPIKES WITH VCUT05F1-HD0:

The VCUT05F1-HD0 is a bidirectional and symmetrical (BiSy) ESD-protection device which clamps positive and negative overvoltage transients to ground. Connected between the signal or data line and the ground the VCUT05F1-HD0 offers a high isolation (low leakage current, low capacitance) within the specified working range. Due to the short leads and small package size of the tiny LLP0603-2L package the line inductance is very low, so that fast transients like an ESD-strike can be clamped with minimal over- or undershoots.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

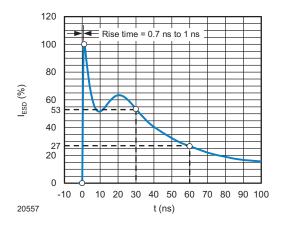


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 Ω /150 pF)

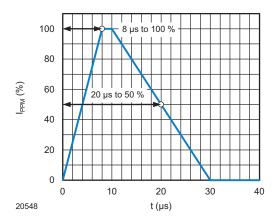


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

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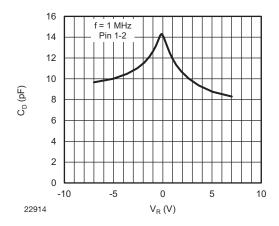


Fig. 3 - Typical Capacitance vs. Reverse Voltage

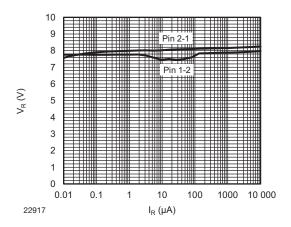


Fig. 6 - Typical Reverse Voltage vs. Reverse Current

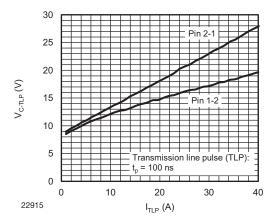


Fig. 4 - Typical Camping Voltage vs. Peak Pulse Current

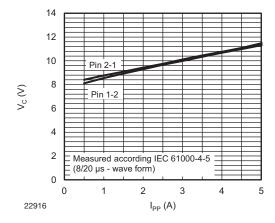
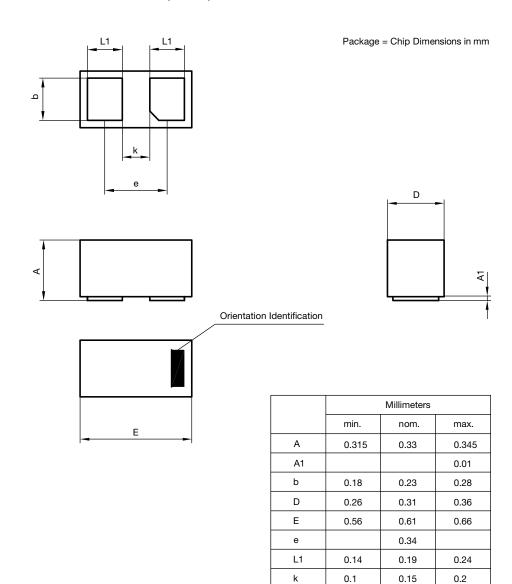


Fig. 5 - Typical Peak Clamping Voltage vs. Peak Pulse Current

PACKAGE DIMENSIONS in millimeters (inches): LLP0603-2L



foot print recommendation:

0.15

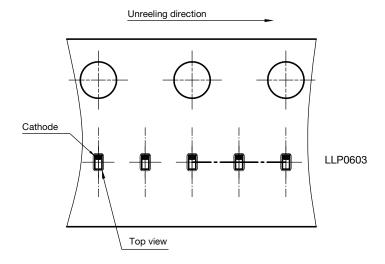
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22918

ORIENTATION IN CARRIER TAPE: LLP0603



S8-V-3906.04-22 (4) Created Date: 04.02.2010

22919

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