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(5-2008)



Vishay Semiconductors

High Intensity SMD LED PLCC-2



DESCRIPTION

The package of the VLMS31.. is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

PRODUCT GROUP AND PACKAGE DATA

Product group: LED
Package: SMD PLCC-2
Product series: standard
Angle of half intensity: ± 60°

FEATURES

- SMD LED with exceptional brightness
- · Luminous intensity categorized
- Compatible with automatic placement equipment
- EIA and ICE standard package
- Compatible with IR reflow, vapor phase and wave solder processes according to CECC 00802 and J-STD-020
- Available in 8 mm tape
- Low profile package
- Non-diffused lens: excellent for coupling to light pipes and backlighting
- Low power consumption
- Luminous intensity ratio in one packaging unit $I_{Vmax.}/I_{Vmin.} \le 1.6$
- Preconditioning according to JEDEC® level 2a
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Automotive: backlighting in dashboards and switches
- Telecommunication: indicator and backlighting in telephone and fax
- Indicator and backlight for audio and video equipment
- · Indicator and backlight in office equipment
- Flat backlight for LCDs, switches, and symbols
- General use

PARTS TABLE														
PART	COLOR	LUMINOUS INTENSITY (mcd)		at I _F	WAVELENGTH (nm)		at I _F (mA)	FORWARD VOLTAGE (V)		at I _F (mA)	TECHNOLOGY			
		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.	-	MIN.	TYP.	MAX.		
VLMS31J1K2-GS08	Red	4.5	7.8	11.2	10	624	630	638	10	-	1.9	2.6	20	GaAsP on GaP
VLMS31J1K2-GS18	Red	4.5	7.8	11.2	10	624	630	638	10	-	1.9	2.6	20	GaAsP on GaP
VLMS31J1L2-GS08	Red	4.5	7.8	18	10	624	630	638	10	-	1.9	2.6	20	GaAsP on GaP
VLMS31J1L2-GS18	Red	4.5	7.8	18	10	624	630	638	10	-	1.9	2.6	20	GaAsP on GaP
VLMS31J2L1-GS08	Red	5.6	8	14	10	624	630	638	10	-	1.9	2.6	20	GaAsP on GaP
VLMS31J2L1-GS18	Red	5.6	8	14	10	624	630	638	10	-	1.9	2.6	20	GaAsP on GaP
VLMS31K1L2-GS08	Red	7.1	8.5	18	10	624	630	638	10	-	1.9	2.6	20	GaAsP on GaP
VLMS31K1L2-GS18	Red	7.1	8.5	18	10	624	630	638	10	-	1.9	2.6	20	GaAsP on GaP



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C unless otherwise specified) VLMS31					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage 1)		V_{R}	6	V	
DC forward current	T _{amb} ≤ 85 °C	I _F	30	mA	
Surge forward current	t _p ≤ 10 μs	I _{FSM}	0.1	А	
Power dissipation		P_V	80	mW	
Junction temperature		Tj	100	°C	
Operating temperature range		T _{amb}	-40 to +100	°C	
Storage temperature range		T _{stg}	-40 to +100	°C	
Thermal resistance junction / ambient	Mounted on PC board (pad size > 16 mm ²)	R_{thJA}	400	K/W	

Note

⁽¹⁾ Driving LED in reverse direction is suitable for short term application

OPTICAL AND ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C unless otherwise specified) VLMS31, RED							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
		VLMS31J1K2	Ι _V	4.5	7.8	11.2	mcd
Luminous intensity 1)	1 10 4	VLMS31J1L2	Ι _V	4.5	7.8	18	mcd
	I _F = 10 mA	VLMS31J2L1	Ι _V	5.6	8	14	mcd
		VLMS31K1L2	Ι _V	7.1	8.5	18	mcd
Dominant wavelength	I _F = 10 mA		λ_{d}	624	630	638	nm
Peak wavelength	I _F = 10 mA		λ_{p}	-	643	-	nm
Angle of half intensity	I _F = 10 mA		φ	-	± 60	-	deg
Forward voltage	I _F = 20 mA		V_{F}	-	1.9	2.6	V
Reverse voltage	I _R = 10 μA		V_R	5	-	-	V
Junction capacitance	V _R = 0, f = 1 MHz		Cj	-	15	-	pF

Note

 $^{^{(1)}}$ In one packing unit $I_{Vmax.}/I_{Vmin.} \leq 1.6$

LUMINOUS INTENSITY CLASSIFICATION					
GROUP	LIGHT INTENSITY (mcd)				
STANDARD	OPTIONAL	MIN.	MAX.		
	1	4.5	5.6		
J	2	5.6	7.1		
К	1	7.1	9.0		
	2	9.0	11.2		
	1	11.2	14		
_	2	14	18		

9.0	11.2	
11.2	14	
14	18	

Note

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of \pm 11 %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).

In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups will not be orderable.

CROSSING TABLE					
VISHAY	OSRAM				
VLMS31J1K2	LST670-J1K2				
VLMS31J1L2	LST670-J1L2				
VLMS31J2L1	LST670-J2L1				
VLMS31K1L2	LST670-K1L2				

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TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

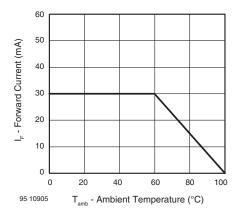


Fig. 1 - Forward Current vs. Ambient Temperature

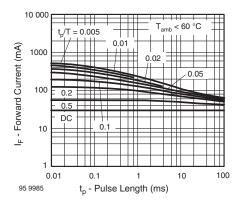


Fig. 2 - Forward Current vs. Pulse Length

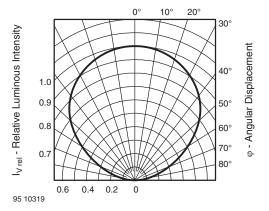


Fig. 3 - Relative Luminous Intensity vs. Angular Displacement

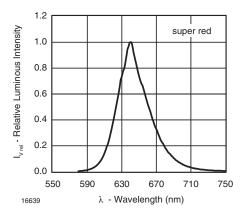


Fig. 4 - Relative Luminous Intensity vs. Wavelength

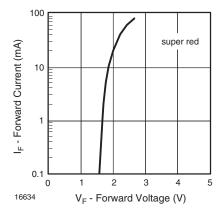


Fig. 5 - Forward Current vs. Forward Voltage

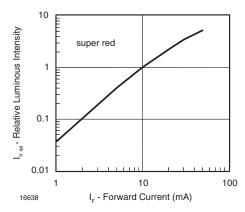


Fig. 6 - Relative Luminous Intensity vs. Forward Current

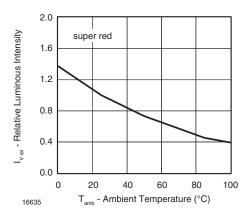


Fig. 7 - Relative Luminous Intensity vs. Ambient Temperature

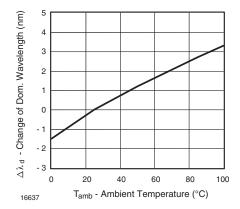


Fig. 8 - Change of Dominant Wavelength vs. Ambient Temperature

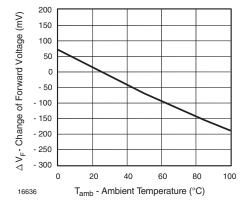
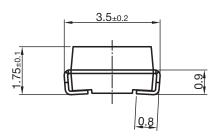


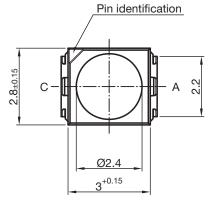
Fig. 9 - Change of Forward Voltage vs. Ambient Temperature



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PACKAGE DIMENSIONS in millimeters



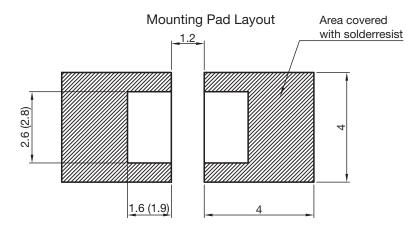




Dimensions in mm

Drawing-No.: 6.541-5067.01-4

Issue: 6; 23.09.13



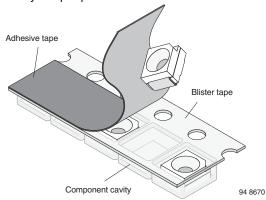
Dimensions: Reflow and vapor phase (wave soldering)



METHOD OF TAPING / POLARITY AND TAPE AND REEL

SMD LED (VLM3-SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



TAPING OF VLM.3..

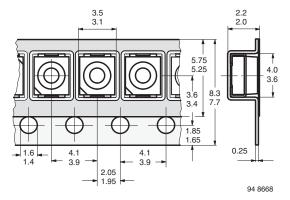


Fig. 10 - Tape Dimensions in mm for PLCC-2

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

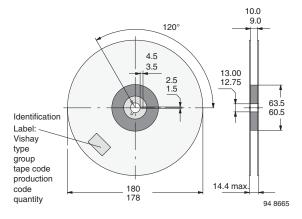


Fig. 11 - Reel Dimensions - GS08

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED

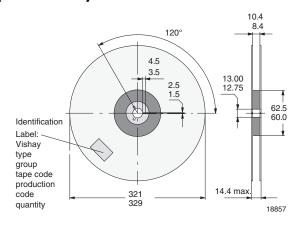


Fig. 12 - Reel Dimensions - GS18

SOLDERING PROFILE

IR Reflow Soldering Profile for Lead (Pb)-free Soldering Preconditioning acc. to JEDEC level 2a

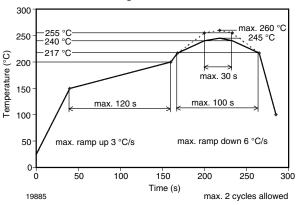


Fig. 13 - Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

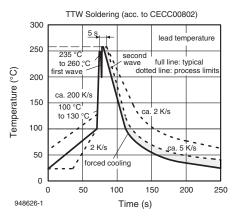
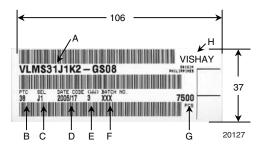


Fig. 14 - Double Wave Soldering of Opto Devices (all Packages)



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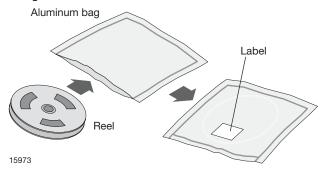
BAR CODE PRODUCT LEVEL



- A) Type of component
- B) Manufacturing plant
- C) SEL selection code (bin):e.g.: J1 = code for luminous intensity group
- D) Date code year / week
- E) Day code (e.g. 3: Wednesday)
- F) Batch no.
- G) Total quantity
- H) Company code

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminium bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

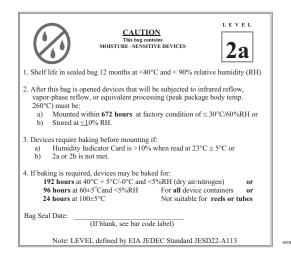
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

Legal Disclaimer Notice



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