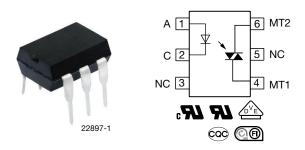


www.vishay.com

Vishay Semiconductors

Optocoupler, Phototriac Output, Non-Zero Crossing, 250 V_{DRM}



FEATURES

- 250 V blocking voltage
- · Wide range of trigger current
- 100 mA_{RMS} on-state current
- Wide temperature range -55 °C to +100 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912







LINKS TO ADDITIONAL RESOURCES













DESCRIPTION

The K301xP series consists of a phototriac optically coupled to a gallium arsenide infrared-emitting diode in a 6-lead plastic dual inline package.

The non-zero crossing functionality enables full wave control. Featuring galvanic and electrical noise isolation, the output is able to directly switch AC loads or drive medium to high power TRIACs.

APPLICATIONS

- Power TRIAC driver
- · Isolated AC load switch
- Air condition
- Heaters
- White goods
- Industrial controls
- Office equipment

AGENCY APPROVALS

- UL
- cUL
- DIN EN 60747-5-5 (VDE 0884-5)
- CQC: GB4943-1-2011
- CQC: GB8898-2011
- FIMKO

ORDERING INFORMATION					
K 3 PART NUMBE	0 1 X P R TRIGGER CURRENT IFT	7.62 mm			
AGENCY CERTIFIED / PACKAGE	TRIGGER CURRENT, I _{FT}				
VDE, cUL, CQC, FIMKO	5 mA 15 mA				
DIP-6	K3012P K3010P				

Note

• Additional options may be possible, please contact sales office



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
Reverse voltage		V_{R}	5	V	
Forward current		I _F	80	mA	
Forward surge current	t _p ≤ 10 µs	I _{FSM}	3	Α	
Power dissipation		P _{diss}	100	mW	
Junction temperature		Tj	125	°C	
OUTPUT					
Off state output terminal voltage		V_{DRM}	250	V	
On state RMS current		I _{TRM}	100	mA	
Peak surge current, non-repetitive	$t_p \le 10 \text{ ms}$	I _{TMS}	1.5	Α	
Power dissipation		P _{diss}	300	mW	
Junction temperature		Tj	125	°C	
COUPLER					
Total power dissipation		P _{tot}	350	mW	
Storage temperature range		T _{stg}	-55 to +150	°C	
Ambient temperature range		T _{amb}	-55 to +100	°C	
Soldering temperature	2 mm from case, $t \le 10 \text{ s}$	T _{sld}	260	°C	

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	$I_F = 50 \text{ mA}$		V_{F}	-	1.25	1.6	V
Junction capacitance	V _R = 0, f = 1 MHz		C _j	-	50	-	pF
OUTPUT							
Forward peak off-state voltage (repetitive)	I _{RDM} = 100 nA		V _{DRM} ⁽¹⁾	250	-	-	V
Peak on-state voltage	I _{TM} = 100 mA		V_{TM}	-	1.5	3	V
Critical rate of rise of off-state voltage	I _{FT} = 0, I _{FT} = 30 mA		dV/dt _{cr}	-	10	-	V/µs
			dV/dt _{crq}	0.1	0.2	-	V/µs
COUPLER (2)							
Collector emitter trigger current	$V_{S} = 3 \text{ V}, R_{L} = 150 \Omega$	K3010P	I _{FT}	-	8	15	mA
		K3012P	I _{FT}	-	2	5	mA
Holding current	$I_F = 10 \text{ mA}, V_S \ge 3 \text{ V}$		I _H	-	100	-	μΑ

Notes

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements
- (1) Test voltage must be applied within dV/dt ratings
- (2) IFT is defined as a minimum trigger current



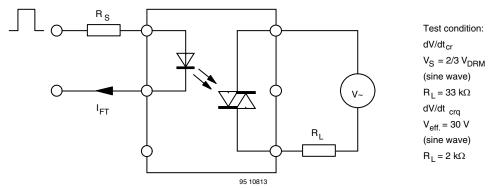
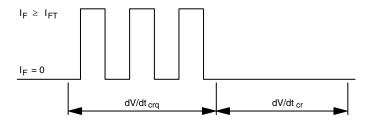


Fig. 1 - Test Circuit for dV/dt_{cr} and dV/dt_{crq}



 dV/dt cr

Highest value of the "rate of rise of off-state voltage" which does not cause any switching from the off state to the on state

dV/dt crq

Highest value of the "rate of rise of communicating voltage" which does not switch on the device again, after the voltage has decreased to zero and the trigger current is switched from I_{FT} to zero

95 10814

Fig. 2

SAFETY AND INSULATION RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Climatic classification	According to IEC 68 part 1		55 / 100 / 21				
Pollution degree	According to DIN VDE 0109		2				
Comparative tracking index	Insulation group IIIa	CTI	175				
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V _{ISO}	4420	V _{RMS}			
Tested withstanding isolation voltage	According to UL1577, t = 1 s	V _{ISO}	5300	V _{RMS}			
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V _{IOTM}	8000	V _{peak}			
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V _{IORM}	890	V _{peak}			
Isolation resistance	T _{amb} = 25 °C, V _{IO} = 500 V	R _{IO}	≥ 10 ¹²	Ω			
	$T_{amb} = 100 ^{\circ}\text{C}, V_{IO} = 500 \text{V}$	R _{IO}	≥ 10 ¹¹	Ω			
Output safety power		P _{SO}	265	mW			
Input safety current		I _{SI}	130	mA			
Input safety temperature		T _S	150	°C			
Creepage distance	DID C		≥ 7	mm			
Clearance distance	DIP-6		≥ 7	mm			
Insulation thickness		DTI	≥ 0.4	mm			
Input to output test voltage, method A	V_{IORM} x 1.6 = V_{PR} , 100 % sample test with t_M = 10 s, partial discharge < 5 pC	V _{PR}	1424	V _{peak}			

Note

According to DIN EN60747-5-5 (see figure 4). This optocoupler is suitable for safe electrical isolation only within the safety ratings.
 Compliance with the safety ratings shall be ensured by means of suitable protective circuits

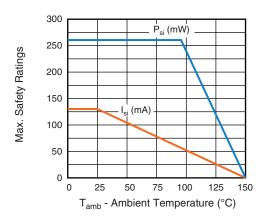


Fig. 3 - Safety Parameter Derating Diagram

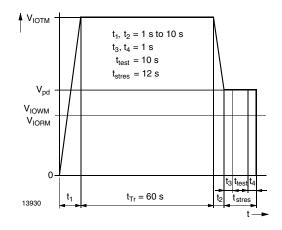


Fig. 4 - Test Pulse Diagram for Sample Test according to DIN EN60747-5-5 / DIN EN60747-; IEC 60747

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

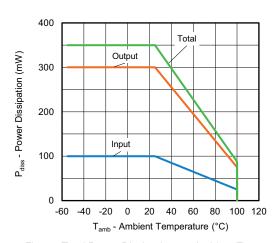


Fig. 5 - Total Power Dissipation vs. Ambient Temperature

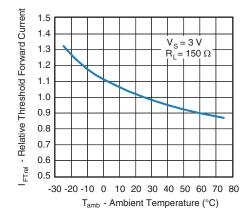


Fig. 7 - Relative Threshold Forward Current vs.
Ambient Temperature

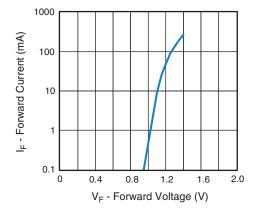


Fig. 6 - Forward Current vs. Forward Voltage

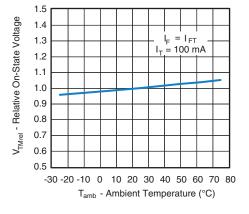


Fig. 8 - Relative On-State vs. Ambient Temperature



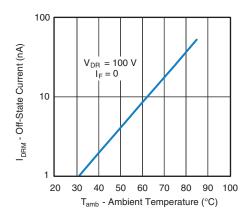


Fig. 9 - Off-State Current vs. Ambient Temperature

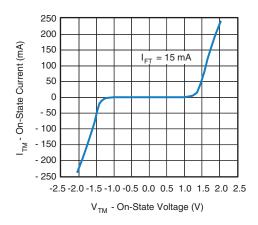
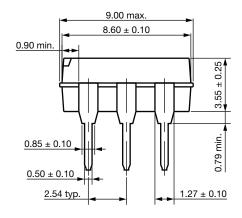
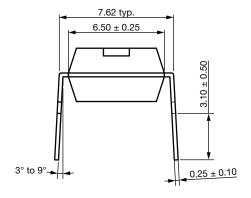


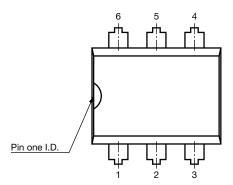
Fig. 10 - On-State Current vs. On-State Voltage

PACKAGE DIMENSIONS (in millimeters)

DIP-6







PACKAGE MARKING



Fig. 11 - Example of K3010P

Notes

- "YWW" is the date code marking (Y = year code, WW = week code)
- The VDE logo is only marked on option1 parts

PACKING INFORMATION (in millimeters)

Tube

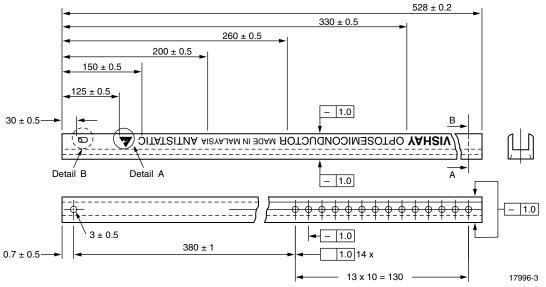


Fig. 12 - Shipping Tube Specifications for DIP Packages

DEVICES PER TUBES					
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX		
DIP-6	50	40	2000		

DIP-6

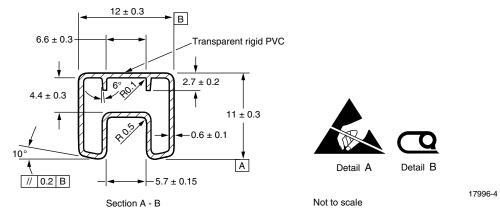


Fig. 13 - Tube Shipping Medium



SOLDER PROFILES

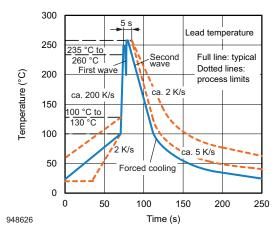


Fig. 14 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices

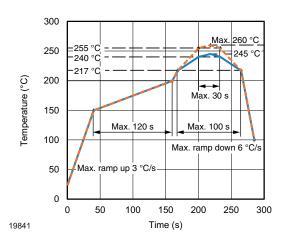


Fig. 15 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions: T_{amb} < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020

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