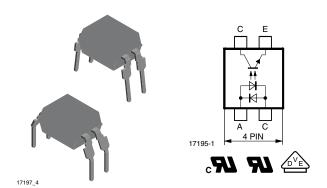


Vishay Semiconductors

Optocoupler, Phototransistor Output, AC Input



DESCRIPTION

The TCET1600, TCET1600G consists of a phototransistor optically coupled to 2 gallium arsenide infrared-emitting diodes in a single (4 pin) package.

VDE STANDARDS

These couplers perform safety functions according to the following equipment standards:

- DIN EN 60747-5-2 (VDE 0884)
 Optocoupler for electrical safety requirements
- IEC 60950/EN 60950
 Office machines (applied for reinforced isolation for mains voltage ≤ 400 V_{RMS})
- VDE 0804

Telecommunication apparatus and data processing

• IEC 60065

Safety for mains-operated electronic and related household apparatus

- VDE 0700/IEC 335
 - Household equipment
- VDE 0160

Electronic equipment for electrical power installation

 VDE 0750/IEC 60601 Medical equipment

FEATURES

- Isolation materials according to UL94 V-O
- Pollution degree 2 (DIN/VDE 0110 /resp. IEC 60664)



Climatic classification 55/100/21 (IEC 60068 part 1)

RoHS

- Special construction: therefore, extra low coupling capacity of typical 0.2 pF, high common mode rejection
- Low temperature coefficient of CTR
- Rated impulse voltage (transient overvoltage)
 V_{IOTM} = 10 kV peak
- Isolation test voltage (partial discharge test voltage) $V_{pd} = 1.6 \; kV \; peak$
- Rated isolation voltage (RMS includes DC) $V_{IOWM} = 600 V_{RMS}$
- Rated recurring peak voltage (repetitive) V_{IORM} = 890 V_{peak}
- Thickness though insulation ≥ 0.75 mm
- Creepage current resistance according to VDE 0303/ IEC 60112 comparative tracking index: CTI ≥ 175
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

AGENCY APPROVALS

- UL1577, file no. E52744 system code H, double protection
- CSA 22.2 bulletin 5A
- DIN EN 60747-5-2 (VDE 0884)
- DIN EN 60747-5-5 (pending)
- FIMKO

ORDERING INFORMATION			
T C E T 1 6	0 0 # DIP DIP, 400 mil		
PART NUMBER	7.62 mm 10.16 mm		
AGENCY CERTIFIED/PACKAGE	CTR (%)		
AGENOT GENTITIED/FAGNAGE	± 5 mA		
UL, cUL, VDE, FIMKO	20 to 300		
DIP-4, single channel	TCET1600		
DIP-4, single channel, 400 mil	TCET1600G		

Note

• G = leadform 10.16 mm; G is not marked on the body.

TCET1600, TCET1600G



Vishay Semiconductors Optocoupler, Phototransistor Output, AC Input

ABSOLUTE MAXIMUM RATING	S (1) $(T_{amb} = 25 ^{\circ}C, \text{ unless oth})$	erwise specified)	
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V _R	6	V
Forward current		I _F	± 60	mA
Forward surge current	t _p ≤ 10 μs	I _{FSM}	± 1.5	Α
Power dissipation		P _{diss}	100	mW
Junction temperature		Tj	125	°C
OUTPUT				
Collector emitter voltage		V _{CEO}	70	V
Emitter collector voltage		V _{ECO}	7	V
Collector current		I _C	50	mA
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA
Power dissipation		P _{diss}	150	mW
Junction temperature		Tj	125	°C
COUPLER				
Isolation test voltage (RMS)	t = 1 s	V _{ISO}	5300	V _{RMS}
Isolation voltage		V _{IORM}	890	V _P
Total power dissipation		P _{tot}	250	mW
Operating ambient temperature range		T _{amb}	- 55 to + 100	°C
Storage temperature range		T _{stg}	- 55 to + 150	°C
Soldering temperature (2)	2 mm from case, t ≤ 10 s	T _{sld}	260	°C

Notes

⁽²⁾ Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTCS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT								
Forward voltage	$I_F = \pm 50 \text{ mA}$	V_{F}		1.25	1.6	V		
Junction capacitance	V _R = 0 V, f = 1 MHz	C _j		50		pF		
OUTPUT								
Collector emitter voltage	I _C = 100 μA	V_{CEO}	70			V		
Emitter collector voltage	I _E = 100 μA	V _{ECO}	7			V		
Collector dark current	$V_{CE} = 20 \text{ V}, I_F = 0, E = 0$	I _{CEO}			100	nA		
COUPLER								
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V _{CEsat}			0.3	V		
Cut-off frequency	V_{CE} = 5 V, I_F = 10 mA, R_L = 100 Ω	f _c		100		kHz		
Coupling capacitance	f = 1 MHz	C _k		0.3		pF		

Note

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.

⁽¹⁾ Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.



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CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
I _C /I _F	$V_{CE} = 5 \text{ V}, I_F = \pm 5 \text{ mA}$	CTR	20		300	%	

MAXIMUM SAFETY RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward current		I _F			275	mA	
OUTPUT							
Power dissipation		P _{diss}			400	mW	
COUPLER							
Rated impulse voltage		V _{IOTM}			10	kV	
Safety temperature		T _{si}			175	°C	

Note

 According to DIN EN 60747-5-2 (VDE 0884) (see figure 1). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

INSULATION RATED PARAMETERS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Partial discharge test voltage - routine test	100 %, t _{test} = 1 s	V_{pd}	1.669			kV	
Partial discharge test voltage - lot test (sample test)	$t_{Tr} = 60 \text{ s}, t_{test} = 10 \text{ s},$ (see figure 2)	V _{IOTM}	10			kV	
		V_{pd}	1.424			kV	
	V _{IO} = 500 V	R _{IO}	10 ¹²			Ω	
$V_{IO} = 500^{\circ}$	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	10 ¹¹			Ω	
	V _{IO} = 500 V, T _{amb} = 150 °C (construction test only)	R _{IO}	10 ⁹			Ω	

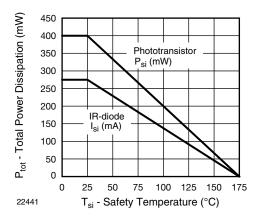


Fig. 1 - Derating Diagram

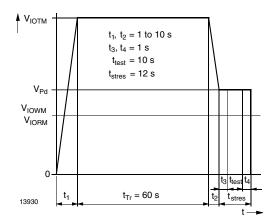


Fig. 2 - Test Pulse Diagram for Sample Test acc. to DIN EN 60747-5-2 (VDE 0884); IEC60747-5-5

TCET1600, TCET1600G



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SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Delay time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$, (see figure 3)	t _d		3		μs	
Rise time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$, (see figure 3)	t _r		3		μs	
Turn-on time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$, (see figure 3)	t _{on}		6		μs	
Storage time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$, (see figure 3)	ts		0.3		μs	
Fall time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$, (see figure 3)	t _f		4.7		μs	
Turn-off time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$, (see figure 3)	t _{off}		5		μs	
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega$, (see figure 4)	t _{on}		9		μs	
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega$, (see figure 4)	t _{off}		10		μs	

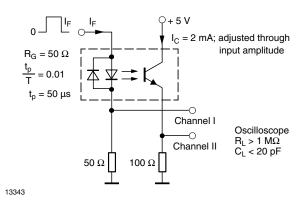


Fig. 3 - Test Circuit, Non-Saturated Operation

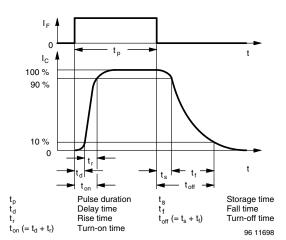


Fig. 5 - Switching Times

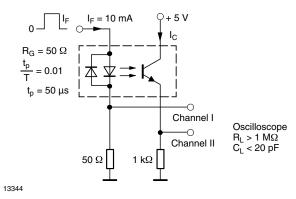


Fig. 4 - Test Circuit, Saturated Operation



Optocoupler, Phototransistor Output, Vishay Semiconductors AC Input

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

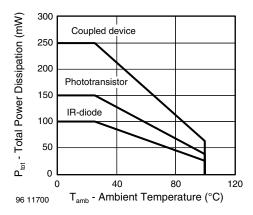


Fig. 6 - Total Power Dissipation vs. Ambient Temperature

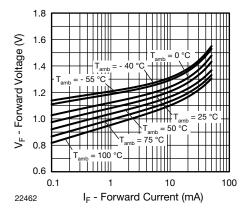


Fig. 7 - Forward Current vs. Forward Voltage

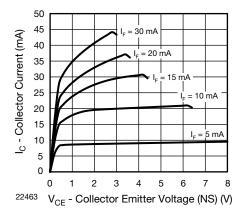


Fig. 8 - Collector Current vs. Collector Emitter Voltage (NS)

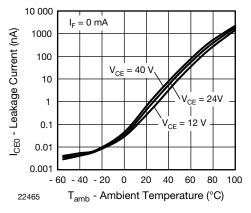


Fig. 9 - Leakage Current vs. Ambient Temperature

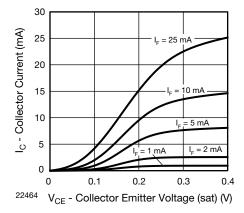


Fig. 10 - Collector Current vs. Collector Emitter Voltage (sat)

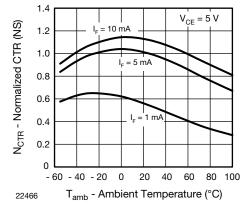


Fig. 11 - Normalized CTR (NS) vs. Ambient Temperature

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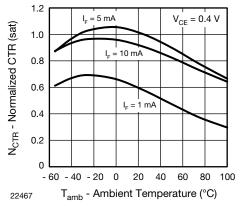


Fig. 12 - Normalized CTR (sat) vs. Ambient Temperature

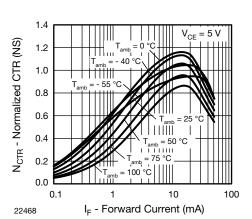


Fig. 13 - Normalized CTR (NS) vs. Forward Current

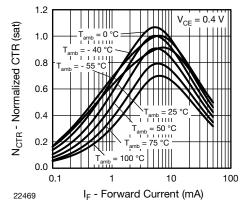


Fig. 14 - Normalized CTR (sat) vs. Forward Current

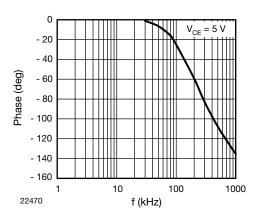


Fig. 15 - F_{CTR} vs. Phase Angle

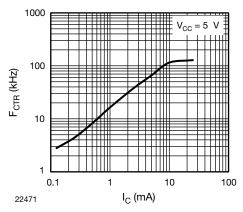


Fig. 16 - F_{CTR} vs. I_C

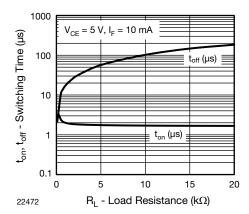
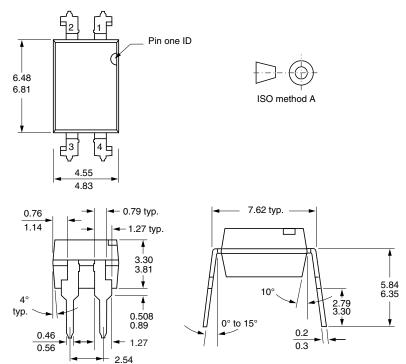


Fig. 17 - Switching Time vs. Load Resistance



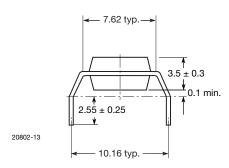
Optocoupler, Phototransistor Output, Vishay Semiconductors AC Input

PACKAGE DIMENSIONS in millimeters



i178027

Option 6



PACKAGE MARKING (example)



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