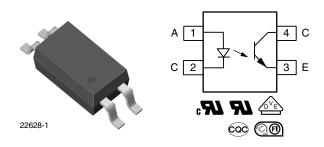


Optocoupler, Phototransistor Output, SSOP-4, Half Pitch, Mini-Flat Package



DESCRIPTION

The VOS615B series has a GaAs infrared emitting diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 4-pin 50 mil lead pitch mini-flat package.

It features a high current transfer ratio at low input current, low coupling capacitance, and high isolation voltage.

The coupling devices are designed for signal transmission between two electrically separated circuits.

FEATURES

- · High CTR with low input current
- · SSOP low profile package (half pitch)
- High collector emitter voltage, V_{CEO} = 80 V
- Isolation test voltage = 3750 V_{RMS}
- · Low coupling capacitance
- High common mode transient immunity
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





RoHS

COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

APPLICATIONS

- Telecom
- Industrial controls
- · Battery powered equipment
- · Office machines
- · Programmable controllers

AGENCY APPROVALS

Safety application model number covering all products in this datasheet is VOS615B. This model number should be used when consulting safety agency documents.

- UL1577, file no. E76222, double protection
- cUL, accordance to CSA component acceptance service no. 5A, double protection
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- FIMKO EN 60950-1
- CQC GB4943.1-2011 and GB8898-2011 (suitable for installation altitude below 2000 m)

ORDERING INFORMATION						
V O S 6 1 5 PART NUMBER	B - #	X 0 0 [TAPE AND REEL	SSOP-4 ≥ 5 mm		
AGENCY CERTIFIED/PACKAGE						
AGENCT CENTIFIED/FACKAGE	10 mA					
UL, cUL, FIMKO, CQC	50 to 600	63 to 125	100 to 200	160 to 320		
SSOP-4, 50 mil pitch	VOS615BT	-	VOS615B-3T	-		
UL, CUL, FIMKO, CQC, VDE (option 1)	50 to 600	63 to 125	100 to 200	160 to 320		
SSOP-4, 50 mil pitch	VOS615B-X001T	VOS615B-2X001T	VOS615B-3X001T	VOS615B-4X001T		

Note

Additional options may be possible, please contact sales office.



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
Reverse voltage		V _R	6	V	
Power dissipation		P _{diss}	100	mW	
Forward current		I _F	60	mA	
Surge forward current	t _p ≤ 10 μs	I _{FSM}	1.5	Α	
Junction temperature		Tj	125	°C	
OUTPUT					
Collector emitter voltage		V _{CEO}	80	V	
Emitter collector voltage		V _{ECO}	7	V	
Collector current		Ic	50	mA	
Power dissipation		P _{diss}	150	mW	
Junction temperature		Tj	125	°C	
COUPLER					
Isolation test voltage between emitter and detector	t = 1 min	V _{ISO}	3750	V_{RMS}	
Total power dissipation		P _{tot}	250	mW	
Storage temperature range		T _{stg}	-55 to +150	°C	
Ambient temperature range		T _{amb}	-55 to +110	°C	
Soldering temperature (1)	t = 10 s	T _{sld}	260	°C	

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices.

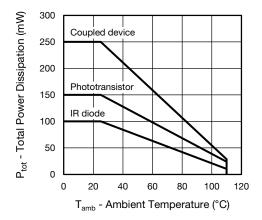


Fig. 1 - Power Dissipation vs. Ambient Temperature

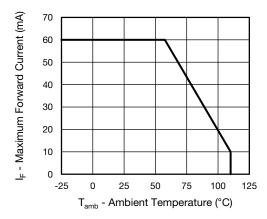


Fig. 2 - Maximum Forward Current vs. Ambient Temperature

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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	$I_F = 5 \text{ mA}$	V_{F}	-	1.18	1.5	V
Reverse current	V _R = 6 V	I _R	-	0.01	10	μA
Capacitance	$V_R = 0 V, f = 1 MHz$	Cı	-	7.3	-	pF
OUTPUT						
Collector emitter leakage current	V _{CE} = 10 V	I _{CEO}	-	0.3	100	nA
Collector emitter breakdown voltage	I _C = 100 μA	BV _{CEO}	80	-	-	V
Emitter collector breakdown voltage	I _E = 10 μA	BV _{ECO}	7	-	-	V
Collector emitter capacitance	V _{CE} = 5 V, f = 1 MHz	C _{CE}	-	5	-	pF
COUPLER						
Collector emitter saturation voltage	$I_F = 5 \text{ mA}, I_C = 2.5 \text{ mA}$	V _{CEsat}	-	0.25	0.4	V
Cut-off frequency	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 100 \Omega$	f _{ctr}	-	155	-	kHz

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I_{C}/I_{F} $I_{F} = 10 \text{ mA}, V_{CE} = 5 \text{ V}$	I _F = 10 mA, V _{CE} = 5 V	VOS615B	CTR	50	-	600	%
		VOS615B-2	CTR	63	-	125	%
		VOS615B-3	CTR	100	-	200	%
		VOS615B-4	CTR	160	-	320	%

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
NON-SATURATED	NON-SATURATED							
Rise and fall time		t _r	-	3	-	μs		
Fall time	$I_C = 2 \text{ mA}, V_{CC} = 5 \text{ V},$	t _f	-	3	-	μs		
Turn-on time	$R_L = 100 \Omega$	t _{on}	-	6	-	μs		
Turn-off time		t _{off}	-	4	-	μs		
SATURATED								
Rise and fall time		t _r	-	3	-	μs		
Fall time	I_F = 1.6 mA, V_{CC} = 5 V, R_L = 1.9 k Ω	t _f	-	12	-	μs		
Turn-on time		t _{on}	-	4	-	μs		
Turn-off time		t _{off}	-	18	-	μs		

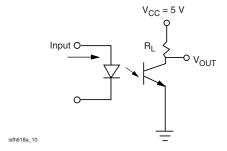


Fig. 3 - Test Circuit

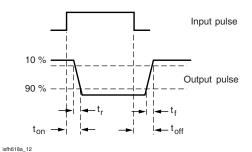


Fig. 4 - Test Circuit and Waveforms



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PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification (according to IEC 68 part 1)			55/110/21	
Comparative tracking index		CTI	175	
Maximum rated withstanding isolation voltage	40 % to 60 % RH, AC test of 1 min	V _{ISO}	3750	V _{RMS}
Maximum transient isolation voltage		V _{IOTM}	6000	V
Maximum repetitive peak isolation voltage		V_{IORM}	707	V
Insulation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	10 ¹¹	Ω
Isolation resistance (under fault conditions)	$V_{IO} = 500 \text{ V}, T_{amb} = T_{SI}$	R _{IO}	10 ⁹	Ω
Output safety power		P _{SO}	350	mW
Input safety current		I _{SI}	200	mA
Input safety temperature		T _{SI}	175	°C
Apparent charge test voltage (method A)	V_{IORM} x 1.6 = V_{PR} , type and sample test t_m = 60 s, partial discharge < 5 pC	V_{PR}	1132	V _{peak}
Apparent charge test voltage (method B)	V_{IORM} x 1.875 = V_{PR} , 100 % production test with t_m = 1 s, partial discharge < 5 pC	V_{PR}	1326	V _{peak}
Creepage distance			≥ 5	mm
Clearance distance			≥ 5	mm
Insulation thickness		DTI	≥ 0.4	mm
Environment (pollution degree in accordance to DI	N VDE 0109)		2	

Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

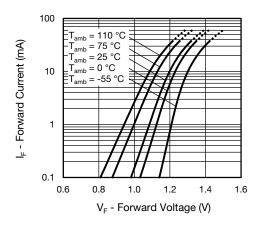


Fig. 5 - Forward Voltage vs. Forward Current

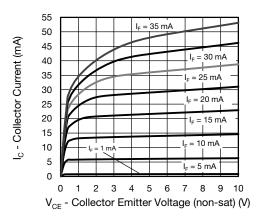


Fig. 6 - Collector Current vs. Collector Emitter Voltage

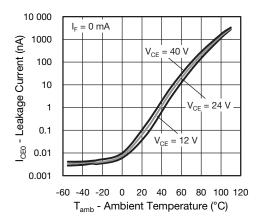


Fig. 7 - Leakage Current vs. Ambient Temperature

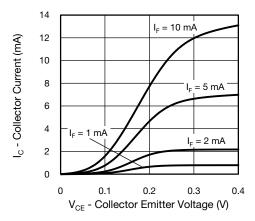


Fig. 8 - Collector Current vs. Collector Emitter Voltage

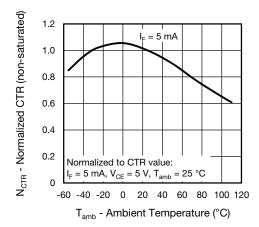


Fig. 9 - Normalized Current Transfer Ratio (non-saturated) vs.

Ambient Temperature

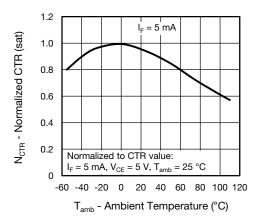


Fig. 10 - Normalized Current Transfer Ratio (saturated) vs.
Ambient Temperature

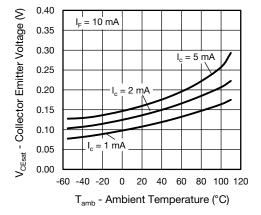


Fig. 11 - Collector Emitter Voltage vs. Ambient Temperature

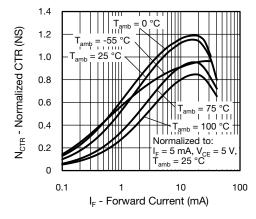


Fig. 12 - Normalized CTR (non-saturated) vs. Forward Current



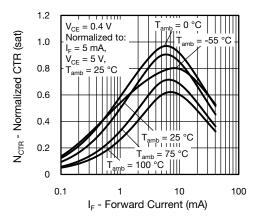


Fig. 13 - Normalized CTR (saturated) vs. Forward Current

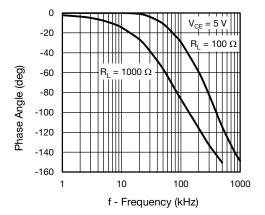


Fig. 14 - F_{CTR} vs. Phase Angle

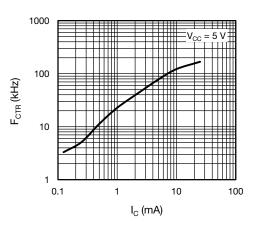


Fig. 15 - F_{CTR} vs. Collector Current

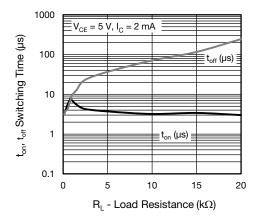
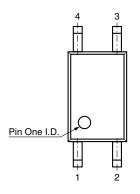
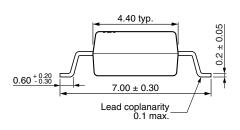


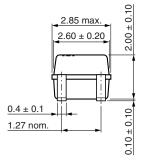
Fig. 16 - Switching Time vs. Load Resistance

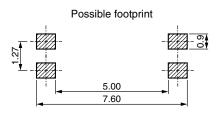


PACKAGE DIMENSIONS (in millimeters)

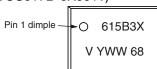








PACKAGE MARKING (example of VOS617B-3X001T)



Notes

- Option 1 is reflected with letter "X".
- Tape and reel suffix (T) is not part of the package marking.



PACKAGING INFORMATION (TAPE AND REEL) (in millimeters)

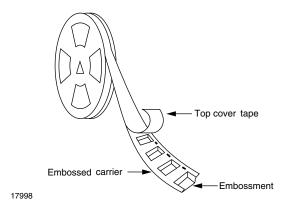


Fig. 17 - Tape and Reel Shipping Medium

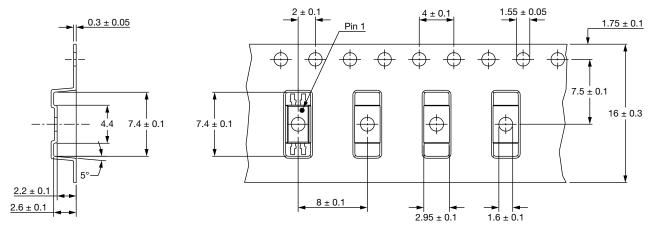


Fig. 18 - Tape and Reel Packing (3000 parts per reel)

SOLDER PROFILES

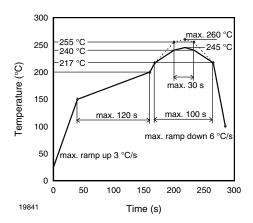


Fig. 19 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2

Floor life: unlimited

Conditions: T_{amb} < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020

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