RoHS

HALOGEN FREE

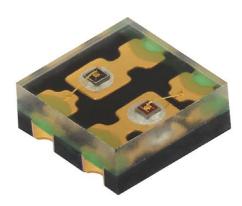
**GREEN** 



www.vishay.com

### Vishay Semiconductors

## Dual Color Emitting Diodes, 660 nm and 940 nm



#### **FEATURES**

- Package type: surface mount
- Package form: square PCB
- Dimensions (L x W x H in mm): 2 x 2 x 0.87
- Peak wavelength:  $\lambda_p = 660 \text{ nm}$  and 940 nm
- High reliability
- · High radiant power
- Angle of half intensity:  $\varphi = \pm 60^{\circ}$
- Floor life: 168 h, MSL 3, according to J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>



- Wearables
- · Health monitoring
- Pulse oximetry

PRODUCT SUMMARY					
COMPONENT	COLOR	I <sub>e</sub> (mW/sr)	φ (deg)	λ <sub>p</sub> (nm)	t <sub>r</sub> (ns)
VSMD66694	Red	2.3	. 60	660	10
VSIVID66694	IR	1.5	± 60	940	10

#### Note

**DESCRIPTION** 

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION	I			
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VSMD66694	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	square PCB	

#### Note

• MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	COLOR	VALUE	UNIT
Reverse voltage		V <sub>R</sub>		5	V
Forward ourrent		1	Red	70	mA
Forward current		I <sub>F</sub>	IR	70	
Dook forward oursent	$t_p/T = 0.1, t_p = 100 \mu s$		Red	140	mA
Peak forward current		I <sub>FM</sub>	IR	140	
Common forward annual to	t <sub>p</sub> = 100 μs	1	Red	1	Α
Surge forward current		IFSM	IR	1	
Power dissipation		Б	Red	161	mW
		P <sub>V</sub>	IR	119	
Junction temperature		Tj		100	°C
Operating temperature range		T <sub>amb</sub>		-25 to +85	°C
Storage temperature range		T <sub>stg</sub>		-25 to +85	°C
Soldering temperature	According fig. 10, J-STD-020	T <sub>sd</sub>		260	°C
Thermal resistance junction / ambient	J-STD-051	R <sub>thJA</sub>		390	K/W

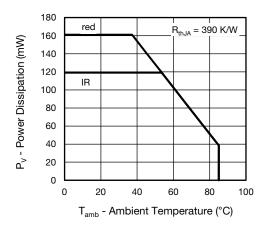


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

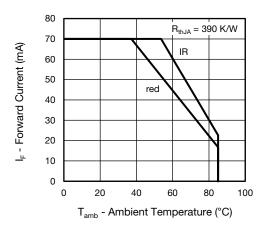
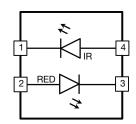


Fig. 2 - Forward Current Limit vs. Ambient Temperature

PARAMETER	TEST CONDITION	SYMBOL	COLOR	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 20 \text{ mA}, t_p = 20 \text{ ms}$	V <sub>F</sub>	Red	-	2.0	2.3	- V
			IR	-	1.4	1.7	
Tamananatana araffiniant	I <sub>F</sub> = 20 mA	TK <sub>VF</sub>	Red	1	-2.3	-	mV/K
Temperature coefficient			IR	ı	-2.3	-	
Reverse current		I <sub>R</sub>	not o	designed for	reverse oper	ation	μΑ
1 12	V <sub>R</sub> = 0 V, f = 1 MHz,	0	Red	ı	7	-	pF
Junction capacitance	$E = 0 \text{ mW/cm}^2$	CJ	IR	ı	5	-	
Padiant intensity	L = 20 mΛ		Red	1.9	2.3	-	mW/sr
Radiant intensity	I <sub>F</sub> = 20 mA	l <sub>e</sub>	IR	0.8	1.5	-	
Padient newer	J 00 A	фе	Red	-	9.5	-	mW
Radiant power	$I_F = 20 \text{ mA}$		IR	-	8.5	-	
Angle of half intensity	I <sub>F</sub> = 20 mA	φ		-	± 60	-	deg
De als sures along eth	I <sub>F</sub> = 20 mA	3	Red	650	660	670	nm
Peak wavelength	IF = 20 IIIA	$\lambda_{p}$	IR	920	940	960	
Spectral bandwidth	I <sub>F</sub> = 20 mA	Δλ	Red	-	20	-	nm
Spectral bandwidth			IR	-	40	-	
Temperature coefficient of $\boldsymbol{\lambda}_p$	I <sub>F</sub> = 20 mA	TK <sub>λp</sub>	Red	ı	0.2	-	nm/K
			IR	-	0.3	-	
Rise time	I <sub>F</sub> = 20 mA	t <sub>r</sub>	Red	-	10	-	ns
			IR	ı	10	-	
Fall time	I <sub>F</sub> = 20 mA	t <sub>f</sub>	Red	-	10	-	ns
			IR	-	10	-	

#### **CIRCUIT BLOCK DIAGRAM**



1	IR LED	IR cathode
2	RED LED	RED anode
3	RED LED	RED cathode
4	IR LED	IR anode

#### BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

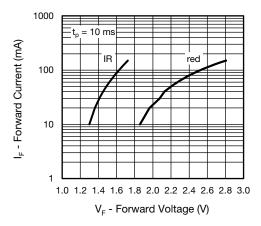


Fig. 3 - Forward Current vs. Forward Voltage

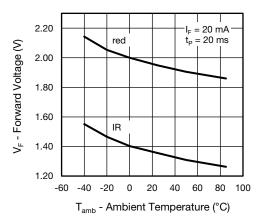


Fig. 4 - Forward Voltage vs. Ambient Temperature

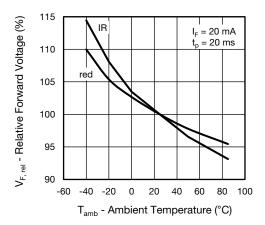


Fig. 5 - Relative Forward Voltage vs. Ambient Temperature

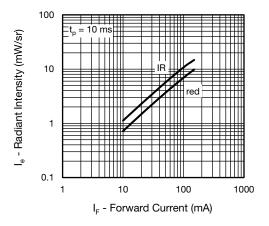


Fig. 6 - Radiant Intensity vs. Forward Current

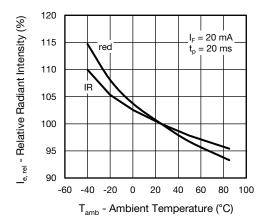


Fig. 7 - Relative Radiant Intensity vs. Ambient Temperature

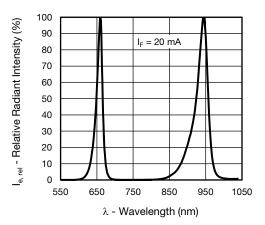


Fig. 8 - Relative Radiant Intensity vs. Wavelength



## 20° 30° l<sub>e, rel</sub> - Relative Radiant Intensity φ - Angular Displacement 1.0 0.9 0.8 0.4 0.2

0

Fig. 9 - Relative Radiant Intensity vs. Angular Displacement

#### **REFLOW SOLDER PROFILE**

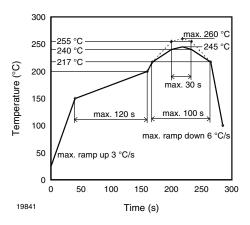


Fig. 10 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

#### **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: level 3

Floor life: 168 h

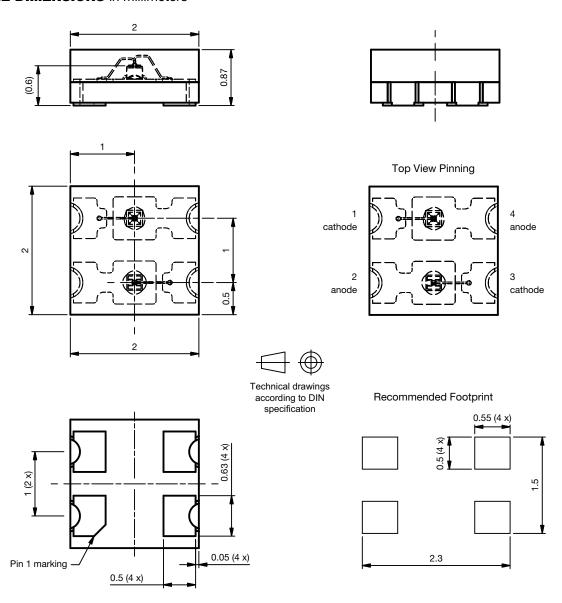
Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

#### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.



#### **PACKAGE DIMENSIONS** in millimeters

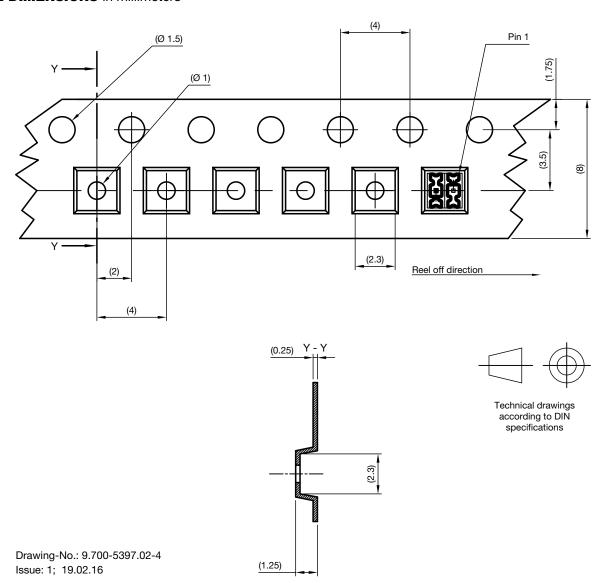


Drawing No.: 6.550-5347.01-4 Not indicated tolerances  $\pm 0.1$ 

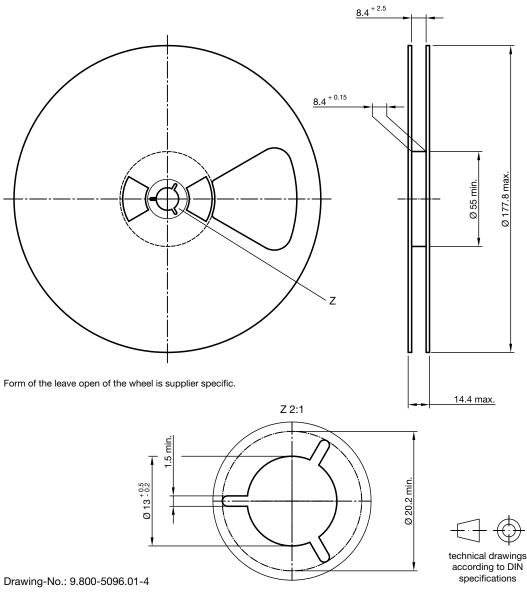
Issue: 1; 19.02.16



#### **TAPE DIMENSIONS** in millimeters



#### **REEL DIMENSIONS** in millimeters



Issue: 4; 08.03.2016

### **Legal Disclaimer Notice**



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