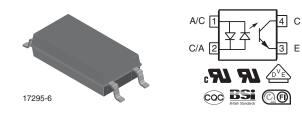
# VOL628A



Vishay Semiconductors

# Optocoupler, Phototransistor Output, AC Input, Low Input Current, 4 Pin LSOP, Long Creepage Mini-Flat Package



### DESCRIPTION

The VOL628A has two GaAs infrared emitting diodes, which are optically coupled to a silicon planar phototransistor detector, and are incorporated in a 4 pin LSOP wide body package.

It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling device is designed for signal transmission between two electrically separated circuits.

### FEATURES

### Low profile package

- High collector emitter voltage, V<sub>CEO</sub> = 80 V
- Isolation test voltage, 5000 V<sub>BMS</sub>
- Low coupling capacitance
- High common mode transient immunity
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

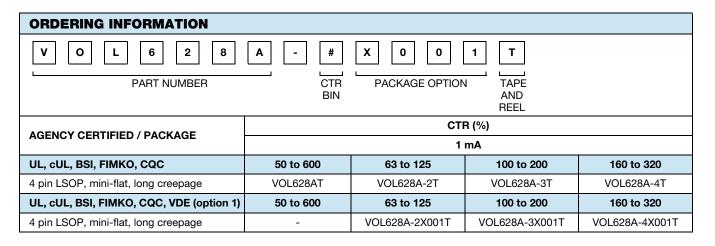
#### **APPLICATIONS**

- Telecom
- Industrial controls
- Battery powered equipment
- Office machines
- Programmable controllers

### AGENCY APPROVALS

(All parts are certified under base model VOL628A)

- UL1577, file no. E76222
- cUL CSA 22.2 bulletin 5A, double protection
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- BSI: EN 60065:2002, EN 60950-1:2006
- FIMKO EN60950-1
- CQC: GB8898-2011, GB4943.1-2011



1



(e3) BoHS

COMPLIANT HALOGEN FREE <u>GREEN</u> (5-2008)



<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT				•		
Reverse voltage		V <sub>R</sub>	6	V		
Power dissipation		P <sub>diss</sub>	100	mW		
Forward current		I <sub>F</sub>	± 60	mA		
Junction temperature		Tj	125	°C		
OUTPUT						
Collector emitter voltage		V <sub>CEO</sub>	80	V		
Emitter collector voltage		V <sub>ECO</sub>	7	V		
Collector current		Ι <sub>C</sub>	50	mA		
	$t_p/T = 0.5, t_p < 10 ms$	Ι <sub>C</sub>	100	mA		
Power dissipation		P <sub>diss</sub>	150	mW		
Junction temperature		Tj	125	°C		
COUPLER						
Total power dissipation		Ptot	250	mW		
Storage temperature range		T <sub>stg</sub>	-55 to +125	°C		
Ambient temperature range		T <sub>amb</sub>	-55 to +110	°C		
Soldering temperature <sup>(1)</sup>	≤ 10 s	T <sub>sld</sub>	260	°C		

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

<sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices

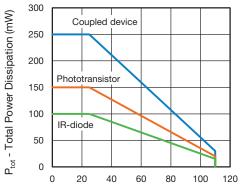




Fig. 1 - Total Power Dissipation vs. Ambient Temperature

ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT		•					
Forward voltage	I <sub>F</sub> = ± 5 mA		V <sub>F</sub>	-	1.16	1.5	V
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>	-	-	100	μA
Capacitance	$V_R = 0 V$ , f = 1 MHz		Co	-	45	-	pF
OUTPUT	-						
Collector emitter leakage current	V <sub>CE</sub> = 10 V, I <sub>F</sub> = 0 A		I <sub>CEO</sub>	-	10	200	nA
Collector emitter capacitance	$V_{CE} = 5 V$ , f = 1 MHz		C <sub>CE</sub>	-	7	-	pF
COUPLER							
	$I_{C} = 0.2 \text{ mA}, I_{F} = \pm 1 \text{ mA}$	VOL628A	V <sub>CEsat</sub>	-	0.25	0.4	V
Collector emitter	$I_{\rm C} = 0.32$ mA, $I_{\rm F} = \pm 1$ mA	VOL628A-2T	V <sub>CEsat</sub>	-	0.25	0.4	V
saturation voltage	$I_{C} = 0.5 \text{ mA}, I_{F} = \pm 1 \text{ mA}$	VOL628A-3T	V <sub>CEsat</sub>	-	0.25	0.4	V
	$I_{C} = 0.8 \text{ mA}, I_{F} = \pm 1 \text{ mA}$	VOL628A-4T	V <sub>CEsat</sub>	-	0.25	0.4	V
Coupling capacitance	f = 1 MHz		C <sub>C</sub>	-	0.25	-	pF

#### Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements

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For technical questions, contact: optocoupleranswers@vishay.com

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<b>CURRENT TRANSFER RATIO</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>		VOL628A	CTR	50	-	600	%
	$I_F = \pm 1 \text{ mA}, V_{CE} = 5 \text{ V}$	VOL628A-2	CTR	63	-	125	%
		VOL628A-3	CTR	100	-	200	%
		VOL628A-4	CTR	160	-	320	%

SWITCHING CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn on time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>on</sub>	-	6	-	μs
Rise time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>r</sub>	-	3.5	-	μs
Turn off time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>off</sub>	-	5.5	-	μs
Fall time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>f</sub>	-	5	-	μs

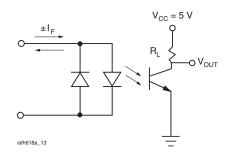
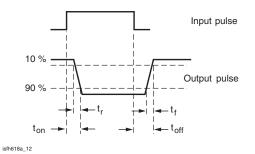
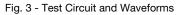


Fig. 2 - Test Circuit



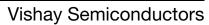


PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		55 / 110 / 21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group IIIa	CTI	275	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V <sub>ISO</sub>	5000	V <sub>RMS</sub>
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	8000	V <sub>peak</sub>
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	VIORM	1050	V <sub>peak</sub>
Isolation resistance	$T_{amb} = 25 \text{ °C}, V_{IO} = 500 \text{ V}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
	$T_{amb} = 100 \ ^{\circ}C, V_{IO} = 500 \ V$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
	$T_{amb} = TS, V_{IO} = 500 V$	R <sub>IO</sub>	≥ 10 <sup>9</sup>	Ω
Output safety power		P <sub>SO</sub>	265	mW
Input safety current		I <sub>SI</sub>	130	mA
Input safety temperature		Ts	150	°C
Creepage distance			≥ 8	mm
Clearance distance			≥8	mm
Insulation thickness		DTI	≥ 0.4	mm
Input to output test voltage, method B	$V_{IORM} x 1.875 = V_{PR}$ , 100 % production test with t <sub>M</sub> = 1 s, partial discharge < 5 pC	V <sub>PR</sub>	2000	V <sub>peak</sub>
Input to output test voltage, method A	$V_{IORM} \times 1.6 = V_{PR}$ , 100 % sample test with t <sub>M</sub> = 10 s, partial discharge < 5 pC	V <sub>PR</sub>	1680	V <sub>peak</sub>

#### Note

According to DIN EN 60747-5-5 (VDE 0884), § 7.4.3.8.2, (see Fig. 4). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits

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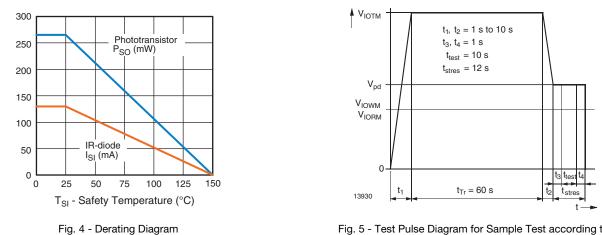
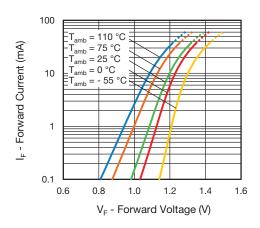


Fig. 5 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-5

### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)



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Fig. 6 - Forward Current vs. Forward Voltage

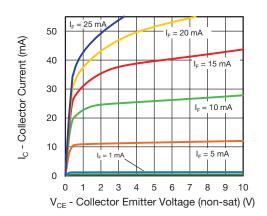


Fig. 7 - Collector Current vs. Collector Emitter Voltage (non-saturated)

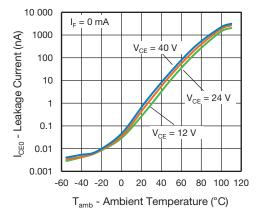


Fig. 8 - Collector Emitter Current vs. Ambient Temperature

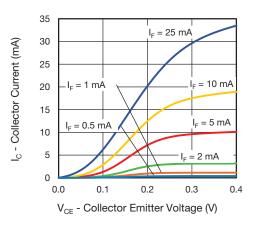
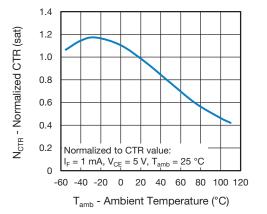


Fig. 9 - Collector Current vs. Collector Emitter Voltage (saturated)

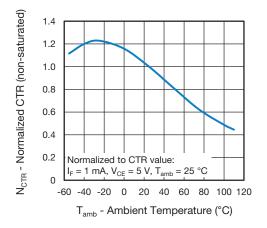
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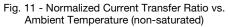
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Fig. 10 - Normalized Current Transfer Ratio vs. Ambient Temperature (saturated)





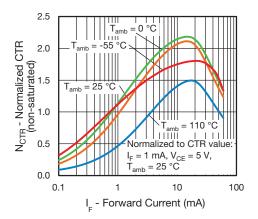


Fig. 12 - Normalized Current Transfer Ratio (non-saturated) vs. Forward Current

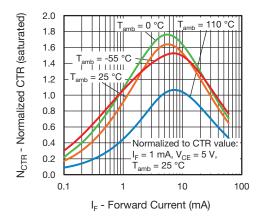


Fig. 13 - Normalized Current Transfer Ratio (saturated) vs. Forward Current

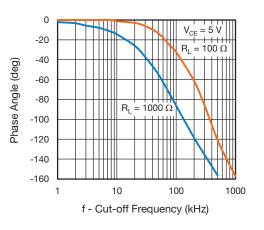


Fig. 14 - Phase Angle vs. Frequency

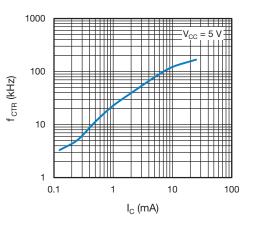
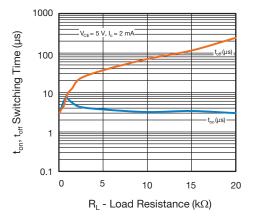


Fig. 15 - f<sub>CTR</sub> vs. Collector Current

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Fig. 16 - Switching Time vs. Load Resistance

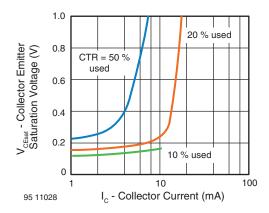


Fig. 17 - Collector Emitter Saturation Voltage vs. Collector Current

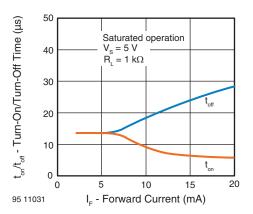


Fig. 18 - Turn-On/Turn-Off Time vs. Forward Current

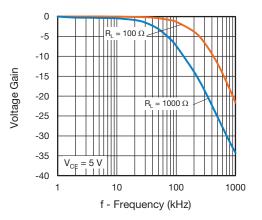
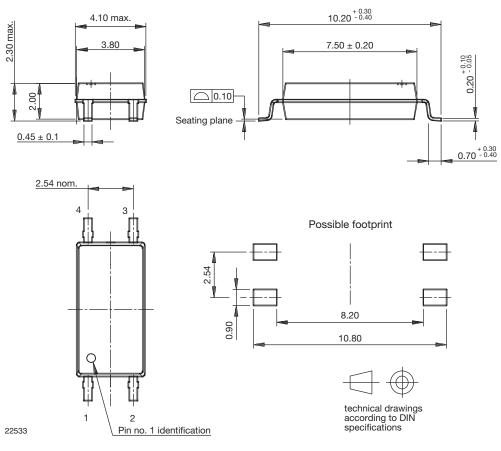


Fig. 19 - Voltage Gain vs. Cut-off Frequency

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### **PACKAGE DIMENSIONS** (in millimeters)



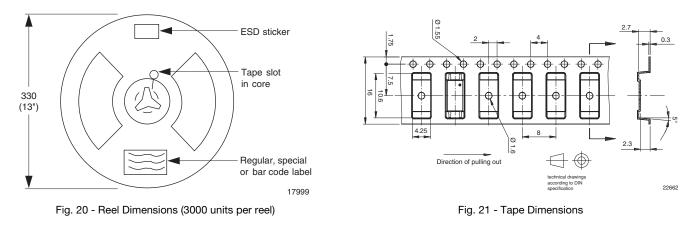
### PACKAGE MARKING (example of VOL628A-3X001T)



#### Notes

- Only option 1 is reflected in the package marking with the characters "X1"
- Tape and reel suffix (T) is not part of the package marking

### TAPE AND REEL DIMENSIONS (in millimeters)



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# VOL628A

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### SOLDER PROFILE

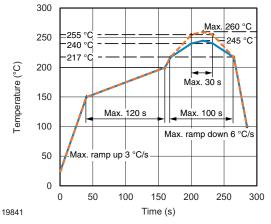


Fig. 22 - Lead (Pb)-free Reflow Solder Profile according to J-STD-020 **Vishay Semiconductors** 

### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited Conditions:  $T_{amb} < 30$  °C, RH < 85 % Moisture sensitivity level 1, according to J-STD-020



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