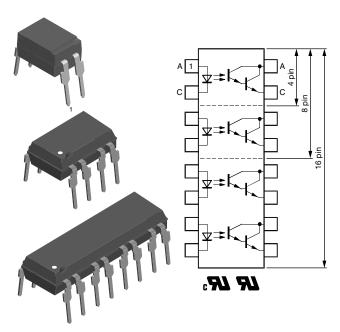


Vishay Semiconductors

Optocoupler, Photodarlington Output



DESCRIPTION

In the K815P, K825P, K845P parts, each channel consist of a photodarlington optically coupled to a gallium arsenide infrared-emitting diode in an 4 pin, 8 pin, and 16 pin plastic dual inline package.

The elements are mounted on one leadframe providing a fixed distance between input and output for highest safety requirements.

FEATURES

- Endstackable to 2.54 mm (0.1") spacing
- Isolation test voltage 5300 V_{RMS}
- Low temperature coefficient of CTR
- Wide ambient temperature range
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT

APPLICATIONS

- Programmable logic controllers
- Modems
- Answering machines
- General applications

AGENCY APPROVALS

- UL1577, file no. E76222 system code C, double protection
- CSA 22.2 bulletin 5A, double protection
- CQC: GB8898-2001 (K815P only)

ORDERING INFORMATION							
K 8	# 5 P PART NUMBER	DIP-4/DIP-8/DIP-16					
AGENCY CERTIFIED/PACKAGE		CTR (%)					
UL, cUL		> 600					
DIP-4 (CQC)		K815P					
DIP-8		K825P					
DIP-16		K845P					

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL VALUE UNI					
INPUT							
Reverse voltage		V _R	6	V			
Forward current		I _F	60	mA			
Forward surge current	t _p ≤ 10 μs	I _{FSM}	1.5	Α			
Power dissipation		P _{diss}	100	mW			
Junction temperature		Tj	125	°C			

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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
OUTPUT						
Collector emitter voltage		V _{CEO}	35	V		
Emitter collector voltage		V _{ECO}	7	V		
Collector current		I _C	80	mA		
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA		
Power dissipation		P _{diss}	150	mW		
Junction temperature		T _j	125	°C		
COUPLER						
AC isolation test voltage (RMS)	t = 1 min, f = 50 Hz	V _{ISO}	5	kV		
Total power dissipation		P _{tot}	250	mW		
Operating ambient temperature		T _{amb}	- 40 to + 100	°C		
Storage temperature range		T _{stg}	- 55 to + 125	°C		
Soldering temperature (1)		T _{sld}	260	°C		

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to wave profile for soldering conditions for through hole devices.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT	•					
Forward voltage	I _F = 20 mA	V_{F}		1.2	1.4	V
Reverse current	V _R = 6 V	I _R			10	μΑ
OUTPUT	•					
Collector emitter voltage	I _C = 100 μA	V_{CEO}	35			V
Emitter collector voltage	I _E = 100 μA	V_{CEO}	7			V
Collector dark current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ A}, E = 0$	I _{CEO}			100	nA
COUPLER						
Collector emitter saturation voltage	$I_C = 5 \text{ mA}, I_F = 20 \text{ mA}$	V_{CEsat}			0.1	V
Cut-off frequency	$I_F = 10 \text{ mA, } V_{CE} = 5 \text{ V,}$ $R_L = 100 \Omega$	f _c		10		kHz
Coupling capacitance	f = 1 MHz	C _k		0.3		pF

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
I _C /I _F	I _F = 1 mA, V _{CE} = 2 V	CTR	600	800		%

SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Rise time	V_{CE} = 2 V, I_{C} = 10 mA, R_{L} = 100 Ω (see figure 1)	t _r		300		μs
Turn-off time	V_{CE} = 2 V, I_{C} = 10 mA, R_{L} = 100 Ω (see figure 1)	t _{off}		250		μs

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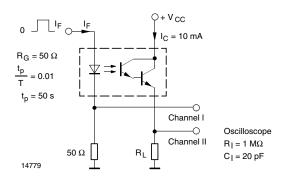


Fig. 1 - Test Circuit, Non-Saturated Operation

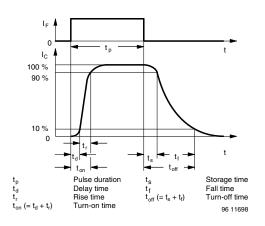


Fig. 2 - Switching Times

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

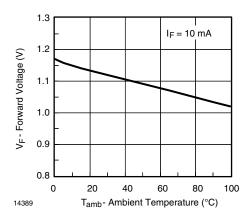


Fig. 3 - Forward Voltage vs. Ambient Temperature

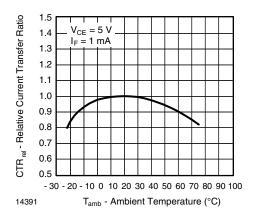


Fig. 5 - Relative Current Transfer Ratio vs. Ambient Temperature

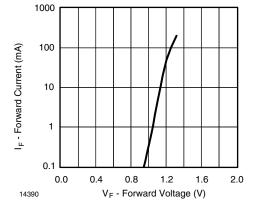


Fig. 4 - Forward Current vs. Forward Voltage

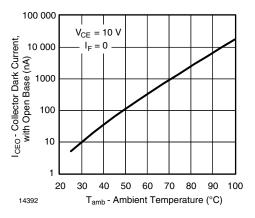


Fig. 6 - Collector Dark Current vs. Ambient Temperature



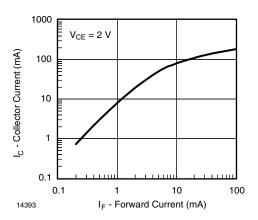


Fig. 7 - Collector Current vs. Forward Current

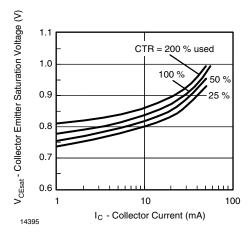


Fig. 9 - Collector Emitter Saturation Voltage vs. Collector Current

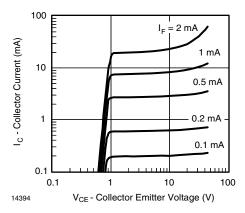


Fig. 8 - Collector Current vs. Collector Emitter Voltage

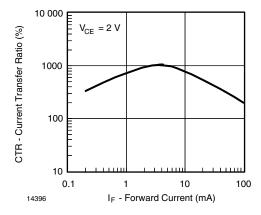
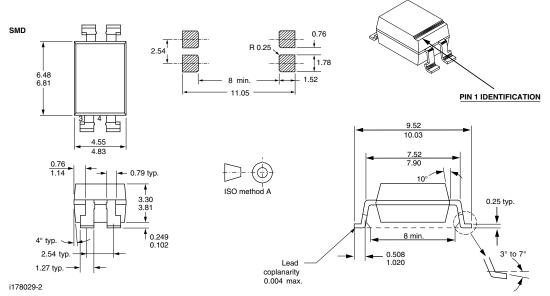


Fig. 10 - Current Transfer Ratio vs. Forward Current

PACKAGE DIMENSIONS in inches (millimeters)

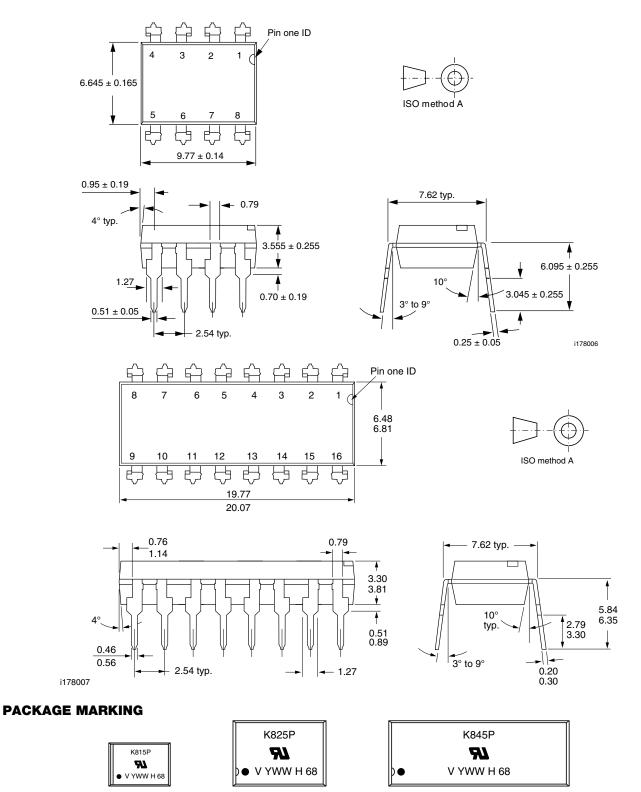


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