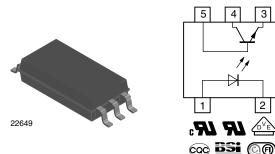
TCLT111. Series

Vishay Semiconductors

Optocoupler, Phototransistor Output, SOP-6L5, 110 °C Rated, Half Pitch, Long Mini-Flat Package

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DESCRIPTION

The TCLT111. series consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 5-lead SOP-6L package.

APPLICATIONS

- Switchmode power supplies
- · Computer peripheral interface
- Microprocessor system interface

FEATURES

- SMD low profile 5 pin package
- Isolation test voltage 5000 V_{RMS}
- CTR flexibility available see order information
- Special construction
- Extra low coupling capacitance
- Connected base
- DC input with transistor output
- Temperature range -55 °C to 110 °C
- Creepage distance > 8 mm
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

AGENCY APPROVALS

- UL1577, file no. E76222
- cUL file no. E76222, equivalent to CSA 22.2 bulletin 5A
- DIN EN 60747-5-5 (VDE 0884-5)
- FIMKO
- BSI
- CQC
- **ORDERING INFORMATION** SOP-6L5 т С L т 1 1 1 # 10.2 mm PART NUMBER AGENCY CTR (%) **CERTIFIED**/ 10 mA 5 mA 5 mA PACKAGE UL, cUL, VDE, 63 to 125 100 to 200 160 to 320 100 to 300 50 to 600 40 to 80 50 to 150 80 to 160 130 to 260 200 to 400 FIMKO, CQC SOP-6L5 TCLT1110 TCLT1111 TCLT1112 TCLT1113 TCLT1114 TCLT1115 TCLT1116 TCLT1117 TCLT1118 TCLT1119

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT	·		•			
Reverse voltage		V _R	6	V		
Forward current		١ _F	60	mA		
Forward surge current	t _p ≤ 10 μs	I _{FSM}	1.5	A		
Power dissipation		P _{diss}	100	mW		
Junction temperature		Tj	125	°C		
OUTPUT						
Collector emitter voltage		V _{CEO}	70	V		
Emitter collector voltage		V _{ECO}	7	V		
Collector current		Ι _C	50	mA		
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA		
Power dissipation		P _{diss}	150	mW		
Junction temperature		Т _і	125	°C		

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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
COUPLER							
Total power dissipation		P _{tot}	250	mW			
Operating ambient temperature range		T _{amb}	-55 to +110	°C			
Storage temperature range		T _{stg}	-55 to +125	°C			
Soldering temperature ⁽¹⁾		T _{sld}	260	٦°			

Notes

Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
Maximum Rating for extended periods of the time can adversely affect reliability.

⁽¹⁾ Wave soldering three cycles are allowed. Also refer to "Assembly Instruction" (www.vishay.com/doc?80054).

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
input								
Forward voltage	I _F = 50 mA	V _F	-	1.25	1.6	V		
Junction capacitance	$V_R = 0 V$, f = 1 MHz	Cj	-	50	-	pF		
output								
Collector emitter voltage	I _C = 1 mA	V _{CEO}	80	-	-	V		
Emitter collector voltage	I _E = 100 μA	V _{ECO}	7	-	-	V		
Collector emitter leakage current	$V_{CE} = 20 \text{ V}, I_F = 0 \text{ A}$	I _{CEO}	-	10	100	nA		
coupler								
Collector emitter saturation voltage	I _F = 10 mA, I _C = 1 mA	V _{CEsat}	-	-	0.3	V		
Cut-off frequency	$\label{eq:Vce} \begin{array}{l} V_{CE}=5~V,~I_{F}=10~mA,\\ R_{L}=100~\Omega \end{array}$	f _c	-	110	-	kHz		
Coupling capacitance	f = 1 MHz	C _k	-	0.3	-	pF		

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
	$V_{CE} = 5 V, I_F = 5 mA$	TCLT1110	CTR	50	-	600	%
	V _{CE} = 5 V, I _F = 10 mA	TCLT1111	CTR	40	-	80	%
		TCLT1112	CTR	63	-	125	%
		TCLT1113	CTR	100	-	200	%
		TCLT1114	CTR	160	-	320	%
	V _{CE} = 5 V, I _F = 1 mA	TCLT1111	CTR	13	30	-	%
I _C /I _F		TCLT1112	CTR	22	45	-	%
IC/IF		TCLT1113	CTR	34	70	-	%
		TCLT1114	CTR	56	100	-	%
	V _{CE} = 5 V, I _F = 5 mA	TCLT1115	CTR	50	-	150	%
		TCLT1116	CTR	100	-	300	%
		TCLT1117	CTR	80	-	160	%
		TCLT1118	CTR	130	-	260	%
		TCLT1119	CTR	200	-	400	%

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SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Tested withstanding isolation voltage	According to UL1577, t = 1 s	VISO	5000	V _{RMS}			
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V _{IOTM}	8000	V _{peak}			
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V _{IORM}	890	V _{peak}			
Partial discharge test voltage - lot test (sample test)	$t_{Tr} = 60 \text{ s}, t_{test} = 10 \text{ s}, \text{ (see figure 2)}$	V _{pd}	13 000	V			
	$T_{amb} = 25 \text{ °C}, V_{IO} = 500 \text{ V}$	R _{IO}	≥ 10 ¹²	Ω			
Isolation resistance	T _{amb} = 100 °C, V _{IO} = 500 V	R _{IO}	≥ 10 ¹¹	Ω			
	T _{amb} = 150 °C, V _{IO} = 500 V (construction test only)	R _{IO}	≥ 10 ⁹	Ω			
Output safety power		P _{SO}	265	mW			
Input safety current		I _{SI}	130	mA			
Input safety temperature		Ts	150	°C			
Creepage distance	DIP-6, option 6		≥ 8	mm			
Clearance distance	DIP-6, option 6		≥ 8	mm			
Insulation distance (internal)			0.75	mm			

Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

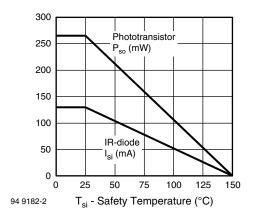


Fig. 1 - Derating Diagram

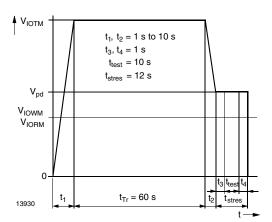


Fig. 2 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-5 (VDE 0884-5); IEC 60747-5-5

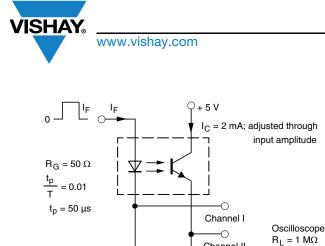
SWITCHING CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Delay time	V_{S} = 5 V, I_{C} = 2 mA, R_{L} = 100 Ω , (see figure 3)	t _d	-	3	-	μs
Rise time	V_{S} = 5 V, I_{C} = 2 mA, R_{L} = 100 Ω , (see figure 3)	t _r	-	3	-	μs
Fall time	$V_S = 5 \text{ V}, \text{ I}_C = 2 \text{ mA}, \text{ R}_L = 100 \Omega$, (see figure 3)	t _f	-	4.7	-	μs
Storage time	V_{S} = 5 V, I_{C} = 2 mA, R_{L} = 100 Ω , (see figure 3)	t _s	-	0.3	-	μs
Turn-on time	V_{S} = 5 V, I_{C} = 2 mA, R_{L} = 100 Ω , (see figure 3)	t _{on}	-	6	-	μs
Turn-off time	V_{S} = 5 V, I_{C} = 2 mA, R_{L} = 100 Ω , (see figure 3)	t _{off}	-	5	-	μs
Turn-on time	$V_S = 5 \text{ V}, \text{ I}_F = 10 \text{ mA}, \text{ R}_L = 1 \text{ k}\Omega$, (see figure 4)	t _{on}	-	9	-	μs
Turn-off time	V_S = 5 V, I_F = 10 mA, R_L = 1 k\Omega, (see figure 4)	t _{off}	-	10	-	μs

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Fig. 3 - Test Circuit, Non-Saturated Operation

100 Ω

50 Ω

Channel II

 $C_{L} = 20 \text{ pF}$

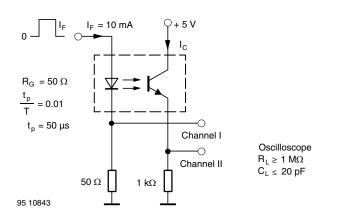


Fig. 4 - Test Circuit, Saturated Operation

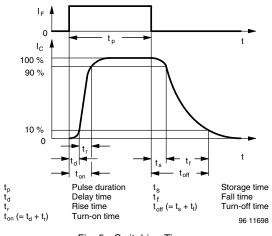


Fig. 5 - Switching Times

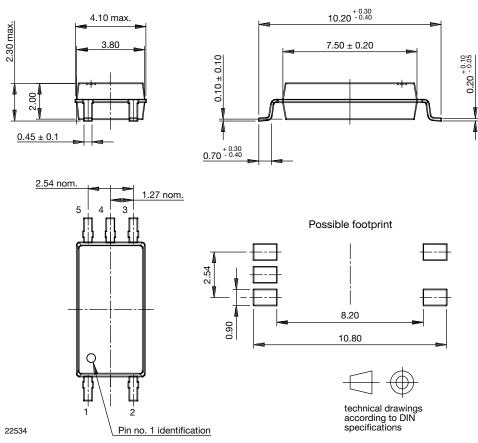
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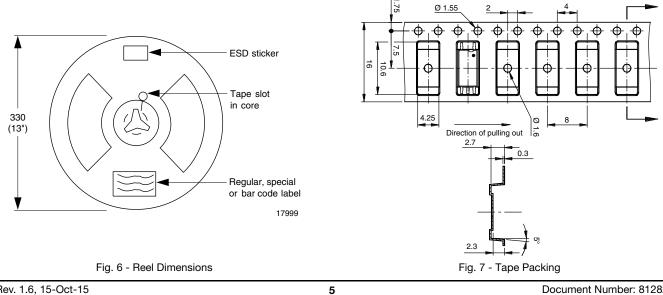
PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING

Ο TCLT1113 V YWW 68

TAPE AND REEL DIMENSIONS (in millimeters)



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SOLDER PROFILE

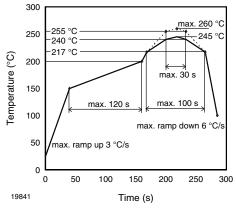


Fig. 8 - Lead (Pb)-free Reflow Solder Profile according to J-STD-020 **Vishay Semiconductors**

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited Conditions: $T_{amb} < 30$ °C, RH < 85 % Moisture sensitivity level 1, according to J-STD-020

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