RoHS

COMPLIANT

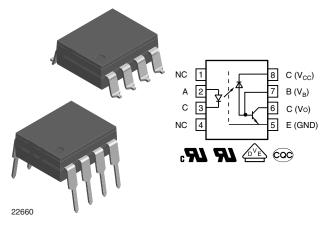
HALOGEN

FREE

GREEN

(5-2008)

Widebody, High Isolation, High Speed Optocoupler, 1 MBd



www.vishay.com

DESCRIPTION

1 MBd widebody optocouplers consist of a GaAlAs infrared emitting diode, optically coupled with an integrated photo detector. An integral Faraday shield provides a high level of noise isolation, required by high power switching applications.

Vishay's 1 MBd wide body couplers feature a high level of isolation distance, exhibiting an external creepage distance of > 10 mm. This makes these parts ideal for applications with working voltages exceeding 1000 V.

FEATURES

- External creepage > 10 mm
- Reinforced isolation
- Internal shield for very high input to output noise isolation
- High common mode interference immunity
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

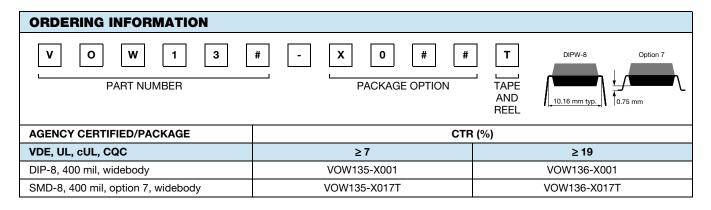
APPLICATIONS

- Solar inverters
- Industrial motor drives
- Welding equipment
- Isolated industrial communications
- Noise isolation of sensitive circuits

AGENCY APPROVALS

The safety application model number covering all products in this datasheet is VOW135 or VOW136 respectively. This model number should be used when consulting safety agency documents.

- UL1577
- cUL
- DIN EN 60747-5-5 (VDE 0884-5)
- CQC



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ABSOLUTE MAXIMUM RA	TINGS (T _{amb} = 25 °C, unless of	herwise specifie	d)	
PARAMETER	CONDITIONS	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V _R	3	V
Forward current		١ _F	25	mA
Peak forward current	t = 1 ms, duty cycle 50 %	I _{FM}	50	mA
Maximum surge forward current	$t \le 1 \ \mu s$, 300 pulses/s	I _{FSM}	1	А
Power dissipation		P _{diss}	45	mW
Input junction temperature		T _{j max.}	125	°C
OUTPUT				
Supply voltage		V _S	-0.5 to 30	V
Output voltage		Vo	-0.5 to 25	V
Emitter base voltage		V _{EBO}	5	V
Average output current		Ι _Ο	8	mA
Peak output current		Ι _Ο	16	mA
Base current		Ι _Β	5	mA
Power dissipation		P _{diss}	100	mW
Output junction temperature		T _{j max.}	125	°C
COUPLER				
Storage temperature range		T _{stg}	-55 to +150	°C
Ambient temperature range		T _{amb}	-40 to +100	°C
Soldering temperature	max. ≤ 10 s, dip soldering ≥ 0.5 mm distance from case bottom	T _{sld}	260	°C

Notes

• Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

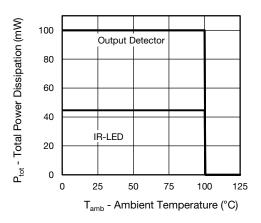


Fig. 1 - Maximum Power vs. Operating Temperature

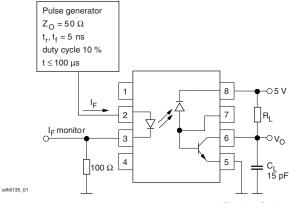


ELECTRICAL CHARACT	ERISTICS ($T_{amb} = 0 \ ^{\circ}C$ to 70	°C, unles	s otherwis	e specit	ied)		
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I _F = 16 mA		V _F		1.38	1.9	V
Breakdown voltage	I _R = 10 μA		V _{BR}	3			V
Reverse current	V _R = 3 V		I _R		0.5	10	μA
Input capacitance	$V_R = 0 V, f = 1 MHz$		CI		36		pF
Temperature coefficient of forward voltage	I _F = 16 mA		$\Delta V_{\rm F} / \Delta T_{\rm amb}$		-1.9		mV/°C
OUTPUT	·						
Logic low supply current	$I_F = 16 \text{ mA}, V_O = \text{open}, V_{CC} = 15 \text{ V}$		I _{CCL}		50	200	μA
Logic high supply current	$I_F = 0 \text{ A}, V_O = \text{open}, V_{CC} = 15 \text{ V}$		I _{CCH}		0.02	2	μA
Output voltage, output logic low	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V}, I_O = 0.8 \text{ mA}$	VOW135	V _{OL}		0.1	0.5	V
Output voltage, output logic low	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V}, I_O = 2.4 \text{ mA}$	VOW136	V _{OL}		0.1	0.5	V
Output ourrant, output logic high	$I_F = 0 \text{ mA}, V_O = V_{CC} = 5.5 \text{ V}$		I _{OH}		3	500	nA
Output current, output logic high	$I_F = 0 \text{ mA}, V_O = V_{CC} = 15 \text{ V}$		I _{OH}		0.01	1	μA
Output capacitance	$V_R = 0 V, f = 1 MHz$		Co		3.70		pF
COUPLER							
Capacitance (input to output)	f = 1 MHz		CIO		0.9		pF

Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T _{amb} = 0 °C to 70 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio	$I_F = 16 \text{ mA}, V_O = 0.4 \text{ V}, V_{CC} = 4.5 \text{ V}, T_{amb} = 25 \text{ °C}$	VOW135	CTR	7	18	50	%
		VOW136	CTR	19	24	50	%
	$I_F = 16 \text{ mA}, V_O = 0.5 \text{ V}, V_{CC} = 4.5 \text{ V}$	VOW135	CTR	5	19		%
		VOW136	CTR	15	25		%



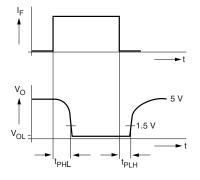


Fig. 2 - Schematics

SWITCHING CHARACTERISTICS ($T_{amb} = 0$ °C to 70 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
High to low	I_{F} = 16 mA, V_{CC} = 5 V, R_{L} = 4.1 $k\Omega$	VOW135	t _{PHL}		0.2	2.0	μs
High to low	I_F = 16 mA, V_{CC} = 5 V, R_L = 1.9 $k\Omega$	VOW136	t _{PHL}		0.2	1.0	μs
Low to high	I_F = 16 mA, V_{CC} = 5 V, R_L = 4.1 $k\Omega$	VOW135	t _{PLH}		1.3	2.0	μs
Low to high	I_{F} = 16 mA, V_{CC} = 5 V, R_{L} = 1.9 k Ω	VOW136	t _{PLH}		0.6	1.0	μs

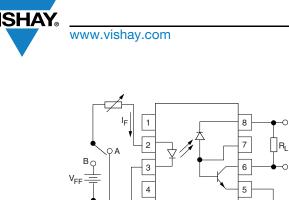
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+V_{CM}

isfh6135_02

Pulse generator

 $Z_{O} = 50 \ \Omega$ $t_{r}, t_{f} = 8 \ ns$

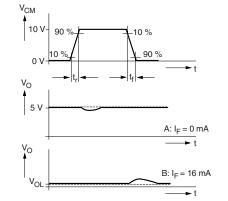


Fig. 3 - Common Mode Interference Immunity

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ovo

COMMON MODE TRANSIENT IMMUNITY ($T_{amb} = 0$ °C to 70 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
High	V_{CM} = 10 V_{PP} , V_{CC} = 5 V, I_F = 0 mA, R_L = 4.1 k Ω	VOW135	CM _H	1000			V/µs
nign	V_{CM} = 10 V_{PP} , V_{CC} = 5 V, I_F = 0 mA, R_L = 1.9 k Ω	VOW136	CM _H	1000			V/µs
Low	V_{CM} = 10 V_{PP},V_{CC} = 5 V, I_F = 16 mA, R_L = 4.1 $k\Omega$	VOW135	CML	1000			V/µs
LUW	V_{CM} = 10 V_{PP},V_{CC} = 5 V, I_F = 16 mA, R_L = 1.9 $k\Omega$	VOW136	CML	1000			V/µs

PARAMETER		SYMBOL	VALUE	UNIT
Climatic classification (according to IEC	68 part 1)		55/100/21	
Comparative tracking index		CTI	250	
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	5300	V _{RMS}
Maximum transient isolation voltage		V _{IOTM}	8000	V _{peak}
Maximum repetitive peak isolation voltage	e	V _{IORM}	1414	V _{peak}
Insulation resistance	$T_{amb} = 25 \text{ °C}, V_{DC} = 500 \text{ V}$	R _{IO}	≥ 10 ¹²	Ω
Insulation resistance	$T_{amb} = 100 \ ^{\circ}C, V_{DC} = 500 \ V$	R _{IO}	≥ 10 ¹¹	Ω
Output safety power		P _{SO}	700	mW
Input safety current		I _{SI}	400	mA
Safety temperature		T _S	150	°C
Clearance distance (DIP-8, widebody)			≥ 10	mm
Creepage distance (DIP-8, widebody)			≥ 10	mm
Insulation thickness		DTI	≥ 0.4	mm
Input to output test voltage, method B	$V_{IORM} x 1.875 = V_{PR}$, 100 % production test with $t_M = 1$ s, partial discharge < 5 pC	st V _{PR} 2651		V _{peak}
Input to output test voltage, method A	$V_{IORM} x 1.6 = V_{PR}$, 100 % production test with $t_M = 10$ s, partial discharge < 5 pC	V _{PR}	2262	V _{peak}
Environment (pollution degree in accordance to DIN VDE 0109)			2	

Note

• As per DIN EN 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.





TYPICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified)

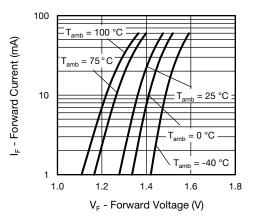


Fig. 4 - Output Current vs. Forward Voltage

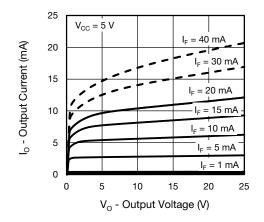


Fig. 5 - Output Current vs. Output Voltage

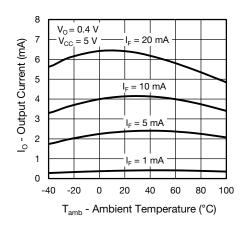


Fig. 6 - Output Current vs. Temperature

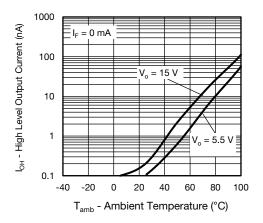


Fig. 7 - Logic High Level Output Current vs. Temperature

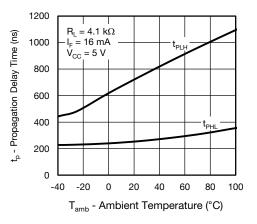


Fig. 8 - Propagation Delay vs. Ambient Temperature - VOW135

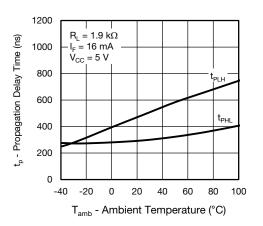


Fig. 9 - Propagation Delay vs. Ambient Temperature - VOW136

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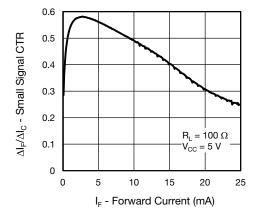


Fig. 10 - Small Signal Current Transfer Ratio vs. Forward Current

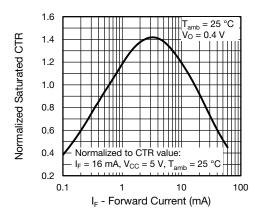


Fig. 11 - Normalized Saturated CTR vs. Forward Current

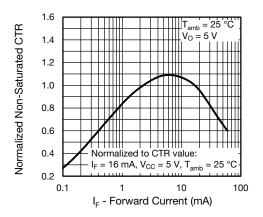


Fig. 12 - Normalized Non-Saturated CTR vs. Forward Current

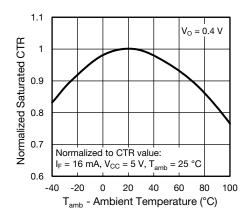


Fig. 13 - Normalized Saturated CTR vs. Ambient Temperature

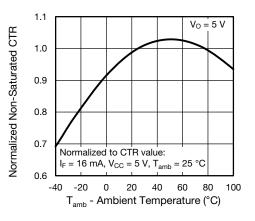


Fig. 14 - Normalized Non-Saturated CTR vs. Ambient Temperature

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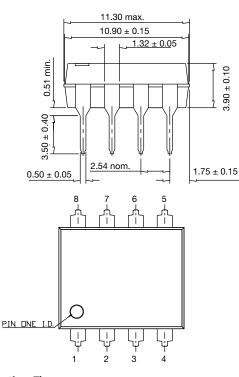
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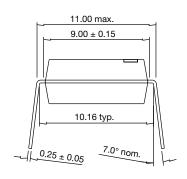
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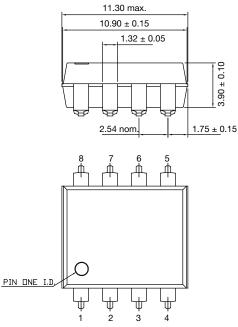
PACKAGE DIMENSIONS in millimeters

DIP-8, widebody



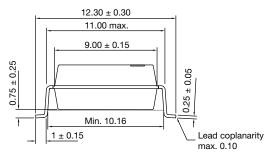


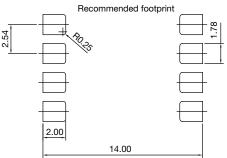
SMD-8, widebody (option 7)



PACKAGE MARKING (example of VOW136-X017T)







Note

Tape and reel suffix (T) is not part of the package marking.

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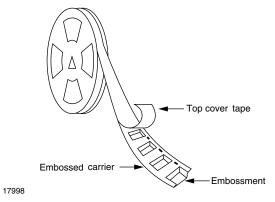
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PACKING INFORMATION (tape and reel)





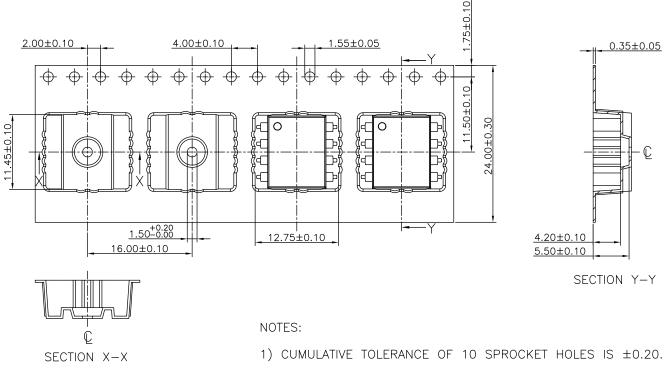
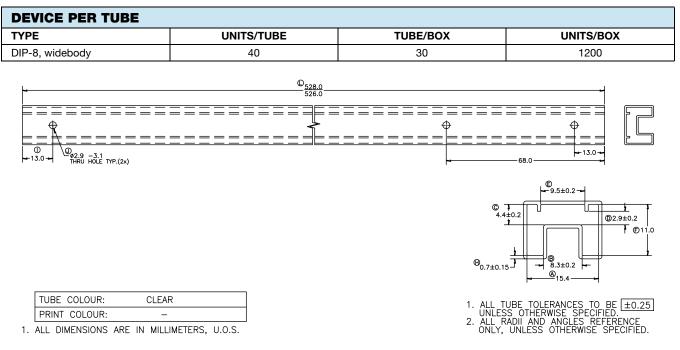


Fig. 16 - Tape and Reel Packing Option 7 (750 parts per reel)



PACKING INFORMATION (tubes)



SOLDER PROFILES

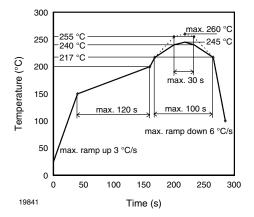


Fig. 17 - Lead (Pb)-free Reflow Solder Profile According to
J-STD-020 for SMD Devices

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited Conditions: $T_{amb} < 30$ °C, RH < 85 % Moisture sensitivity level 1, according to J-STD-020



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