



Vishay Semiconductors

AUTOMOTIVE

RoHS

COMPLIANT

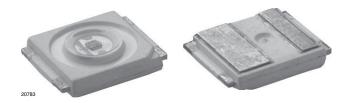
HALOGEN

FREE

GREEN

(5-2008)

Little Star® 1 W Power SMD LED



DESCRIPTION

The VLMR71.., VLMY71.. is one of the most robust and light efficient LEDs in the market. With its extremely high level of brightness and the ultra low high profile, which is only 1.5 mm are highly suitable for both conventional lighting and specialized application such as automotive signal lights, traffic lights, channel lights, tube lights and garden lights among others.

PRODUCT GROUP AND PACKAGE DATA

Product group: LED
Package: SMD Little Star
Product series: power
Angle of half intensity: ± 60°

FEATURES

- Super high brightness surface-mount LED
- · High flux output
- 120° viewing angle
- Compact package outline (L x W x H) in mm: 6.0 x 6.0 x 1.5
- Ultra low height profile: 1.5 mm
- · Designed for high current drive; typically 400 mA
- Low thermal resistance; R_{thJP} = 20 K/W
- Qualified according to JEDEC[®] moisture sensitivity level 2a
- Compatible with IR reflow solder processes according to CECC 00802 and J-STD-020C
- Little Star[®] are class 1M LED products. Do not view directly with optical instrument
- AEC-Q101 qualified
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- Optical efficiency typical up to 52 lm/W
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Automotive: exterior applications, e.g. center high mounted stop light (CHMSL), rear combination lights (RCLs), signal lighting, etc.
- Communication: indicator and backlight in mobile phone
- Industry: white goods (e.g. oven, microwave, etc.)
- Lighting: garden light, architecture lighting, general lighting, etc.

PARTS TABLE														
PART	COLOR	LUMINOUS INTENS		ENSITY	at I _F	1 (1111)		at I _F	FORWARD VOLTAGE (V)		at I _F	TECHNOLOGY		
		MIN.	TYP.	MAX.	(mA)	MIN.	TYP.	MAX.	(IIIA)	MIN.	TYP.	MAX.	(mA)	
VLMR71AAAC-GS08	Red	7150	-	14 000	400	620	-	630	400	2.2	-	2.8	400	AllnGaP
VLMY71AAAC-GS08	Yellow	7150	-	14 000	400	585	-	597	400	2.2	-	2.8	400	AllnGaP

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified) VLMR71, VLMY71						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Forward current		I _F	400	mA		
Power dissipation		P _{tot}	1120	mW		
Junction temperature		Tj	+120	°C		
Surge current t < 10 µs, d = 0.1		I _{FM}	500	mA		
Operating temperature range		T _{amb}	-40 to +100	°C		
Storage temperature range		T _{stg}	-40 to +100	°C		
Thermal resistance junction-to-pin		R _{thJP}	20	K/W		

Note

· Not designed for reverse operation



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OPTICAL AND ELECTRICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified) VLMR71AAAC-GS08, RED							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I _F = 400 mA	VLMR71AAAC	l _V	7150	-	14 000	mcd
Dominant wavelength	I _F = 400 mA		λ_{d}	620	-	630	nm
Spectral bandwidth at 50 % I _{rel max} .	I _F = 400 mA		Δλ	-	18	-	nm
Angle of half intensity	I _F = 400 mA		φ	-	± 60	-	٥
Forward voltage (1)	I _F = 400 mA		V_{F}	2.2	-	2.8	V
Optical efficiency	I _F = 400 mA		η _{opt}	-	30	39	lm/W

Note

 $^{^{(1)}}$ Forward voltages are tested at a current pulse duration of 1 ms and a tolerance of \pm 0.05 V

OPTICAL AND ELECTRICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified) VLMY71AAAC-GS08, YELLOW							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I _F = 400 mA	VLMY71AAAC	l _V	7150	-	14 000	mcd
Dominant wavelength	I _F = 400 mA		λ_{d}	585	-	597	nm
Spectral bandwidth at 50 % I _{rel max} .	I _F = 400 mA		Δλ	-	15	-	nm
Angle of half intensity	I _F = 400 mA		φ	-	± 60	-	0
Forward voltage (1)	I _F = 400 mA		V_{F}	2.2	-	2.8	V
Optical efficiency	I _F = 400 mA		η_{opt}	- 1	30	39	Im/W

Note

 $^{^{(2)}}$ Forward voltages are tested at a current pulse duration of 1 ms and a tolerance of \pm 0.05 V

LUMINOUS INTENSITY / FLUX CLASSIFICATION						
GROUP	LUMINOUS INTENSITY I _V (mcd)		LUMINOUS FLUX ϕ $_{ m V}$ (mlm) CORRELATION TABLE			
STANDARD	MIN.	MAX.	MIN.	MAX.		
AA	7150	9000	20 700	26 100		
AB	9000	11 250	26 100	33 000		
AC	11 250	14 000	33 000	39 000		
AD	14 000	18 000	39 000	52 000		
AE	18 000	22 400	52 000	71 000		
AF	22 400	28 500	71 000	97 000		

Note

[•] Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 11 %. The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups will not be orderable. In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped in any one reel. In order to ensure availability, single wavelength groups will not be orderable



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COLOR CLASSIFICATION						
	DOM. WAVE	DOM. WAVELENGTH (nm)				
GROUP	YELLOW					
	MIN.	MAX.				
A	585	588				
В	588	591				
С	591	594				
D	594	597				

Note

Wavelengths are tested at a current pulse duration of 25 ms and an accuracy of ± 1 nm

FORWARD VOLTAGE CLASSIFICATION					
GROUP	FORWARD VOLTAGE (V)				
GROOP	MIN.	MAX.			
02	2.2	2.5			
03	2.5	2.8			

Note

Forward voltages are tested at a current pulse duration of 25 ms and a tolerance of ± 0.05 V. In order to ensure availability, a single forward
voltage group can not be ordered

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

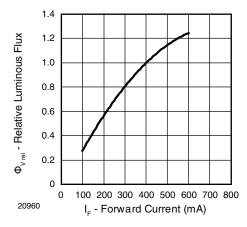


Fig. 1 - Relative Luminous Flux vs. Forward Current

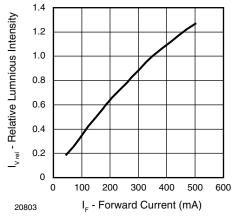


Fig. 2 - Relative Luminous Intensity vs. Forward Current

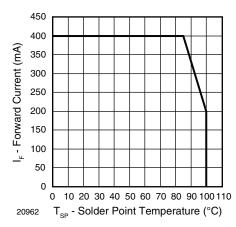


Fig. 3 - Forward Current vs. Solder Point Temperature

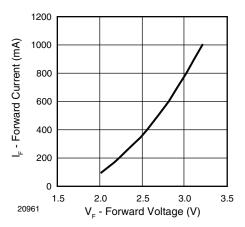


Fig. 4 - Forward Current vs. Forward Voltage

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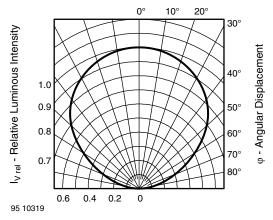
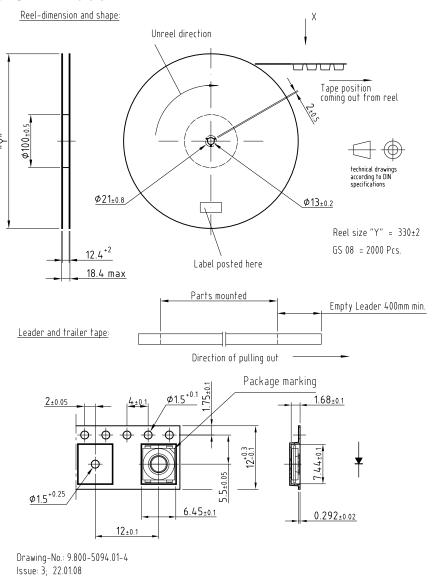


Fig. 5 - Relative Luminous Intensity vs. Angular Displacement

TAPING DIMENSIONS in millimeters

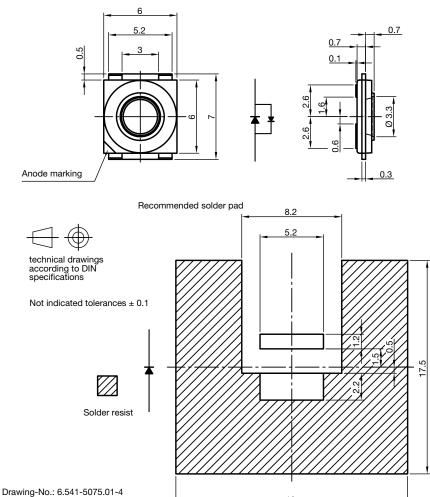


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PACKAGE DIMENSIONS / SOLDERING PADS DIMENSIONS in millimeters



SOLDERING PROFILE

50

19885-1

Preconditioning acc. to JEDEC level 3 300 max. 260 °C 250 245 °C -240 °C -217 °C Temperature (°C) 200 max. 30 s 150 max. 100 s max. 120 s 100 50 max. ramp up 3 °C/s max. ramp down 6 °C/s

Issue: 3; 23.03.12

IR Reflow Soldering Profile for Lead (Pb)-free Soldering

Fig. 6 - Vishay Lead (Pb)-free Reflow Soldering Profile (according to J-STD-020C)

150

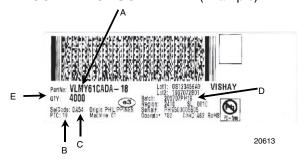
Time (s)

200

max. 2 cycles allowed

100

BAR CODE PRODUCT LABEL (Example)



A. Type of component

19

- B. Manufacturing plant
- C. SEL selection code (bin):
 - e.g.: DA = code for luminous intensity group
 - 5 = code for color group
 - 4 = code for forward voltage
- D. Batch no.
 - 20070 = year 2007, week 07
 - PH19 = plant code

E. Total quantity

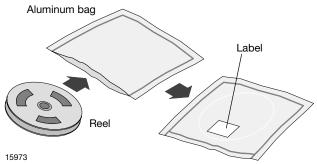


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DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

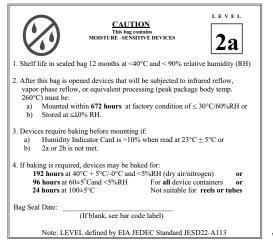
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

Legal Disclaimer Notice



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