

**Vishay Semiconductors** 

## Thyristor High Voltage, Phase Control SCR, 25 A



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PRIMARY CHARACTERISTICS			
I <sub>T(AV)</sub>	16 A		
V <sub>DRM</sub> /V <sub>RRM</sub>	800 V, 1200 V		
V <sub>TM</sub>	1.25 V		
I <sub>GT</sub>	45 mA		
TJ	-40 °C to 125 °C		
Package	TO-220AB 3L		
Circuit configuration	Single SCR		

### **FEATURES**

- · Designed and qualified according to JEDEC®-JESD 47
- 125 °C max. operating junction temperature
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

#### **APPLICATIONS**

• Typical usage is in input rectification crowbar (soft start) and AC switch in motor control, UPS, welding, and battery charge.

#### DESCRIPTION

The VS-25TTS... high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

OUTPUT CURRENT IN TYPICAL APPLICATIONS					
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS		
Capacitive input filter $T_A = 55 \text{ °C}, T_J = 125 \text{ °C},$ common heatsink of 1 °C/W	18	22	A		

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I <sub>T(AV)</sub>	Sinusoidal waveform	16	٨		
I <sub>RMS</sub>		25	A		
V <sub>RRM</sub> /V <sub>DRM</sub>		800, 1200	V		
I <sub>TSM</sub>		320	А		
V <sub>T</sub>	16 A, T <sub>J</sub> = 25 °C	1.25	V		
dV/dt		500	V/µs		
dl/dt		150	A/µs		
TJ		-40 to +125	°C		

VOLTAGE RATINGS							
PART NUMBER	V <sub>RRM</sub> , MAXIMUM PEAK REVERSE VOLTAGE V	V <sub>DRM</sub> , MAXIMUM PEAK DIRECT VOLTAGE V	I <sub>RRM</sub> /I <sub>DRM</sub> AT 125 °C mA				
VS-25TTS08-M3	800	800	10				
VS-25TTS12-M3	1200	1200	10				

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# VS-25TTS08-M3, VS-25TTS12-M3

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ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CO	VAL	UNITS				
PARAMETER	STINDUL	TEST CO	NDITION5	TYP.	MAX.	UNITS		
Maximum average on-state current	I <sub>T(AV)</sub>	$T_{\rm C} = 93 ^{\circ}{\rm C}$ , 180° conduc	tion half sine wave	1	6			
Maximum RMS on-state current	I <sub>RMS</sub>			2	25	А		
Maximum peak, one-cycle,	l	10 ms sine pulse, rated	√ <sub>RRM</sub> applied	2	70	A		
non-repetitive surge current	I <sub>TSM</sub>	10 ms sine pulse, no volt	age reapplied	3	20			
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	10 ms sine pulse, rated	/ <sub>RRM</sub> applied	3	65	•2		
Maximum I-t for fusing	14t	10 ms sine pulse, no volt	515		A <sup>2</sup> s			
Maximum I <sup>2</sup> $\sqrt{t}$ for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied		5152		A²√s		
Maximum on-state voltage drop	V <sub>TM</sub>	16 A, T <sub>J</sub> = 25 °C		1.25		V		
On-state slope resistance	r <sub>t</sub>			12	2.0	mΩ		
Threshold voltage	V <sub>T(TO)</sub>	$T_{\rm J} = 125 \ ^{\circ}{\rm C}$ 1.0		.0	V			
	1 /1	$T_J = 25 \text{ °C}$		0	.5			
Maximum reverse and direct leakage current	I <sub>RM</sub> /I <sub>DM</sub>	$T_{\rm J} = 125 ^{\circ}{\rm C}$ $V_{\rm R} = {\rm Rated}  V_{\rm RRM} / V_{\rm DRM}$		1	0			
Holding current	Ι <sub>Η</sub>	Anode supply = 6 V, resistive load, initial $I_T$ = 1 A, $T_J$ = 25 °C		-	150	mA		
Maximum latching current	١ <sub>L</sub>	Anode supply = 6 V, resistive load, $T_J = 25 \text{ °C}$		Anode supply = 6 V, resistive load, $T_J = 25 \text{ °C}$		20	00	
Maximum rate of rise of off-state voltage	dV/dt	$T_J = T_J max.$ , linear to 80 °C, $V_{DRM} = R_g - k = Open$		50	00	V/µs		
Maximum rate of rise of turned-on current	dl/dt			1		50	A/µs	

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P <sub>GM</sub>	GM		W	
Maximum average gate power	P <sub>G(AV)</sub>		2.0	vv	
Maximum peak positive gate current	+ I <sub>GM</sub>		1.5	А	
Maximum peak negative gate voltage	- V <sub>GM</sub>		10	V	
	I <sub>GT</sub>	Anode supply = 6 V, resistive load, $T_J$ = - 10 °C	60	mA	
Maximum required DC gate current to trigger		Anode supply = 6 V, resistive load, $T_J = 25 \ ^{\circ}C$	45		
		Anode supply = 6 V, resistive load, $T_J = 125 \text{ °C}$	20		
	V <sub>GT</sub>	Anode supply = 6 V, resistive load, $T_J$ = - 10 °C	2.5		
Maximum required DC gate voltage to trigger		Anode supply = 6 V, resistive load, $T_J = 25 \text{ °C}$ 2.0		V	
		Anode supply = 6 V, resistive load, $T_J$ = 125 °C	1.0	v	
Maximum DC gate voltage not to trigger	V <sub>GD</sub>	$T = 125 \circ C M$ = Detectively:	0.25		
Maximum DC gate current not to trigger	I <sub>GD</sub>	T <sub>J</sub> = 125 °C, V <sub>DRM</sub> = Rated value	2.0	mA	

SWITCHING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Typical turn-on time	t <sub>gt</sub>	T <sub>J</sub> = 25 °C	0.9		
Typical reverse recovery time	t <sub>rr</sub>	T <sub>.1</sub> = 125 °C	4	μs	
Typical turn-off time	tq	1j = 125 C	110		

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### VS-25TTS08-M3, VS-25TTS12-M3

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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	Maximum junction and storage temperature range			-40 to +125	°C
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation	1.1	
Maximum thermal resistance, junction to ambient		R <sub>thJA</sub>		62	°C/W
Typical thermal resistance, case to heatsink			Mounting surface, smooth and greased	0.5	
Approximate weight				2	g
Approximate weight				0.07	oz.
Mounting torque	minimum			6 (5)	kgf ⋅ cm
	maximum			12 (10)	(lbf ⋅ in)
			Coop atula TO 220AB 21	25TTS08	
Marking device			Case style TO-220AB 3L	25TTS12	

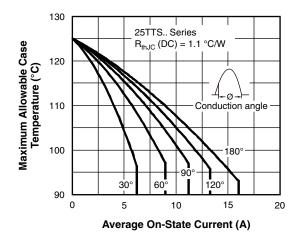


Fig. 1 - Current Rating Characteristics

-Ø-

Conduction period

DĊ

30

25

25TTS.. Series R<sub>thJC</sub> (DC) = 1.1 °C/W

120

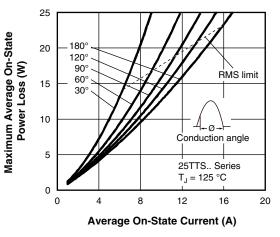
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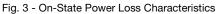
Average On-State Current (A)

Fig. 2 - Current Rating Characteristics

180

20





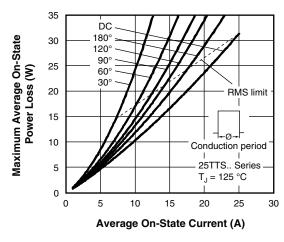


Fig. 4 - On-State Power Loss Characteristics

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130

120

110

100

90

80

0

30

5

60

10

90

Maximum Allowable Case

Temperature (°C)

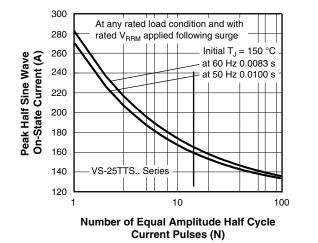
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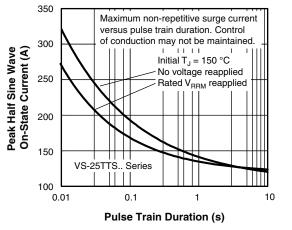
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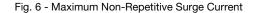


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Fig. 5 - Maximum Non-Repetitive Surge Current





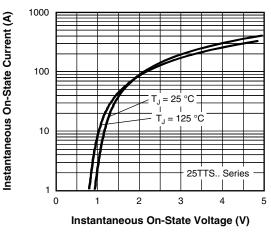
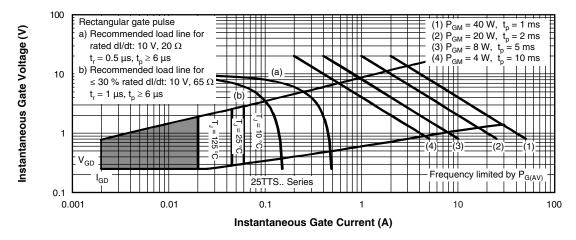
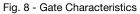


Fig. 7 - On-State Voltage Drop Characteristics

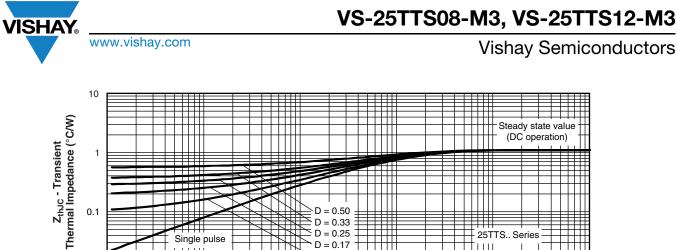


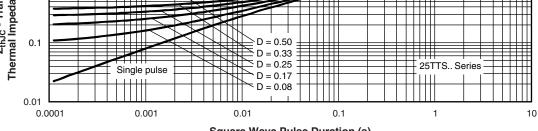


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Square Wave Pulse Duration (s)

Fig. 9 - Thermal Impedance ZthJC Characteristics

### **ORDERING INFORMATION TABLE**

1 2 3 4 5 6 7 1 - Vishay Semiconductors product 2 - Current rating $(25 = 25 \text{ A})$ 3 - Circuit configuration: T = single thyristor 4 - Package: T = TO-220AB 5 - Type of silicon: S = standard recovery rectifier 08 = 800 V	evice code	ode VS-	25 T	T S	12	-МЗ	
<ul> <li>Current rating (25 = 25 A)</li> <li>Circuit configuration: T = single thyristor</li> <li>Package: T = TO-220AB</li> <li>Type of silicon: S = standard recovery rectifier</li> </ul>		1	2 3	4 5	6	7	
6         -         Voltage rating         12 = 1200 V           7         -         Environmental digit:         12 = 1200 V		2 - 3 - 4 - 5 - 6 -	Current ratii Circuit confi T = single th Package: T = TO-220 Type of silic S = standar Voltage rati	ng (25 = 25 A) iguration: hyristor AB con: d recovery recting			

ORDERING INFORMATION (Example)						
PREFERRED P/N         BASE QUANTITY         PACKAGING DESCRIPTION						
VS-25TTS08-M3	50	Antistatic plastic tubes				
VS-25TTS12-M3	50	Antistatic plastic tubes				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96154			
Part marking information	www.vishay.com/doc?95028			

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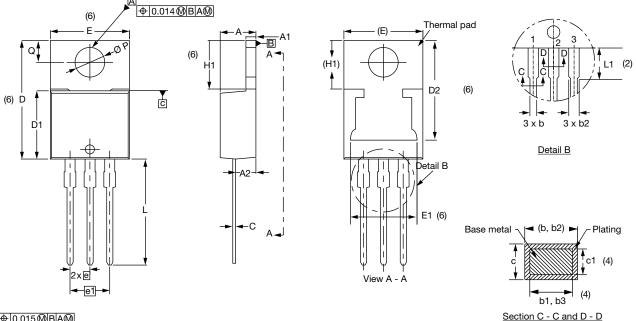
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### **TO-220AB 3L**

#### **DIMENSIONS** in millimeters and inches

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⊕0.015@BA@



SYMBOL	MILLIN	MILLIMETERS INCHES		HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.50	2.92	0.098	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.35	0.585	0.604	3
D1	8.38	9.02	0.330	0.355	

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
D2	11.68	13.30	0.460	0.524	6, 7
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

Conforms to JEDEC<sup>®</sup> outline TO-220AB

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- <sup>(5)</sup> Controlling dimensions: inches
- <sup>(6)</sup> Thermal pad contour optional within dimensions E, H1, D2, and E1
- <sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> TO-220, except D2

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