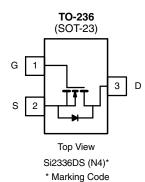


**Vishay Siliconix** 

## N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
	0.042 at V <sub>GS</sub> = 4.5 V	5.2				
30	0.046 at V <sub>GS</sub> = 2.5 V	4.9	5.7 nC			
	0.052 at V <sub>GS</sub> = 1.8 V	4.1				



**FEATURES** 

- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- DC/DC Converters
- **Boost Converters**



HALOGEN FREE

# GO S

N-Channel MOSFET

D

**Ordering Information:** 

Si2336DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ , unless otherwise noted)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30	v	
Gate-Source Voltage		V <sub>GS</sub>	± 8	v	
	T <sub>C</sub> = 25 °C		5.2		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 70 °C	- I <sub>D</sub>	4.1		
	T <sub>A</sub> = 25 °C		4.3 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		3.5 <sup>b, c</sup>	А	
Pulsed Drain Current		I <sub>DM</sub>	20		
	T <sub>C</sub> = 25 °C		1.5		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	۱ <sub>S</sub>	1 <sup>b, c</sup>		
	T <sub>C</sub> = 25 °C		1.8		
Maximum Dawar Dissinction	T <sub>C</sub> = 70 °C	P <sub>D</sub>	1.1	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C		1.25 <sup>b, c</sup>	V	
	T <sub>A</sub> = 70 °C		0.8 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	
Soldering Recommendations (Peak Tempera	ature) <sup>d, e</sup>		260		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 5 s	R <sub>thJA</sub>	80	100	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	55	70	0/W		

Notes:

a.  $T_C = 25 \ ^{\circ}C.$ 

b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 130 °C/W.

For technical questions, contact: pmostechsupport@vishay.com

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### Vishay Siliconix



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	1			1	<u> </u>	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	30			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$			31		m\//º(
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μΑ		- 2.7		mV/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	0.4		1	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA
Zara Cata Valtaga Drain Current	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ
Zero Gate Voltage Drain Current		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, \text{ T}_{\text{J}} = 55 ^{\circ}\text{C}$			10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	10			A
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 3.8 \text{ A}$		0.034	0.042	Ω
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 3.6 \text{ A}$		0.038	0.046	
		$V_{GS} = 1.8 \text{ V}, \text{ I}_{D} = 2 \text{ A}$		0.041	0.052	1
Forward Transconductance <sup>a</sup>				30		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			560		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		60		
Reverse Transfer Capacitance	C <sub>rss</sub>			27		
	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 8 \text{ V}, I_D = 3.4 \text{ A}$	10		15	
Total Gate Charge		V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 3.4 A		5.7	8.6	nC
Gate-Source Charge	Q <sub>gs</sub>			0.85		
Gate-Drain Charge	Q <sub>gd</sub>			0.75		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.6	3	6	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			6	12	
Rise Time	t <sub>r</sub>	$V_{DD} = 15 \text{ V}, \text{ R}_1 = 4.3 \Omega$		10	20	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 3.5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		20	40	
Fall Time	t <sub>f</sub>	_		10	20	
Turn-On Delay Time	t <sub>d(on)</sub>			5	10	ns
Rise Time	t <sub>r</sub>	$V_{DD} = 15 \text{ V}, \text{ R}_{\text{I}} = 4.3 \Omega$		10	20	-
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 3.5 \text{ A}, V_{GEN} = 8 \text{ V}, \text{ R}_g = 1 \Omega$		17	30	
Fall Time	t <sub>f</sub>	, , , , , , , , , , , , , , , , , , ,		10	20	1
Drain-Source Body Diode Characteristic				1	I	
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			1.5	
Pulse Diode Forward Current	I <sub>SM</sub>	-		1	20	A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 3.5 A, V <sub>GS</sub> = 0 V		0.8	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			15	30	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			6	12	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 3.5 \text{ A}, \text{ dl/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 ^\circ\text{C}$		8		-
Reverse Recovery Rise Time	t <sub>b</sub>			7		ns

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %

b. Guaranteed by design, not subject to production testing.

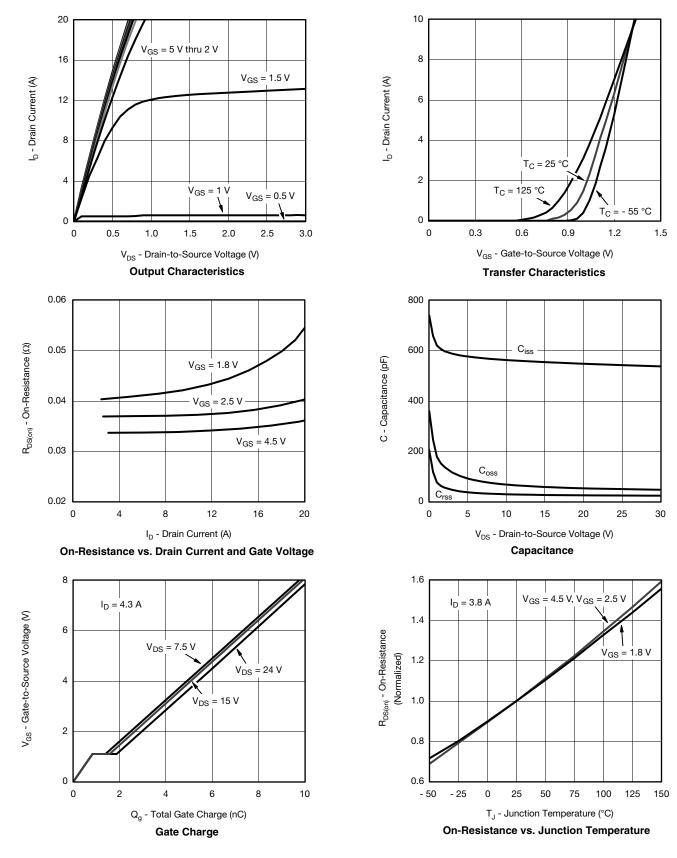
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



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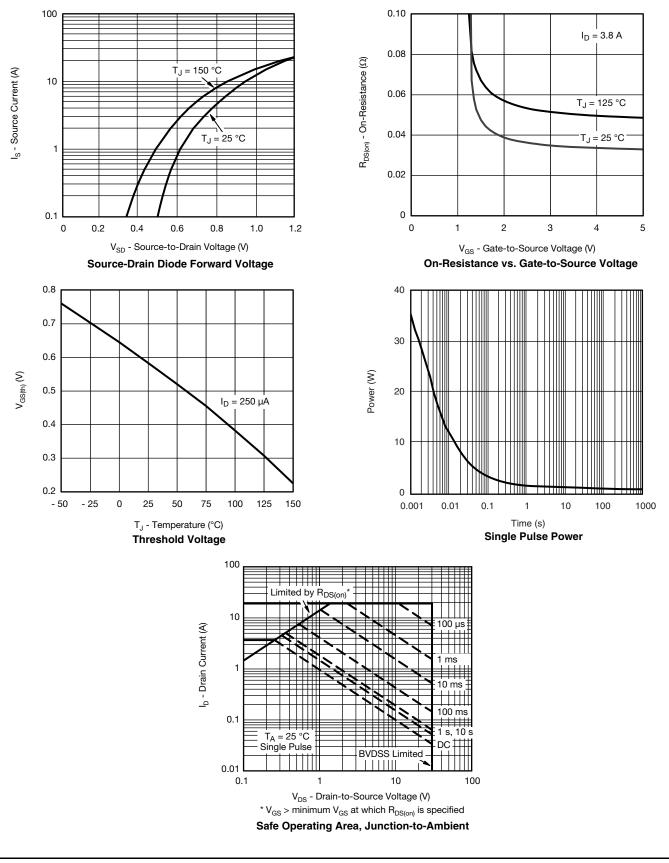
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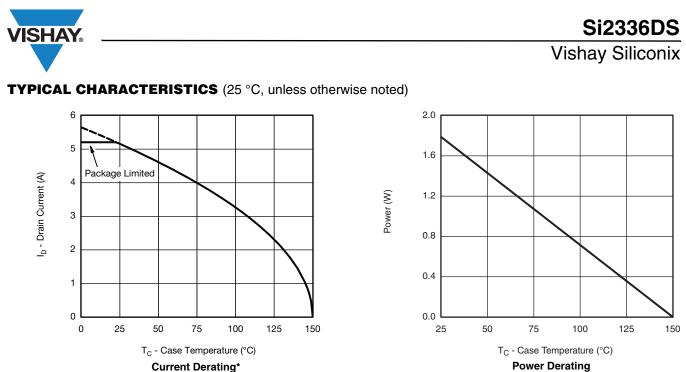


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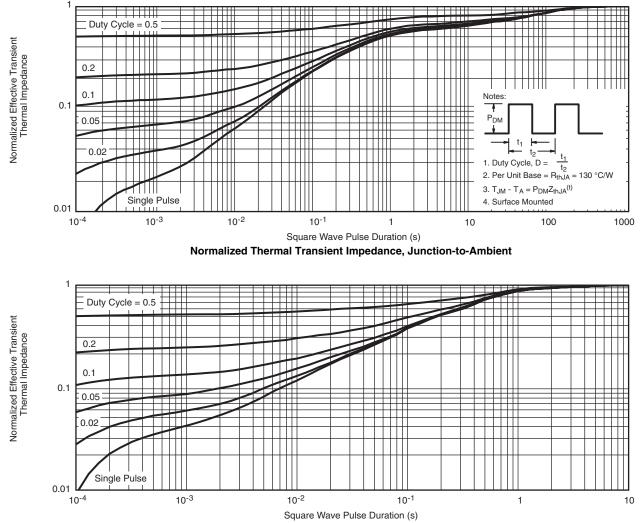
**Power Derating** 

\* The power dissipation  $P_D$  is based on  $T_{J(max.)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

#### Vishay Siliconix



#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?71978">www.vishay.com/ppg?71978</a>.

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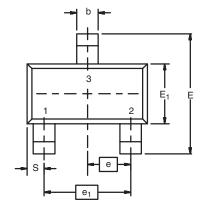
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## Package Information

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#### SOT-23 (TO-236): 3-LEAD







Dim	MILLIM	IETERS	INCHES		
	Min	Мах	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.02	025 Ref	
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K, 09- DWG: 5479	Jul-01				



# Application Note 826

Vishay Siliconix

#### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

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