

Half Bridge IGBT Power Module, 1200 V, 100 A



INT-A-PAK

PRODUCT SUMMARY						
V _{CES}	1200 V					
I _C at T _C = 80 °C	100 A					
V _{CE(on)} (typical) at I _C = 100 A, 25 °C	1.90 V					
Speed	8 kHz to 30 kHz					
Package	INT-A-PAK					
Circuit	Half bridge					

FEATURES

- Low V_{CE(sat)} trench IGBT technology
- 10 µs short circuit capability



- V_{CE(sat)} with positive temperature coefficient
- Maximum junction temperature 175 °C
- · Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- UPS (Uninterruptable Power Supply)
- Inverter for motor drive
- · AC and DC servo drive amplifier

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Gate to emitter voltage	V _{GES}		± 30	V	
Collector current	Ic	T _C = 25 °C	180		
Collector current		T _C = 80 °C	100		
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	200	Α	
Diode continuous forward current	I _F	T _C = 80 °C	100		
Diode maximum forward current	I _{FM} ⁽¹⁾	$t_p = 1 \text{ ms}$	200		
Maximum power dissipation	P _D	T _J = 175 °C	652	W	
Short circuit withstand time	t _{SC}	T _C = 125 °C	10	μs	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	4000	V	

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature.

IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS MIN. TYP. M			MAX.	UNITS	
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-		
Collector to emitter saturation voltage		V _{GE} = 15 V, I _C = 100 A, T _J = 25 °C	-	1.90	2.35] ,,	
Collector to enfitter saturation voltage	V _{CE(sat)}	V _{GE} = 15 V, I _C = 100 A, T _J = 175 °C	-	2.50	-] v	
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 5.0$ mA, $T_J = 25$ °C	5.0	5.9	7.5		
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA	
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA	



SWITCHING CHARACTERISTICS	3					
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS			MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	187	-	ns
Rise time	t _r		-	57	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 100 \text{ A}, R_{g} = 5.6 \Omega,$	-	180	-	
Fall time	t _f	$\begin{array}{c} \mbox$	-	149	-	
Turn-on switching loss	E _{on}		-	4.97	-	- mJ
Turn-off switching loss	E _{off}		-	4.69	-	
Turn-on delay time	t _{d(on)}		-	189	-	- ns
Rise time	t _r	V_{CC} = 600 V, I_{C} = 100 A, R_{g} = 5.6 Ω , V_{GE} = ± 15 V, T_{J} = 125 °C	-	58	-	
Turn-off delay time	t _{d(off)}		-	187	-	
Fall time	t _f		-	220	-	
Turn-on switching loss	E _{on}		-	7.80	-	I
Turn-off switching loss	E _{off}		-	5.85	-	- mJ
Input capacitance	C _{ies}		-	12.8	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 30 V, f = 1.0 MHz	-	0.46	-	nF
Reverse transfer capacitance	C _{res}		-	0.32	-	
SC data	I _{SC}	$t_p \leq 10~\mu s,~V_{GE} = 15~V,~T_J = 125~^{\circ}C,\\ V_{CC} = 900~V,~V_{CEM} \leq 1200~V$	-	890	-	Α
Stray inductance	L _{CE}		-	-	30	nΗ
Module lead resistance, terminal to chip	R _{CC'+EE'}		-	0.75	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Forward voltage		I _F = 100 A	$T_J = 25 ^{\circ}C$	ı	1.82	2.20	V
Torward voltage	V_{F}		T _J = 125 °C	-	1.95	-	
Davis and an annual shares	Q _{rr}	$I_F = 100 \text{ A}, V_R = 600 \text{ V},$ $R_G = 5.6 \Omega$ $V_{GE} = -15 \text{ V}$	T _J = 25 °C	-	8.1	-	μC
Reverse recovery charge			T _J = 125 °C	-	14.0	-	
Dook reverse receivery current	I _{rr}		$T_J = 25 ^{\circ}C$	-	81	-	Α
Peak reverse recovery current			T _J = 125 °C	-	98	-	
Davieres resources energy	E _{rec}		T _J = 25 °C	-	2.99	-	mJ
Reverse recovery energy			T _J = 125 °C	ı	4.85	-	IIIJ

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction temperature		TJ		-	-	175	°C
Storage temperature range		T _{Stg}		-40	-	125	°C
Junction to case	IGBT	R _{thJC} -		-	-	0.23	
per ½ module	Diode			-	-	0.36	K/W
Case to sink (Conductive greas	e applied)	R _{thCS}		-	0.05	-	
Mounting torque			Power terminal screw: M5	2.5 to 5.0		- Nm	
			Mounting screw: M6		3.0 to 5.0		
Weight			Weight of module	-	150	-	g



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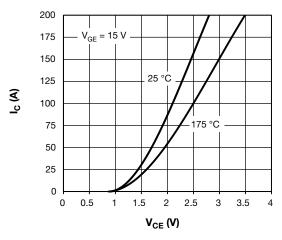


Fig. 1 - IGBT Typical Output Characteristics

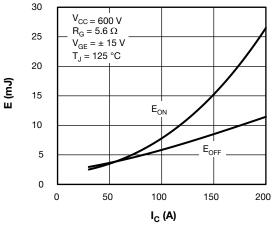


Fig. 3 - IGBT Switching Loss vs. I_C

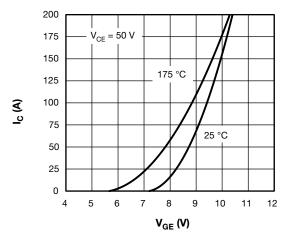


Fig. 2 - IGBT Transfer Characteristics

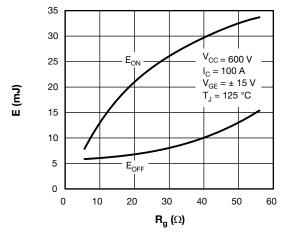
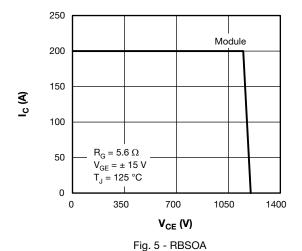


Fig. 4 - IGBT Switching Loss vs. R_G



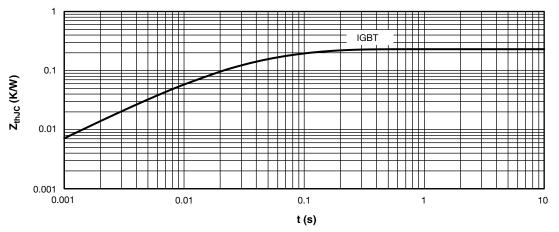
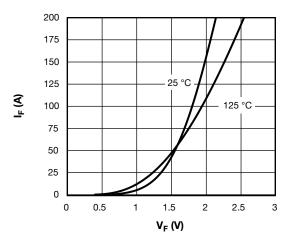


Fig. 6 - IGBT Transient Thermal Impedance





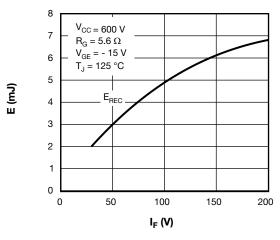


Fig. 8 - Diode Switching Loss vs. I_F

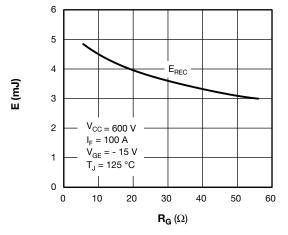


Fig. 9 - Diode Switching Loss vs. $\ensuremath{R_{G}}$

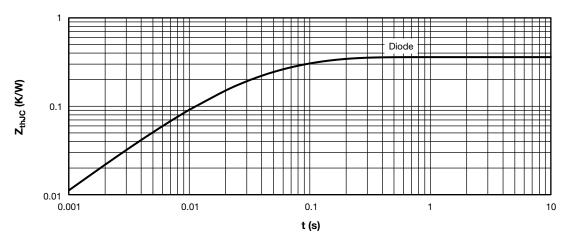
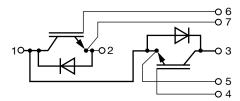


Fig. 10 - Diode Transient Thermal Impedance

CIRCUIT CONFIGURATION

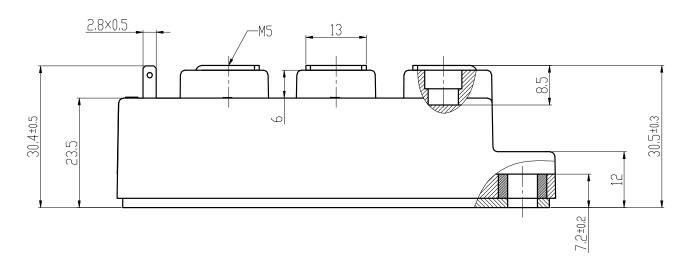


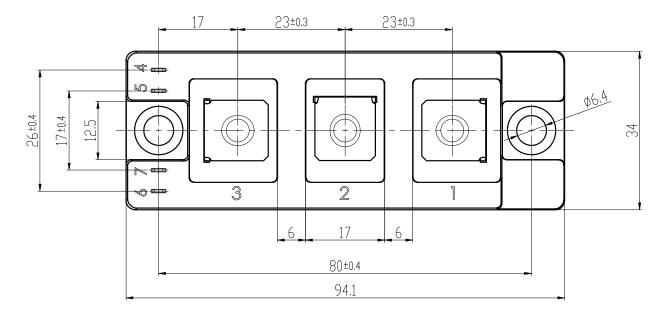
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95524			



INT-A-PAK

DIMENSIONS in millimeters (inches)





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