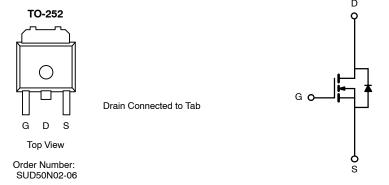


# N-Channel 20-V (D-S), 175°C MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$r_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>a, b</sup>		
20	0.006 @ V <sub>GS</sub> = 4.5 V	30		
	0.009 @ V <sub>GS</sub> = 2.5 V	25		

#### **FEATURES**

- TrenchFET® Power MOSFET
- 175°C Maximum Junction Temperature
- 100% R<sub>g</sub> Tested



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = $25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	20	V		
Gate-Source Voltage		V <sub>GS</sub>	±12			
	T <sub>A</sub> = 25°C	I <sub>D</sub>	30			
Continuous Drain Current <sup>a, b</sup>	T <sub>A</sub> = 100°C		21			
Pulsed Drain Current		I <sub>DM</sub>	100	Α		
Continuous Source Current (Diode Conduction) <sup>a, b</sup>		I <sub>S</sub>	30			
Mayimum Dayay Dissination	T <sub>C</sub> = 25°C	В	100	w		
Maximum Power Dissipation	T <sub>A</sub> = 25°C	- P <sub>D</sub>	8.3a, b	vv		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	$t \le 10$ sec.		15	18		
Maximum Junction-to-Ambienta	Steady State	R <sub>thJA</sub>	40	50	°C/W	
Maximum Junction-to-Case		R <sub>thJC</sub>	1.2	1.5		

#### Notes

a. Surface Mounted on 1" x 1" FR4 Board

 $b. \quad t \leq 10 \ \text{sec}.$ 

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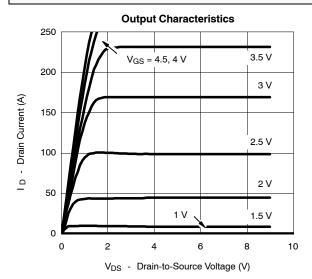
Parameter	Symbol	Test Condition	Min	Typ <sup>a</sup>	Max	Unit
Static	1		1			
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS}$ = 0 V, $I_D$ = 250 $\mu A$	20			.,
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.6			V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = $\pm$ 12 V			±100	nA
		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ
Zero Gate Voltage Drain Current	DSS	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125^{\circ}\text{C}$			50	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	50			Α
		$V_{GS} = 4.5 \text{ V}, I_D = 30 \text{ A}$			0.006	
Drain-Source On-State Resistance <sup>b</sup>	r <sub>DS(on)</sub>	$V_{GS}$ = 4.5 V, $I_{D}$ =30 A, $T_{J}$ = 125°C			0.009	Ω
		$V_{GS} = 2.5 \text{ V}, I_D = 20 \text{ A}$			0.009	
Forward Transconductanceb	9 <sub>fs</sub>	$V_{DS} = 5 \text{ V}, I_{D} = 30 \text{ A}$	20			S
Dynamic <sup>a</sup>			•	•		•
Input Capacitance	C <sub>iss</sub>			6600		pF
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 20 V, f = 1 MHz		1150		
Reverse Transfer Capacitance	C <sub>rss</sub>			600		
Total Gate Charge <sup>c</sup>	Qg			65	130	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS}$ = 10 V, $V_{GS}$ = 4.5 V, $I_{D}$ = 50 A		13		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			14		
Gate Resistance	R <sub>g</sub>		1		3.1	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			25	40	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 10 \text{ V. R}_1 = 0.2 \Omega$		120	180	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$\begin{aligned} V_{DD} &= 10 \text{ V},  R_L = 0.2  \Omega \\ I_D &\cong 50 \text{ A},  V_{GEN} = 4.5 \text{ V},  R_G = 2.5  \Omega \end{aligned}$		80	120	ns
Fall Time <sup>c</sup>	t <sub>f</sub>			100	150	
Source-Drain Diode Ratings an	d Characteristi	c (T <sub>C</sub> = 25°C)	•	•		
Pulsed Current	I <sub>SM</sub>				100	А
Diode Forward Voltage <sup>b</sup>	V <sub>SD</sub>	I <sub>F</sub> = 100 A, V <sub>GS</sub> = 0 V		1.2	1.5	V
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 50 A, di/dt = 100 A/μs		45	100	ns

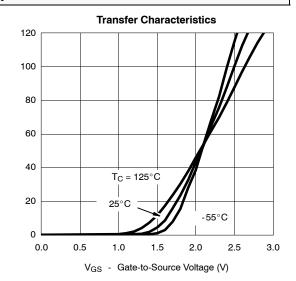
- Notes a. Guaranteed by design, not subject to production testing. b. Pulse test; pulse width  $\leq 300~\mu s$ , duty cycle  $\leq 2\%$ . c. Independent of operating temperature.



# **Vishay Siliconix**

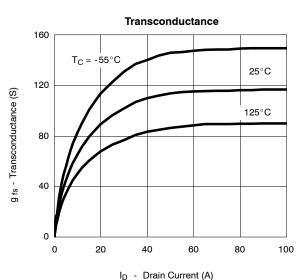
### TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

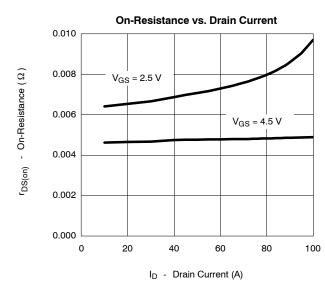


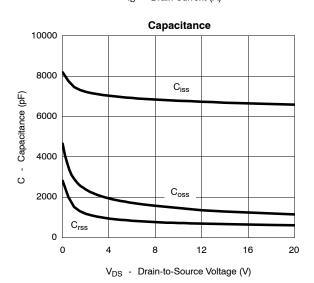


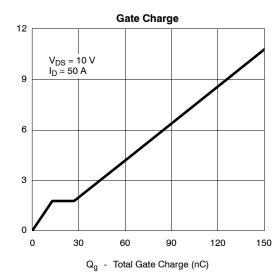
I D - Drain Current (A)

VGS - Gate-to-Source Voltage (V)







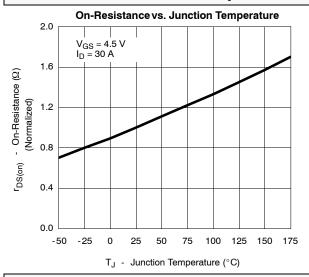


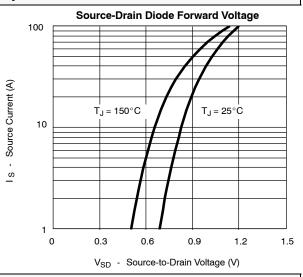
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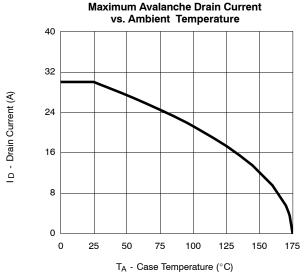


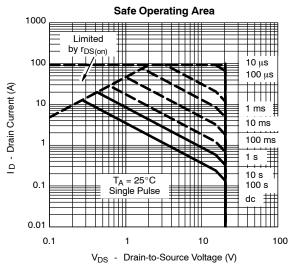
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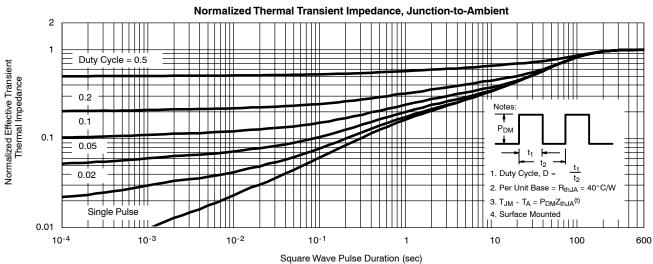




#### **THERMAL RATINGS**









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