

"Low Side Chopper" IGBT SOT-227 (Trench IGBT), 100 A



SOT-227

PRODUCT SUMMARY						
V _{CES}	1200 V					
I _C DC	100 A at 71 °C					
V _{CE(on)} typical at 100 A, 25 °C	2.36 V					
Speed	8 kHz to 30 kHz					
Package	SOT-227					
Circuit	Chopper low side switch					

FEATURES

- Trench IGBT technology
- Very low V_{CE(on)}
- Square RBSOA
- HEXFRED® clamping diode
- 10 µs short circuit capability
- · Fully isolated package
- Very low internal inductance (≤ 5 nH typical)
- Industry standard outline
- UL approved file E78996
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

BENEFITS

- · Designed for increased operating efficiency in power conversion: UPS, SMPS, welding, induction heating
- Easy to assemble and parallel
- · Direct mounting on heatsink
- Plug-in compatible with other SOT-227 packages
- · Low EMI, requires less snubbing

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V _{CES}		1200	V
Continuous collector current	1-	T _C = 25 °C	134	
Continuous conector current	I _C	T _C = 80 °C	92	
Pulsed collector current	I _{CM}		270	A
Clamped inductive load current	I _{LM}		270	A
Diede vertiere en fan een de verd		T _C = 25 °C	87	
Diode continuous forward current	I _F	T _C = 80 °C	59	
Gate to emitter voltage	V_{GE}		± 20	V
D. III I IODT		T _C = 25 °C	463	
Power dissipation, IGBT	P _D	T _C = 80 °C	260	W
	В	T _C = 25 °C	338	VV
Power dissipation, diode	P _D	T _C = 80 °C	190	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{BR(CES)}	V _{GE} = 0 V, I _C = 1 mA	1200	-	-		
		V _{GE} = 15 V, I _C = 50 A	-	1.79	2.33		
Collector to amittan valtage	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	V _{GE} = 15 V, I _C = 100 A	-	2.36	2.85	V	
Collector to emitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 50 A, T _J = 125 °C	-	2.05	2.62		
		V _{GE} = 15 V, I _C = 100 A, T _J = 125 °C	-	2.8	3.42		
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}, I_C = 500 \mu\text{A}$ 5		5.8	7		
Temperature coefficient of threshold voltage	V _{GE(th)} /ΔT _J	$V_{CE} = V_{GE}$, $I_{C} = 1$ mA (25 °C to 125 °C)	-	-15.6	-	mV/°C	
Collector to emitter leakage aurrent		V _{GE} = 0 V, V _{CE} = 1200 V	-	0.5	100	μΑ	
Collector to emitter leakage current	I _{CES}	V _{GE} = 0 V, V _{CE} = 1200 V, T _J = 125 °C	-	0.052	2	mA	
Diode reverse breakdown voltage	V _{BR}	I _R = 1 mA	1200	-	-	V	
	, ,	I _C = 50 A, V _{GE} = 0 V	-	2.53	3.55	V	
Diada fanyard valtaga dran		I _C = 100 A, V _{GE} = 0 V	-	3.32	4.35		
Diode forward voltage drop V _{FN}	V _{FM}	I _C = 50 A, V _{GE} = 0 V, T _J = 125 °C	-	2.66	3.70	V	
		I _C = 100 A, V _{GE} = 0 V, T _J = 125 °C	-	3.70	4.50	1	
Diada varrana laakaga arrimant		V _R = V _R rated	-	4	50	μΑ	
Diode reverse leakage current	I _{RM}	T _J = 125 °C, V _R = V _R rated	-	0.6	3	mA	
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 200	nA	

PARAMETER	SYMBOL	= 25 °C unless otherwise specified) TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		TEST CONDIT	110113			IVIAA.	UNITS
Total gate charge (turn-on)	Qg			-	400	-	
Gate to emitter charge (turn-on)	Q _{ge}	$I_C = 100 \text{ A}, V_{CC} = 600 \text{ V},$	$I_C = 100 \text{ A}, V_{CC} = 600 \text{ V}, V_{GE} = 15 \text{ V}$		120	-	nC
Gate to collector charge (turn-on)	Q_{gc}			=	170	-	
Turn-on switching loss	E _{on}	I _C = 100 A, V _{CC} = 600 V,		-	21.9	-	
Turn-off switching loss	E _{off}	$V_{GE} = 15 \text{ V}, R_q = 5 \Omega,$		-	5.48	-	- mJ
Total switching loss	E _{tot}	$L = 500 \mu H, T_{J} = 25 °C$		-	27.38	-	
Turn-on switching loss	E _{on}			-	23.6	-	
Turn-off switching loss	E _{off}		Energy losses include tail and	-	7.65	-	
Total switching loss	E _{tot}	$\begin{split} &I_{C} = 100 \text{ A, V}_{CC} = 600 \text{ V,} \\ &V_{GE} = 15 \text{ V, R}_{g} = 5 \Omega, \\ &L = 500 \text{ \muH, T}_{J} = 125 \text{ °C} \end{split}$	diode recovery (see fig. 18)	-	31.25	-	
Turn-on delay time	t _{d(on)}			-	195	-	ns
Rise time	t _r			-	259	-	
Turn-off delay time	t _{d(off)}			-	188	-	
Fall time	t _f			-	212	-	
Reverse bias safe operating area	RBSOA	T_J = 150 °C, I_C = 270 A, R_g = 22 Ω , V_{GE} = 15 V to 0 V, V_{CC} = 900 V, V_P = 1200 V		Fullsquare			
Short circuit safe operating area	SCSOA	T_J = 150 °C, R_g = 22 Ω , V_{GE} = 15 V to 0 V, V_{CC} = 900 V, V_P = 1200 V			10		μѕ
Diode reverse recovery time	t _{rr}			-	129	161	ns
Diode peak reverse current	I _{rr}	I _F = 50 A, dI _F /dt = 200 A/μs, V _R = 200 V			11	14	Α
Diode recovery charge	Q _{rr}				700	1046	nC
Diode reverse recovery time	t _{rr}			-	208	257	ns
Diode peak reverse current	I _{rr}	I _F = 50 A, dI _F /dt = 200 A/μs, V _R = 200 V, T _J = 125 °C		-	17	21	Α
Diode recovery charge	Q _{rr}			_	1768	2698	nC



THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL		MIN.	TYP.	MAX.	UNITS
Junction and storage temperature range	T _J , T _{Stg}		-40	-	150	°C
Junction to case	- R _{thJC}		-	-	0.27	
Diode			-	-	0.37	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque			-	-	1.3	Nm
Case style		SOT-227	•			

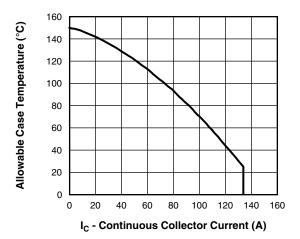


Fig. 1 - Maximum DC IGBT Collector Current vs.

Case Temperature

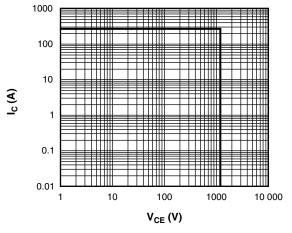


Fig. 2 - IGBT Reverse Bias SOA $T_J = 150 \, ^{\circ}\text{C}, \, V_{GE} = 15 \, \text{V}$

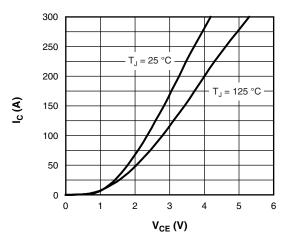


Fig. 3 - Typical IGBT Collector Current Characteristics

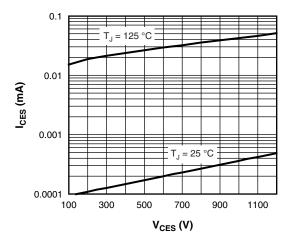


Fig. 4 - Typical IGBT Zero Gate Voltage Collector Current



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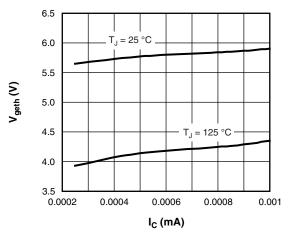


Fig. 5 - Typical IGBT Threshold Voltage

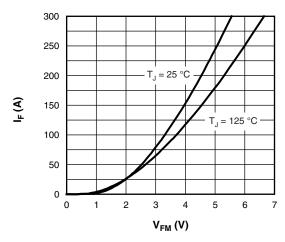


Fig. 8 - Typical Diode Forward Characteristics

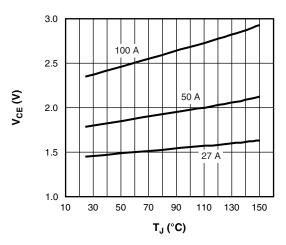


Fig. 6 - Typical IGBT Collector to Emitter Voltage vs. Junction Temperature, $V_{GE} = 15 \text{ V}$

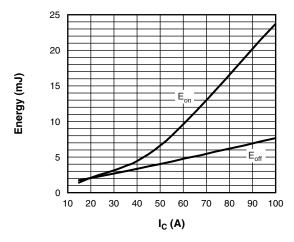


Fig. 9 - Typical IGBT Energy Loss vs. I_C T_J = 125 °C, L = 500 μ H, V_{CC} = 600 V, R_q = 5 Ω , V_{GE} = 15 V

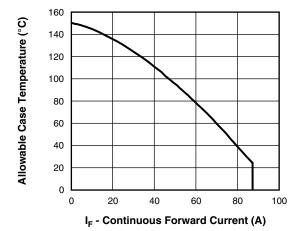


Fig. 7 - Maximum DC Forward Current vs.
Case Temperature

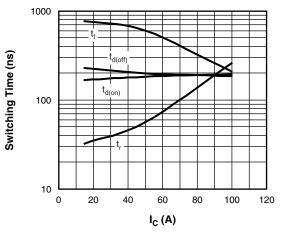


Fig. 10 - Typical IGBT Switching Time vs. I_C T_J = 125 °C, L = 500 μ H, V_{CC} = 600 V, R_q = 5 Ω , V_{GE} = 15 V



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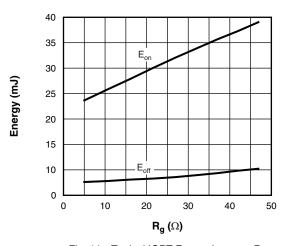


Fig. 11 - Typical IGBT Energy Loss vs. R_g T_J = 125 °C, I_C = 100 A, L = 500 μ H, V_{CC} = 600 V, V_{GE} = 15 V

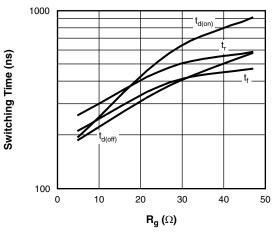


Fig. 12 - Typical IGBT Switching Time vs. R_g T_J = 125 °C, L = 500 μ H, V_{CC} = 600 V, I_C = 100 A, V_{GE} = 15 V

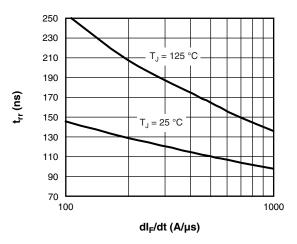


Fig. 13 - Typical t_{rr} Diode vs. dI_F/dt $V_R = 200$ V, $I_F = 50$ A

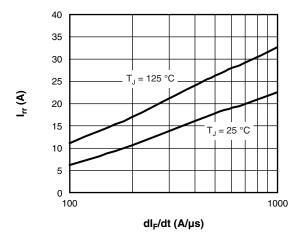


Fig. 14 - Typical I $_{\rm rr}$ Diode vs. dI $_{\rm F}$ /dt V $_{\rm R}$ = 200 V, I $_{\rm F}$ = 50 A

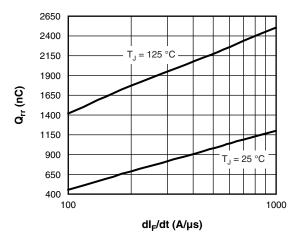


Fig. 15 - Typical Q_{rr} Diode vs. dI_F/dt $V_R = 200 \text{ V}, I_F = 50 \text{ A}$

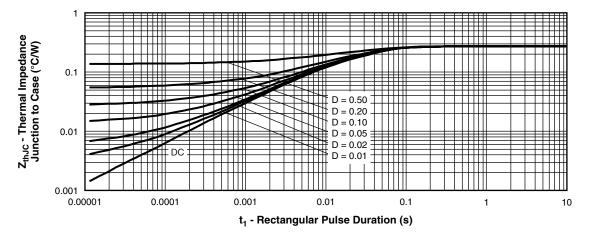


Fig. 16 - Maximum Thermal Impedance Z_{thJC} Characteristics (IGBT)

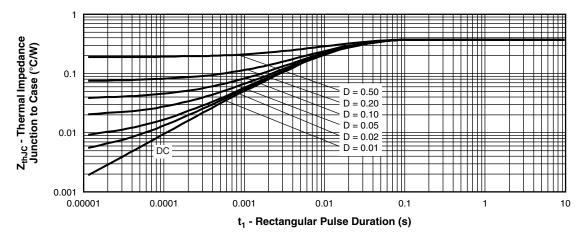
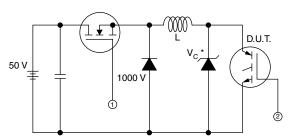


Fig. 17 - Maximum Thermal Impedance Z_{thJC} Characteristics (Diode)



- * Driver same type as D.U.T.; V $_{C}$ = 80 % of V $_{ce(max)}$ * Note: Due to the 50 V power supply, pulse width and inductor will increase to obtain Id

Fig. 18a - Clamped Inductive Load Test Circuit

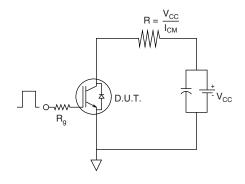


Fig. 18b - Pulsed Collector Current Test Circuit

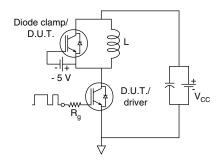


Fig. 19a - Switching Loss Test Circuit

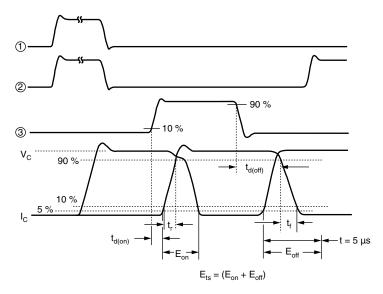
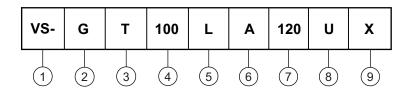


Fig. 19b - Switching Loss Waveforms Test Circuit

ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

Insulated Gate Bipolar Transistor (IGBT)

3 - T = Trench IGBT

Current rating (100 = 100 A)

5 - Circuit configuration (L = Low side chopper)

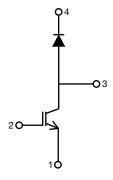
Package indicator (A = SOT-227)

Voltage rating (120 = 1200 V)

Speed/type (U = Ultrafast IGBT)

9 - Diode (X = HEXFRED®)

CIRCUIT CONFIGURATION

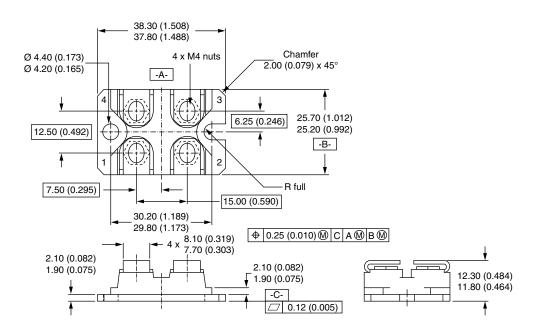


LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95036				
Packaging information	www.vishay.com/doc?95037			



SOT-227

DIMENSIONS in millimeters (inches)



Notes

- Dimensioning and tolerancing per ANSI Y14.5M-1982
- · Controlling dimension: millimeter

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