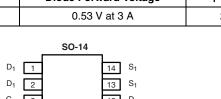




Dual N-Channel 30-V (D-S) MOSFET with Schottky Diode

PRODUCT SUMMARY							
	V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)				
Channel-1 Channel-2	30	0.011 at V _{GS} = 10 V	10				
		0.016 at V _{GS} = 4.5 V	8.2				
		0.0085 at V _{GS} = 10 V	14				
		0.0095 at V _{GS} = 4.5 V	13				

SCHOTTKY PRODUCT SUMMARY						
V _{DS} (V)	V _{SD} (V) Diode Forward Voltage	I _F (A)				
30	0.53 V at 3 A	2				



D₂

D₂

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Ordering Information: Si4310BDY-T1-E3 (Lead (Pb)-free)

Si4310BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

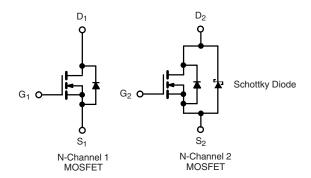
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC

Pb-free

ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

- DC/DC Converters
 - Game Stations
 - Video Equipment



ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted								
			Ch	nannel-1	Cł			
Parameter		Symbol	10 s	Steady State	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	30				V	
Gate-Source Voltage		V_{GS}		± 20		v		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I _D	10	7.5	14	9.8	А	
	T _A = 70 °C		8	6	11	7.8		
Pulsed Drain Current		I _{DM}	40		50		_ ^	
Continuous Source Current (Diode Conduction) ^a		I _S	1.8	1.04	2.73	1.33		
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	2	1.14	3.0	1.47	w	
	T _A = 70 °C		1.28	0.73	1.9	0.94	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150				°C	

THERMAL RESISTANCE RATINGS									
			Channel-1		Channel-2		Schottky		
Parameter		Symbol	Тур.	Max.	Тур.	Max.	Тур.	Max.	Unit
Manipulation to Application	t ≤ 10 s	R_{thJA}	53	62.5	34	35	40	48	
Maximum Junction-to-Ambient ^a	Steady State	' 'thJA	92	110	70	72	76	93	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	35	42	17	24	21	26	

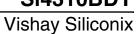
Notes:

a. Surface Mounted on 1" x 1" FR4 board.



MOSFET SPECIFICATIONS T _J = 25 °C, unless otherwise noted									
Parameter	Symbol	Test Conditions		Min.	Typ. ^a	Max.	Unit		
Static									
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	Ch-1	1.0		3.0	V		
date Threshold Voltage	* GS(th)	*DS = *GS, ·D = 200 f2.*	Ch-2	1.0		3.0			
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	Ch-1			100	nA		
, ,	400	20 00	Ch-2			100			
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$	Ch-1 Ch-2			100	μΑ		
Zero Gate Voltage Drain Current	I _{DSS}		Ch-1			15			
		V_{DS} = 30 V, V_{GS} = 0 V, T_{J} = 85 °C	Ch-2			4000			
			Ch-1	20		.000	_		
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	Ch-2	30			Α		
		V _{GS} = 10 V, I _D = 10 A	Ch-1		0.009	0.011			
	5	V _{GS} = 10 V, I _D = 14 A	Ch-2		0.0065	0.0085			
Drain-Source On-State Resistance ^b	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 8.2 \text{ A}$	Ch-1		0.013	0.016	Ω		
		$V_{GS} = 4.5 \text{ V}, I_D = 13 \text{ A}$	Ch-2		0.0075	0.0095			
		V _{DS} = 15 V, I _D = 10 A	Ch-1		30				
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 14 A	Ch-2		60		S		
	V _{SD}	I _S = 1.8 V, V _{GS} = 0 V	Ch-1		0.76	1.1	V		
Diode Forward Voltage ^b		I _S = 2.73 V, V _{GS} = 0 V	Ch-2		0.485	0.53			
Dynamic ^a		3 40	1						
•	_		Ch-1	790	1580	2370			
Input Capacitance	C _{iss}		Ch-2	1530	3060	4590			
Outrout Connections	_	V - 15 V V - 0 V f - 1 MHz	Ch-1	145	290	435			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	Ch-2	300	600	900	pF -		
Reverse Transfer Capacitance	C _{rss}		Ch-1	70	140	210			
Theverse Transier Capacitance	rss		Ch-2	115	225	340			
Total Gate Charge	Q_{g}	Channel-1	Ch-1		12	18			
	9	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$	Ch-2		19	30			
Gate-Source Charge	Q_{gs}	20 / GO / D	Ch-1		5.3		nC		
		Channel-2	Ch-2		10		_		
Gate-Drain Charge	Q_{gd}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 14 \text{ A}$	Ch-1 Ch-2		4.3 5				
			Ch-1	0.90	1.8	2.7			
Gate Resistance	R_{g}	f = 1 MHz	Ch-2	0.3	0.95	1.4	Ω		
			Ch-1	0.0	13	20			
Turn-On Delay Time	t _{d(on)}	Channel-1	Ch-2		17	26			
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 6 Ω	Ch-1		10	15			
			Ch-2		12	20			
Turn-Off Delay Time	t _{at/-10}	Channel-2	Ch-1		33	50	na		
Turn-Off Delay Time	t _{d(off)}	$V_{DD} = 15 \text{ V}, R_L = 15 \Omega$	Ch-2		53	80	ns		
Fall Time	t _f	$I_D\cong$ 1 A, V_{GEN} = 10 V, R_g = 6 Ω	Ch-1		10	15			
	7		Ch-2		17	26			
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 1.8 A, dl/dt = 100 A/μs	Ch-1		25	40			
		I _F = 2.73 V, dl/dt = 100 A/μs	Ch-2		31	50			

a. Guaranteed by design, not subject to production testing. b. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

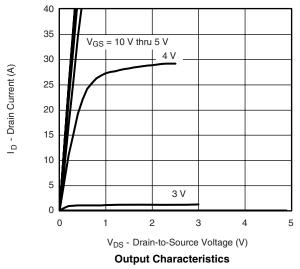


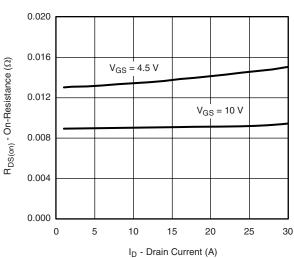


SCHOTTKY SPECIFICATIONS T _J = 25 °C, unless otherwise noted									
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit			
Forward voltage Drop	V _F	I _F = 3 A		0.485	0.53	V			
Forward voltage Drop	VF.	I _F = 3 A, T _J = 125 °C		0.42	0.42	V			
		V _R = 30 V		0.008	0.100				
Maximum Reverse Leakage Current	I _{rm}	V _R = 30 V, T _J = 75 °C		0.4	5	mA			
		V _R = 30 V, T _J = 125 °C		0.5	20				
Junction Capacitance	C _T	V _R = 15 V		102		pF			

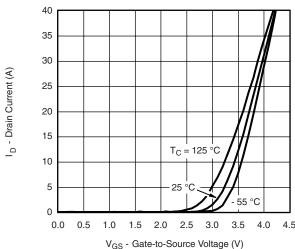
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

CHANNEL-1 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

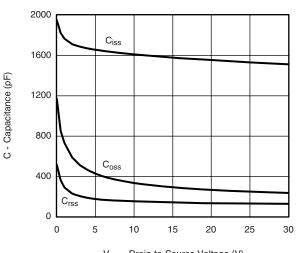




On-Resistance vs. Drain Current





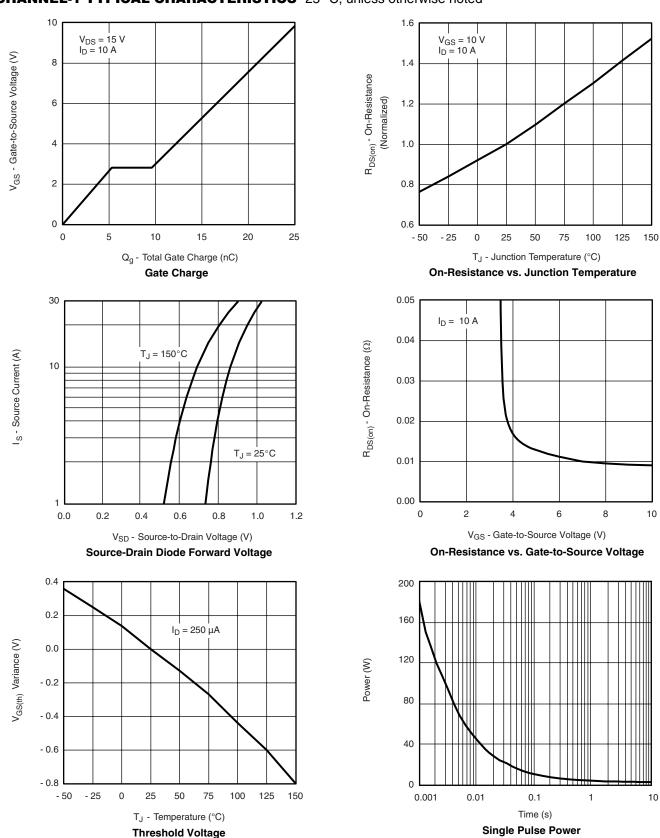


V_{DS} - Drain-to-Source Voltage (V)

Capacitance

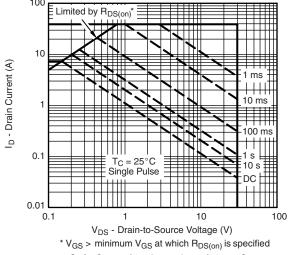
VISHAY

CHANNEL-1 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

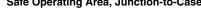


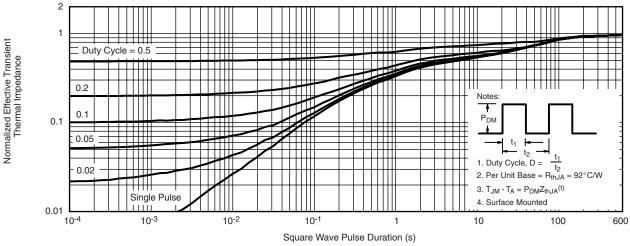


CHANNEL-1 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

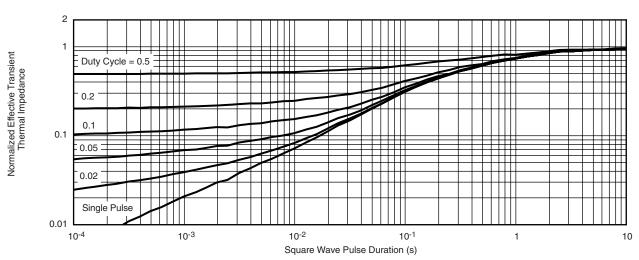


Safe Operating Area, Junction-to-Case





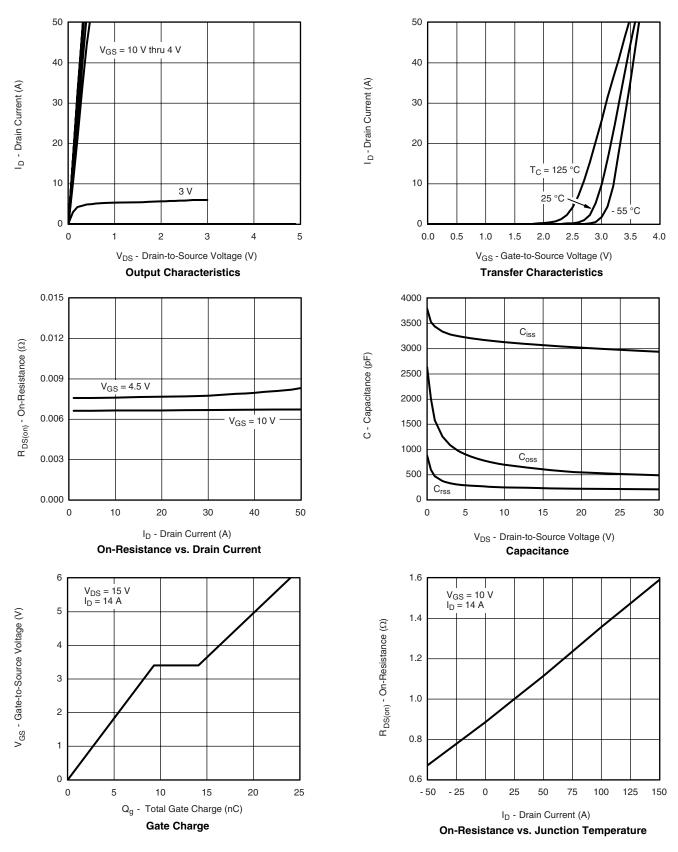
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

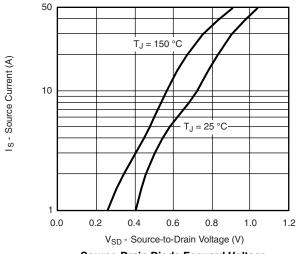
VISHAY

CHANNEL-2 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

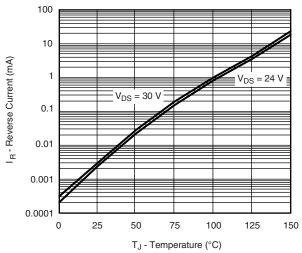




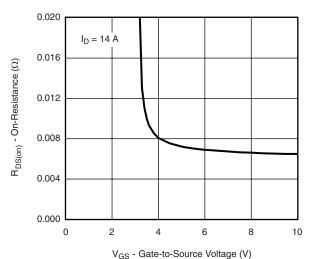
CHANNEL-2 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



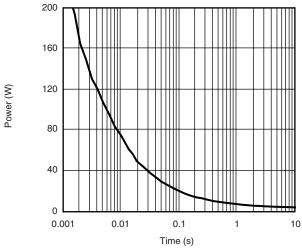
Source-Drain Diode Forward Voltage



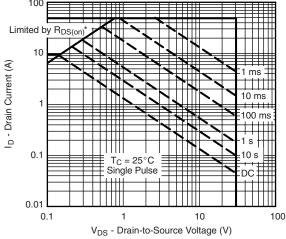
Reverse Current vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage



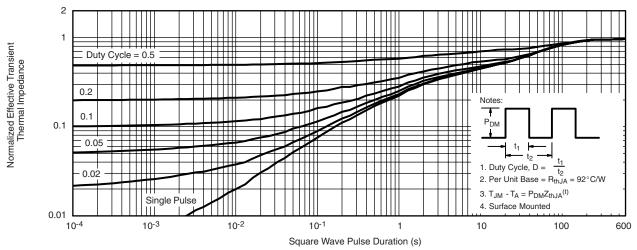
Single Pulse Power



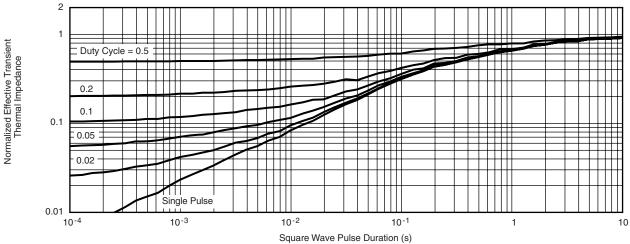
* $V_{GS} > \mbox{minimum } V_{GS}$ at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Case

CHANNEL-2 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

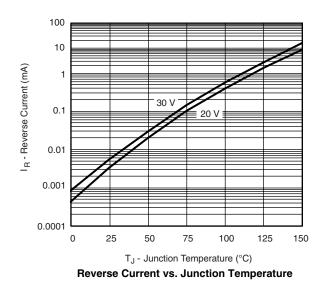


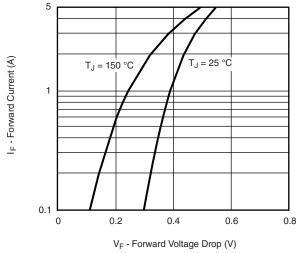
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

SCHOTTKY TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

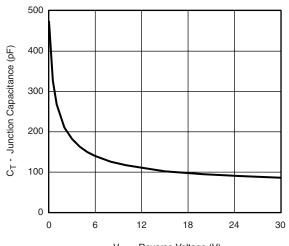




Forward Voltage Drop

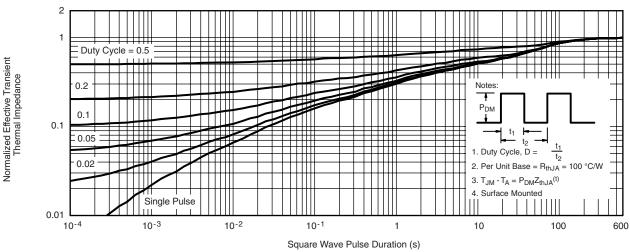


SCHOTTKY TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

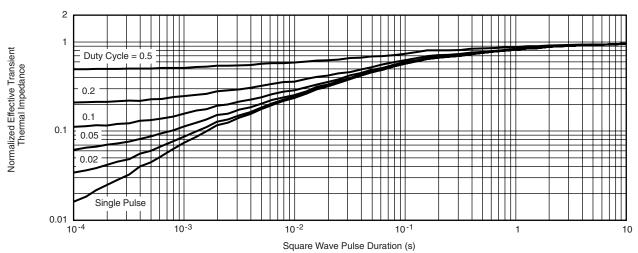


V_{KA} - Reverse Voltage (V)

Capacitance



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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