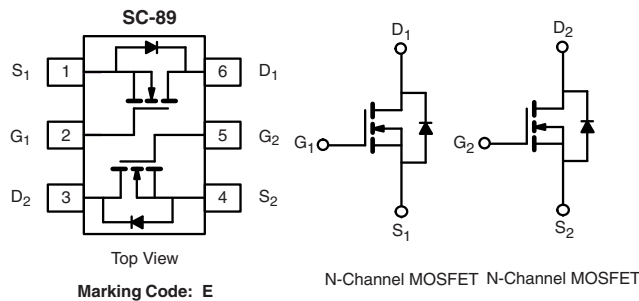


Automotive Dual N-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY	
V_{DS} (V)	60
$R_{DS(on)}$ (Ω) at $V_{GS} = 10$ V	1.4
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5$ V	2.0
I_D (A)	0.3
Configuration	Dual

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Compliant to RoHS Directive 2002/95/EC
- AEC-Q101 Qualified^c



ORDERING INFORMATION	
Package	SC-89
Lead (Pb)-free and Halogen-free	SQ1026X-T1-GE3

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	$T_A = 25$ °C	A
		$T_A = 125$ °C	
Continuous Source Current (Diode Conduction)	I_S	0.3	
Pulsed Drain Current ^a	I_{DM}	1.2	
Maximum Power Dissipation ^a	P_D	$T_A = 25$ °C	W
		$T_A = 125$ °C	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	R_{thJA}	500	°C/W

Notes

- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.

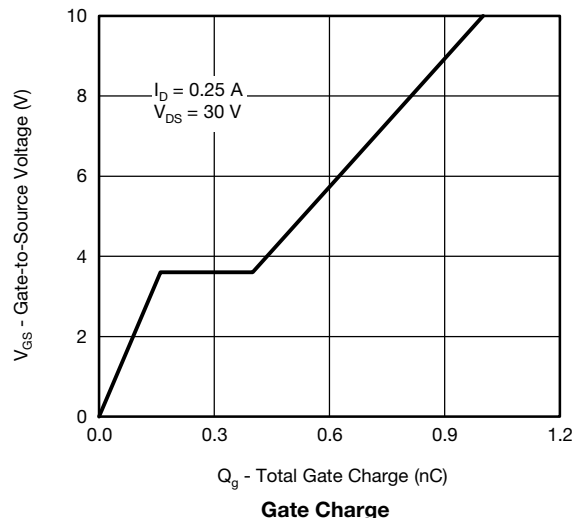
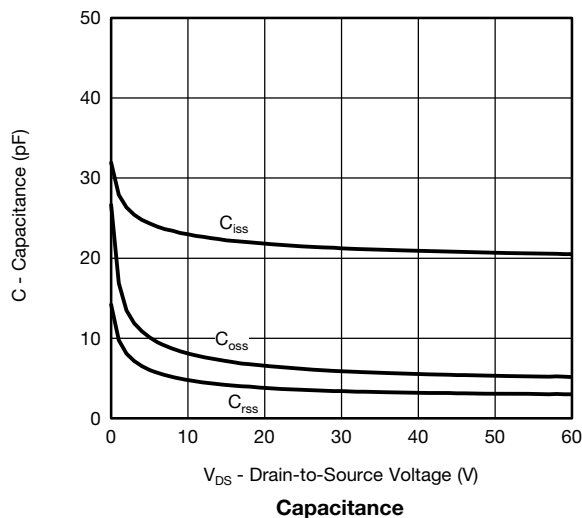
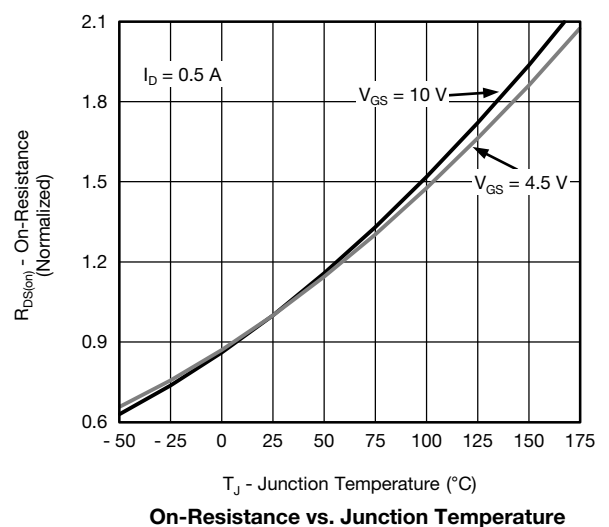
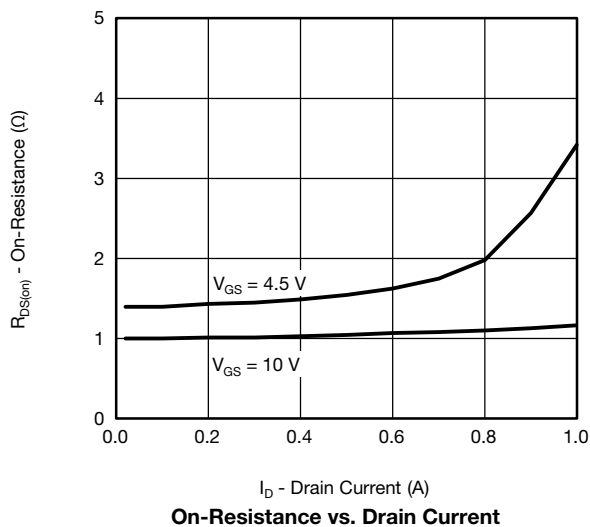
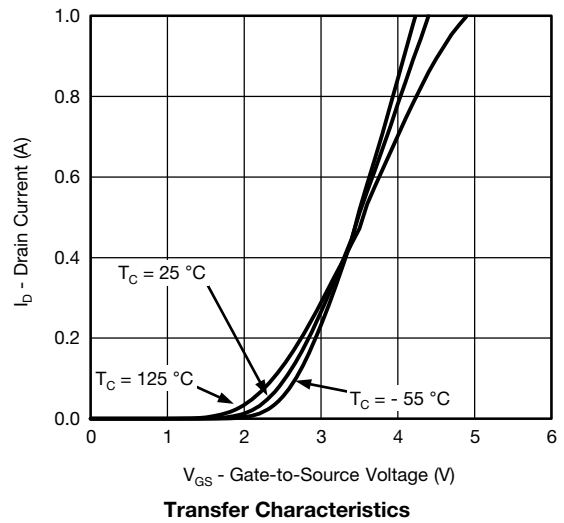
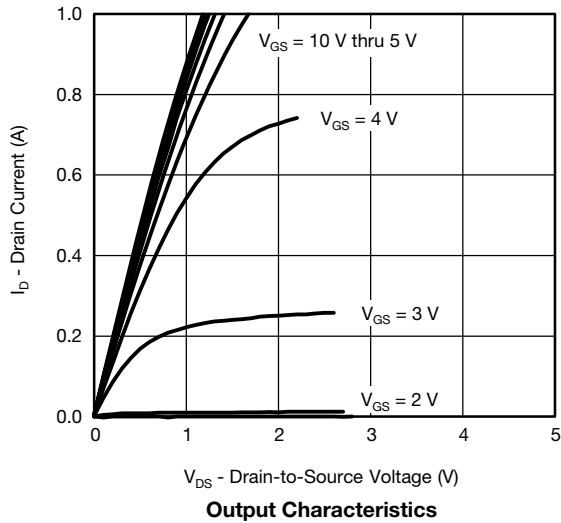
SPECIFICATIONS ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$		60	-	-	V
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$		1	1.5	2.5	
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 12\text{ V}$		-	-	± 1	μA
		$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$		-	-	± 1	mA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0\text{ V}$	$V_{DS} = 60\text{ V}$	-	-	1.0	μA
		$V_{GS} = 0\text{ V}$	$V_{DS} = 60\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	50	
		$V_{GS} = 0\text{ V}$	$V_{DS} = 60\text{ V}, T_J = 175\text{ }^\circ\text{C}$	-	-	150	
On-State Drain Current ^a	$I_{D(on)}$	$V_{GS} = 10\text{ V}$	$V_{DS} \geq 5\text{ V}$	0.500	-	-	A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}$	$I_D = 500\text{ mA}$	-	1.1	1.4	Ω
		$V_{GS} = 10\text{ V}$	$I_D = 500\text{ mA}, T_J = 125\text{ }^\circ\text{C}$	-	-	2.41	
		$V_{GS} = 10\text{ V}$	$I_D = 500\text{ mA}, T_J = 175\text{ }^\circ\text{C}$	-	-	3.04	
		$V_{GS} = 4.5\text{ V}$	$I_D = 200\text{ mA}$	-	1.5	2.0	
Forward Transconductance ^b	g_{fs}	$V_{DS} = 10\text{ V}, I_D = 200\text{ mA}$		-	0.200	-	S
Dynamic^b							
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}$	$V_{DS} = 25\text{ V}, f = 1\text{ MHz}$	-	21	27	pF
Output Capacitance	C_{oss}			-	6	8	
Reverse Transfer Capacitance	C_{rss}			-	4	5	
Total Gate Charge ^c	Q_g	$V_{GS} = 10\text{ V}$	$V_{DS} = 30\text{ V}, I_D = 250\text{ mA}$	-	1.00	1.5	nC
Gate-Source Charge ^c	Q_{GS}			-	0.16	-	
Gate-Drain Charge ^c	Q_{GD}			-	0.24	-	
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 30\text{ V}, R_L = 30\text{ }\Omega$ $I_D \cong 1\text{ A}, V_{GEN} = 10\text{ V}, R_g = 6\text{ }\Omega$		-	5	8	ns
Rise Time	t_r			-	11	17	
Turn-Off Delay Time ^c	$t_{d(off)}$	$V_{DD} = 30\text{ V}, R_L = 30\text{ }\Omega$ $I_D \cong 1\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		-	9	14	
Fall Time	t_f			-	10	15	
Diode Ratings and Characteristics^b							
Pulsed Current ^a	I_{SM}			-	-	1.2	A
Forward Voltage	V_{SD}	$I_F = 200\text{ mA}, V_{GS} = 0\text{ V}$		-	0.83	1.2	V

Notes

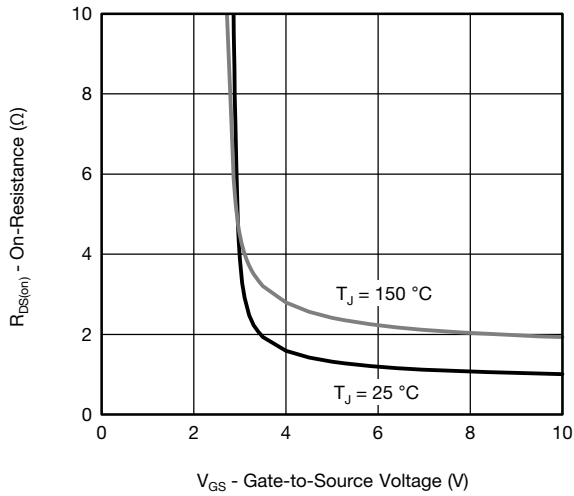
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

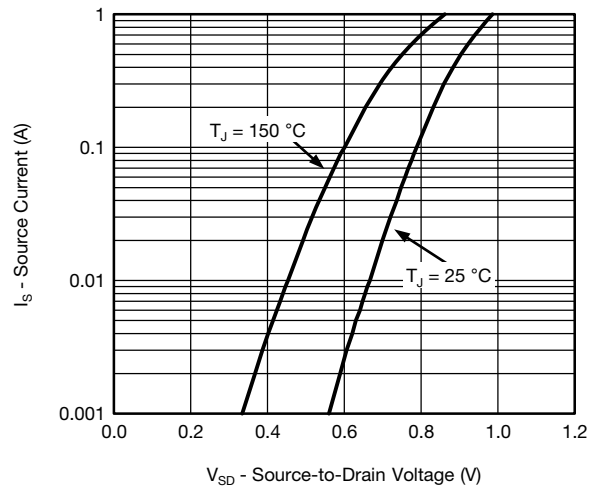
TYPICAL CHARACTERISTICS ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise noted)



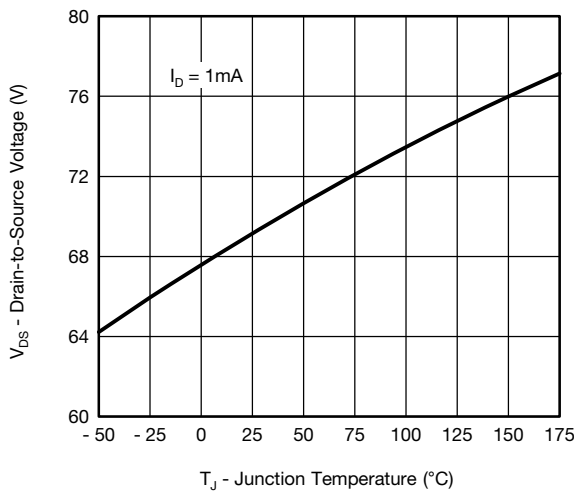
TYPICAL CHARACTERISTICS ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise noted)



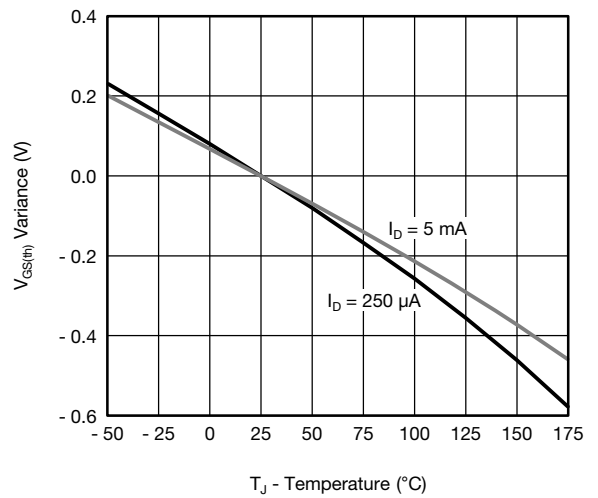
On-Resistance vs. Gate-to-Source Voltage



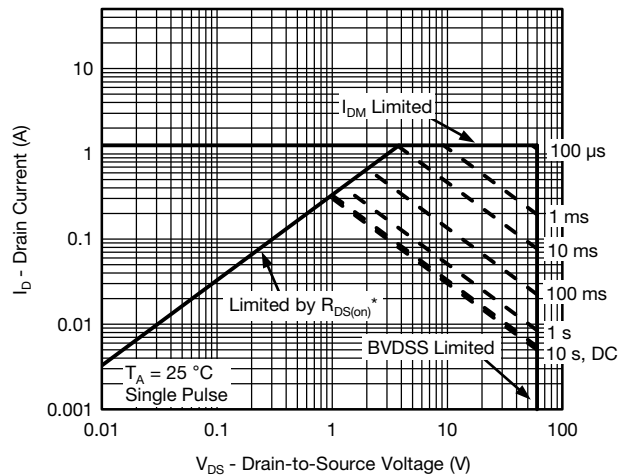
Source-Drain Diode Forward Voltage



Drain Source Breakdown vs. Junction Temperature



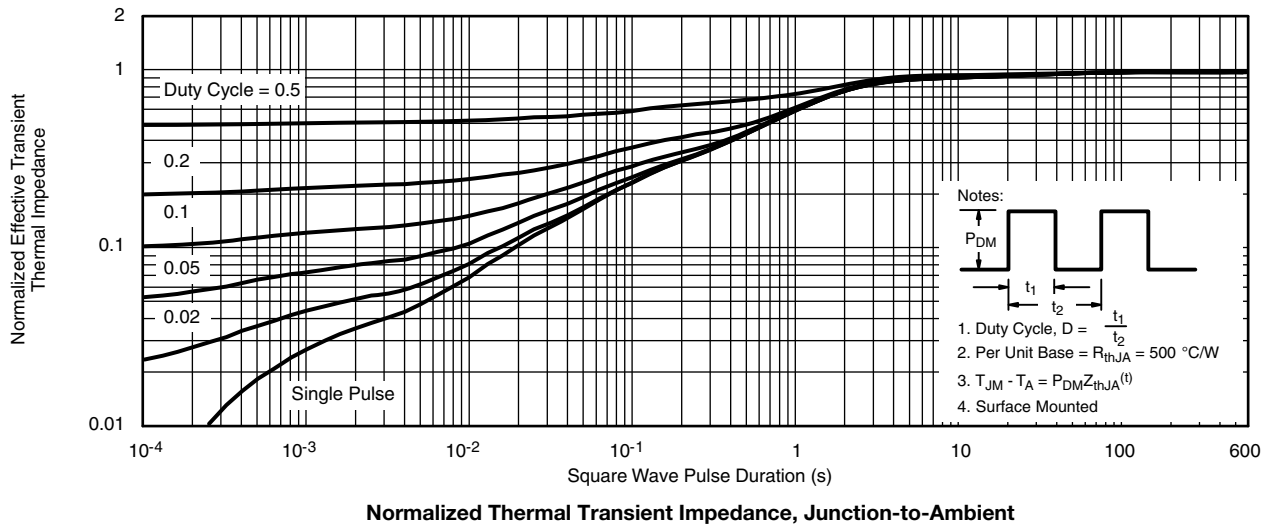
Threshold Voltage



* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area

THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



Note

The characteristics shown in the the graph Normalized Transient Thermal Impedance Junction to Ambient ($25\text{ }^\circ\text{C}$) is given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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