Si2308BDS

RoHS

COMPLIANT

HALOGEN

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Marking Code: Si2308BDS (L8)

PRODUCT SUMMARY					
V _{DS} (V)	60				
$R_{DS(on)}$ max. (Ω) at V_{GS} = 10 V	0.156				
$R_{DS(on)}$ max. (Ω) at V_{GS} = 4.5 V	0.192				
Q _g typ. (nC)	2.3				
I _D (A) ^a	2.1				
Configuration	Single				

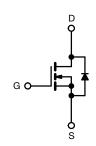
FEATURES

N-Channel 60 V (D-S) MOSFET

- Halogen-free according to IEC 61249-2-21 available
- TrenchFET[®] power MOSFET
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- · Battery Switch
- DC/DC Converter



N-Channel MOSFET

ORDERING INFORMATION				
Package	TSOP-6 Single			
Lead (Pb)-free	SI2308BDS-T1-E3			
Lood (Db) free and beleasen free	SI2308BDS-T1-GE3			
Lead (Pb)-free and halogen-free	SI2308BDS-T1-BE3			

ABSOLUTE MAXIMUM RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	60	V	
Gate-source voltage		V _{GS}	±20	v	
	T _C = 25 °C		2.3		
Continuous drain current (T ₁ = 150 °C)	T _C = 70 °C	L_	1.8		
Continuous drain current $(1) = 150^{\circ}$ C)	T _A = 25 °C	I _D	1.9 ^{b,c}		
	T _A = 70 °C	1 1	1.5 ^{b,c}		
Pulsed drain current		I _{DM}	8	А	
Continuous source-drain diode current	T _C = 25 °C	- I _S	1.39		
Continuous source-drain diode current	T _A = 25 °C		0.91 ^{b,c}		
Avalanche current	L = 0.1 mH	I _{AS}	6		
Single pulse avalanche energy	L = 0.1 MH	E _{AS}	1.8		
	T _C = 25 °C	- P _D	1.66		
Maximum power dissipation	T _C = 70 °C		1.06	w	
	T _A = 25 °C		1.09 ^{b,c}	vv	
	T _A = 70 °C		0.7 ^{b,c}		
Operating junction and storage temperature rang	е	T _J , T _{stg}	-55 to +150	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum Junction-to-Ambient b, d	t ≤ 5 s	R _{thJA}	90	115	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	60	75	C/W	

Notes

a. T_C = 25 °C

b. Surface mounted on 1" x 1" FR4 board

c. t = 5 s

d. Maximum under steady state conditions is 130 °C/W

S21-0226-Rev. D, 08-Mar-2021

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	V _{DS}	$V_{DS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	60	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_J$	L 050	-	55	-	mV/°C	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μΑ	-	-5	-		
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1	-	3	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-	-	± 100	nA	
		$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1	μΑ	
Zero gate voltage drain current	IDSS	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$	-	-	10		
On-state drain current ^a	I _{D(on)}	V_{DS} \geq -5 V, V_{GS} = 10 V	8	-	-	A	
		V _{GS} = 10 V, I _D = 1.9 A	-	0.130	0.156	Ω	
Drain-source on-state resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 1.7 \text{ A}$	-	0.160	0.192		
Forward Transconductance ^a	g fs	V _{DS} = 15 V, I _D = 1.9 A	-	5	-	S	
Dynamic ^b							
Input capacitance	Ciss		-	190	-	pF	
Output capacitance	Coss	V _{DS} = 30 V, V _{GS} = 0 V, f = 1 MHz	-	26	-		
Reverse transfer capacitance	C _{rss}		-	15	-		
-		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 1.9 \text{ A}$	-	4.5	6.8		
Total gate charge	Qg		-	2.3	3.5	nC	
Gate-source charge	Q _{gs}	V_{DS} = 30 V, V_{GS} = 4.5 V, I_{D} = 1.9 A	-	0.8	-		
Gate-drain charge	Q _{gd}		-	1	-		
Gate resistance	Rg	f = 1 MHz	0.6	2.8	5.6	Ω	
Turn-on delay time	t _{d(on)}		-	4	6		
Rise time	tr	$V_{DD} = 30 \text{ V}, \text{ R}_{L} = 20 \Omega$	-	10	15	1	
Turn-off delay time	t _{d(off)}	$I_D \cong 1.5 \text{ A}, V_{\text{GEN}} = 10 \text{ V}, R_g = 1 \Omega$	-	10	15	-	
Fall time	t _f		-	7	10.5	-	
Turn-on delay time	t _{d(on)}		-	15	23	ns	
Rise time	tr	$V_{DD} = 30 \text{ V}, \text{ R}_{L} = 20 \Omega$	-	16	24	1	
Turn-off delay time	t _{d(off)}	$I_D = 1.5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$	-	11	17	-	
Fall time	t _f		-	11	17		
Drain-Source Body Diode Characterist	ics			I	1	I	
Continuous source-drain diode current	Is	T _C = 25 °C	-	-	1.39		
Pulse diode forward current ^a	I _{SM}		-	-	8	A	
Body diode voltage	V _{SD}	I _S = 1.5 A	-	0.8	1.2	V	
Body diode reverse recovery time	t _{rr}		-	15	23	ns	
Body diode reverse recovery charge	Q _{rr}		-	10	15	nC	
Reverse recovery fall time	ta	$I_F = 1.5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 ^\circ\text{C}$	-	12	-		
Reverse recovery rise time	t _b		-	3	_	ns	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing

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Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Document Number: 69958

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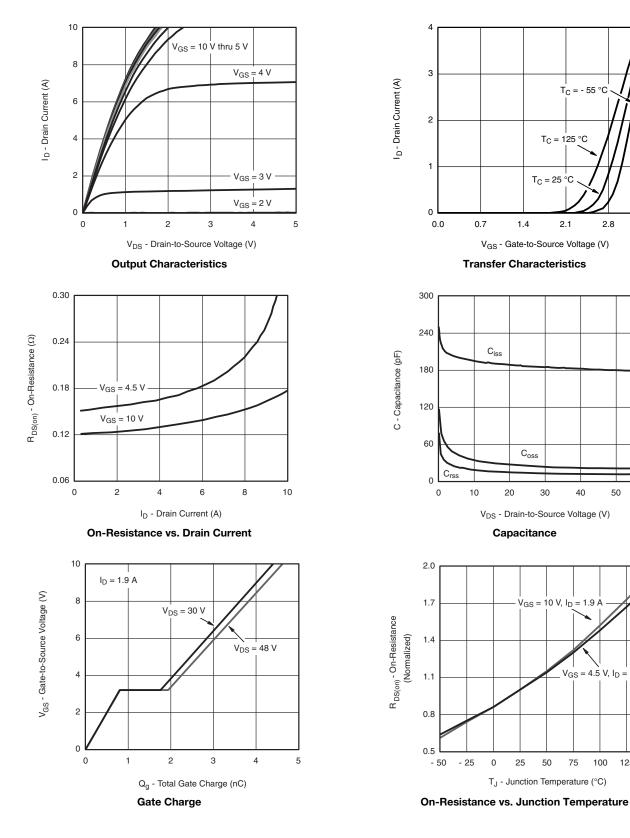
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60

1.7 A

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





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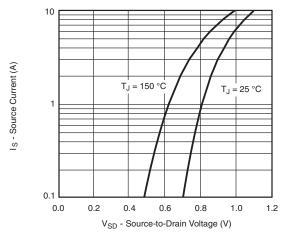
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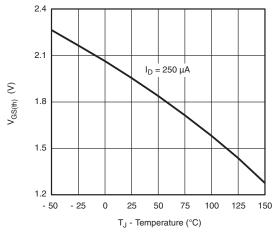
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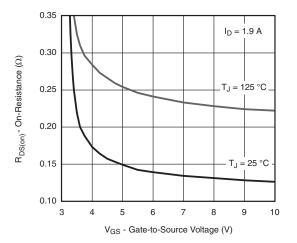
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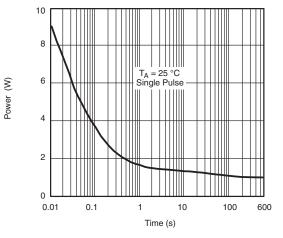
Source-Drain Diode Forward Voltage



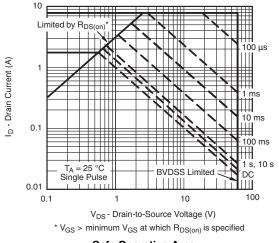
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient



Safe Operating Area

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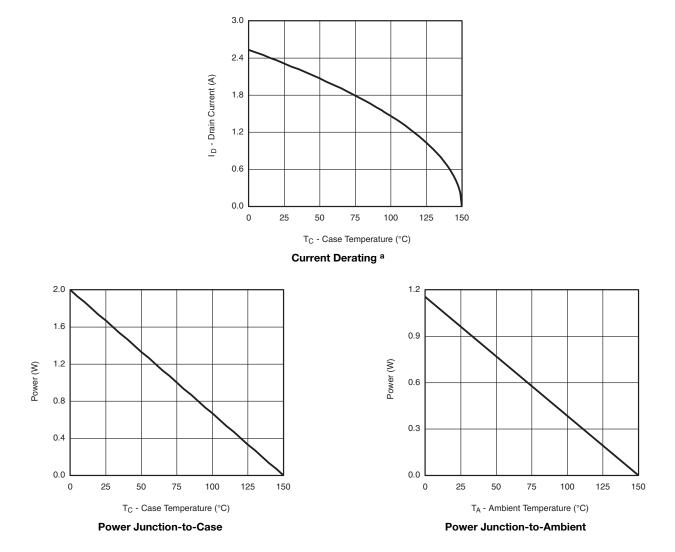
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Note

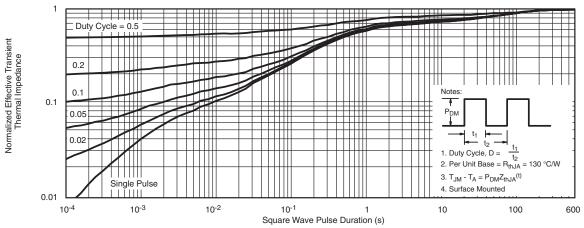
a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



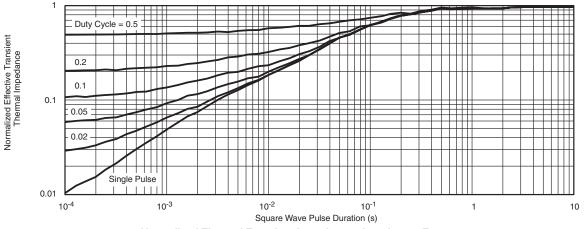
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)







Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?69958.

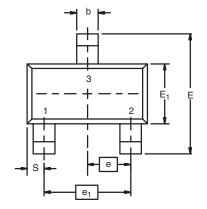
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Package Information

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SOT-23 (TO-236): 3-LEAD







Dim	MILLIM	IETERS	INCHES		
	Min	Мах	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90	1.90 BSC		8 Ref	
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K, 09- DWG: 5479	Jul-01				



Application Note 826

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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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