

# Molding Type Module IGBT, 2 in 1 Package, 1200 V and 100 A



Dual	INT_A	-DAK

PRIMARY CHARACTERISTICS					
V <sub>CES</sub>	1200 V				
$I_C$ at $T_C = 80  ^{\circ}C$	100 A				
V <sub>CE(on)</sub> (typical) at I <sub>C</sub> = 100 A, 25 °C	3.10 V				
Speed	8 kHz to 30 kHz				
Package	Dual INT-A-PAK				
Circuit configuration	Half bridge				

### **FEATURES**

- NPT IGBT technology
- 10 µs short circuit capability
- · Low switching losses
- · Rugged with ultrafast performance
- V<sub>CE(on)</sub> with positive temperature coefficient
- Low inductance case
- Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **TYPICAL APPLICATIONS**

- · Switching mode power supplies
- Inductive heating
- Electronic welder

#### **DESCRIPTION**

Vishay's IGBT power module provides ultrafast switching speed as well as short circuit ruggedness. It is designed for applications such as electronic welders and inductive heating.

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Collector to emitter voltage	V <sub>CES</sub>		1200	V		
Gate to emitter voltage	$V_{GES}$		± 20	V		
Collector current		T <sub>C</sub> = 25 °C	200			
	I <sub>C</sub>	T <sub>C</sub> = 80 °C	100			
Pulsed collector current	I <sub>CM</sub> <sup>(1)</sup>	t <sub>p</sub> = 1 ms	200	Α		
Diode continuous forward current	I <sub>F</sub>		100			
Diode maximum forward current	I <sub>FM</sub> (1)		200			
Maximum power dissipation	P <sub>D</sub>	T <sub>J</sub> = 150 °C	1136	W		
Isolation voltage	V <sub>ISOL</sub>	f = 50 Hz, t = 1 min	2500	V		

#### Note

<sup>(1)</sup> Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS (T <sub>C</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V <sub>(BR)CES</sub>	T <sub>J</sub> = 25 °C	1200	-	-		
Callantanta amittan altana	V	$V_{GE} = 15 \text{ V}, I_{C} = 100 \text{ A}, T_{J} = 25 \text{ °C}$	-	3.10	3.60	V	
Collector to emitter voltage	V <sub>CE(on)</sub>	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 100 A, T <sub>J</sub> = 125 °C	-	3.45	-	]	
Gate to emitter threshold voltage	V <sub>GE(th)</sub>	$V_{CE} = V_{GE}$ , $I_C = 1$ mA, $T_J = 25$ °C	4.4	4.9	6.0		
Zero gate voltage collector current	I <sub>CES</sub>	$V_{CE} = V_{CES}$ , $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA	
Gate to emitter leakage current	I <sub>GES</sub>	$V_{GE} = V_{GES}$ , $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA	



SWITCHING CHARACTERISTICS	3					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t <sub>d(on)</sub>		-	300	-	
Rise time	t <sub>r</sub>		-	64	-	ns
Turn-off delay time	t <sub>d(off)</sub>	$V_{CC} = 600 \text{ V}, I_{C} = 100 \text{ A}, R_{g} = 5.6 \Omega,$	-	340	-	
Fall time	t <sub>f</sub>	$V_{GE} = \pm 15 \text{ V}, L = 200 \text{ nH}, T_{J} = 25 \text{ °C}$	-	105	-	
Turn-on switching loss	E <sub>on</sub>		-	4.76	-	
Turn-off switching loss	E <sub>off</sub>		-	4.25	-	mJ
Turn-on delay time	t <sub>d(on)</sub>		-	320	-	
Rise time	t <sub>r</sub>	$V_{CC}$ = 600 V, $I_{C}$ = 100 A, $R_{g}$ = 5.6 $\Omega$ , $V_{GE}$ = ± 15 V, L = 200 nH, $T_{J}$ = 125 °C	-	65	-	ns
Turn-off delay time	t <sub>d(off)</sub>		-	350	-	
Fall time	t <sub>f</sub>		-	132	-	
Turn-on switching loss	E <sub>on</sub>		-	7.20	-	mJ
Turn-off switching loss	E <sub>off</sub>		-	5.50	-	1113
Short circuit withstand time	t <sub>SC</sub>	T <sub>J</sub> = 125 °C	-	-	10	μs
Input capacitance	C <sub>ies</sub>		-	8.45	-	
Output capacitance	C <sub>oes</sub>	$V_{GE} = 0 \text{ V}, V_{CE} = 20 \text{ V}, f = 1.0 \text{ MHz}$	-	0.76	-	nF
Reverse transfer capacitance	C <sub>res</sub>		-	0.31	-	
SC data	I <sub>SC</sub>	$t_p \le 10~\mu s, V_{GE} = \pm~15~V, V_{CC} = 600~V, \ V_{CEM} \le 1200~V, T_J = 25~^{\circ}C$	-	900	-	
Internal gate resistance	R <sub>GINT</sub>		-	2.4	-	Ω
Stray inductance	L <sub>CE</sub>		-	-	18	nΗ
Module lead resistance, terminal to chip	R <sub>CC'+EE'</sub>		-	0.32	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS M			MAX.	UNITS
Diode forward voltage	\/_	I <sub>F</sub> = 100 A	T <sub>C</sub> = 25 °C	-	1.82	2.22	V
Diode forward voltage	$V_F$ $I_F =$	IF = 100 A	T <sub>C</sub> = 125 °C	-	1.95	-	- V
Diada rayaraa raaayary aharaa	Q <sub>rr</sub>	Q <sub>rr</sub>	T <sub>C</sub> = 25 °C	-	5.4	-	
Diode reverse recovery charge			T <sub>C</sub> = 125 °C	-	11.2	-	μC
Diada paak rayaraa raaayary ayrrant		$I_F = 100 \text{ A}, V_R = 600 \text{ V},$	T <sub>C</sub> = 25 °C	-	81	-	^
Diode peak reverse recovery current	I <sub>rr</sub>	dI <sub>F</sub> /dt = -1900 A/μs, V <sub>GF</sub> = -15 V	T <sub>C</sub> = 125 °C	-	101	-	A
Diada rayaraa raaayan, anaray	E <sub>rec</sub>		T <sub>C</sub> = 25 °C	-	3.54	-	m l
Diode reverse recovery energy	⊏rec		T <sub>C</sub> = 125 °C	-	6.57	-	mJ

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperatur	e range	$T_J$		-40	-	150	°C
Storage temperature range		$T_{Stg}$		-40	-	125	
Junction to case —	IGBT	D		-	-	0.141	
Junction to case —	Diode	$R_{thJC}$		-	-	0.225	°C/W
Case to sink		R <sub>thCS</sub>	Conductive grease applied	-	0.035	-	
Mounting torque			Power terminal screw: M6 2.5 to 5.0		)	Nm	
			Mounting screw: M6 3.0 to 6.0		)	INIII	
Weight				300		g	



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# Vishay Semiconductors

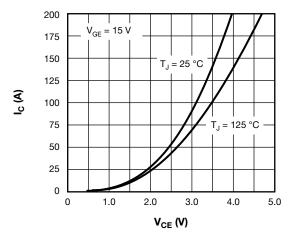


Fig. 1 - IGBT Typical Output Characteristics

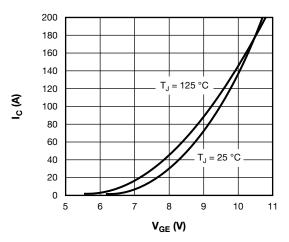


Fig. 2 - IGBT Typical Transfer Characteristics

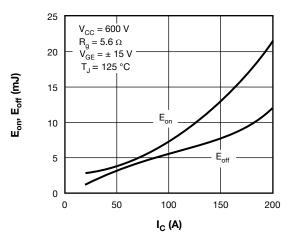


Fig. 3 - Switching Loss vs. I<sub>C</sub>

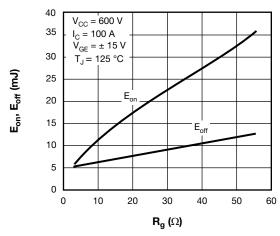
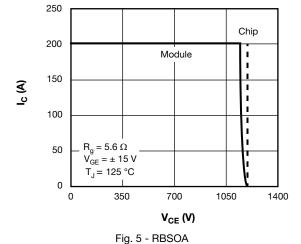


Fig. 4 - IGBT Switching Loss vs. Rq





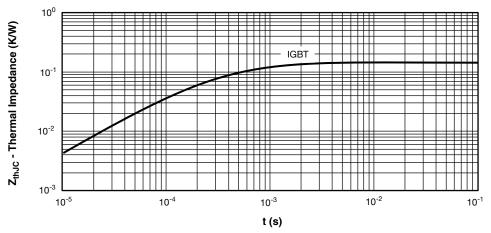
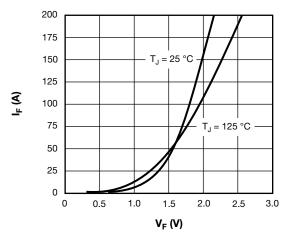


Fig. 6 - IGBT Transient Thermal Impedance



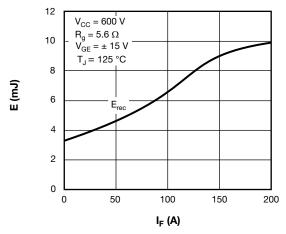


Fig. 7 - Diode Typical Forward Characteristics

Fig. 8 - Diode Switching Loss vs. I<sub>F</sub>

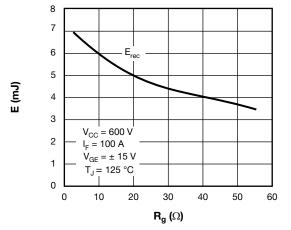


Fig. 9 - Diode Switching Loss vs.  $R_{\mbox{\scriptsize g}}$ 

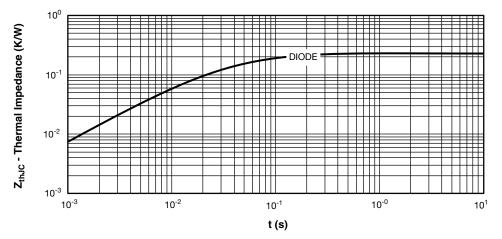
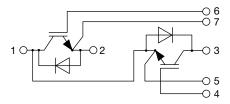


Fig. 10 - Diode Transient Thermal Impedance

### **CIRCUIT CONFIGURATION**

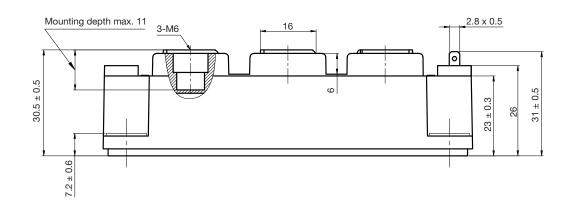


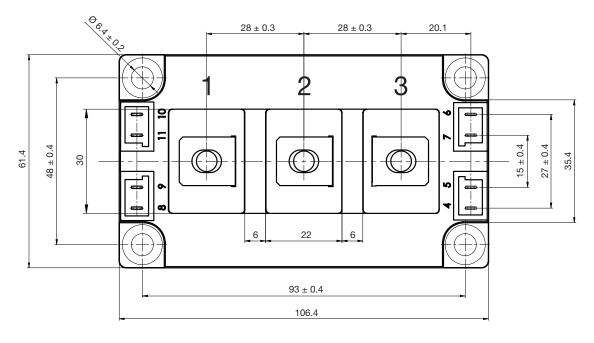
LINKS TO RELAT	ED DOCUMENTS
Dimensions	www.vishay.com/doc?95525



## **Double INT-A-PAK**

### **DIMENSIONS** in millimeters (inches)





# **Legal Disclaimer Notice**



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