VS-16TTS..FPPbF Series, VS-16TTS...FP-M3 Series

Vishay Semiconductors

RoHS

FREE

High Voltage Phase Control Thyristor, 16 A



www.vishay.com

SHA

PRODUCT SUMMARY					
Package	TO-220AB FP				
Diode variation	Single SCR				
I _{T(AV)}	10 A				
V _{DRM} /V _{RRM}	800 V, 1200 V				
V _{TM}	1.4 V				
I _{GT}	60 mA				
TJ	- 40 °C to 125 °C				

FEATURES

- · Designed and gualified for industrial level
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- UL E78996 approved
- 125 °C max. operating junction temperature
- COMPLIANT • Material categorization: HALOGEN For definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

 Typical usage is in input rectification crowbar (soft start) and AC switch in motor control, UPS, welding, and battery charge

DESCRIPTION

The VS-16TTS..FP... high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

OUTPUT CURRENT IN TYPICAL APPLICATIONS					
APPLICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS					
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C, common heatsink of 1 °C/W	13.5	17	А		

MAJOR RATINGS AND CHARACTERISTICS								
PARAMETER	TEST CONDITIONS VALUES U							
I _{T(AV)}	Sinusoidal waveform	Sinusoidal waveform 10						
I _{RMS}		16	A					
V _{DRM} /V _{RRM}		800/1200	V					
I _{TSM}		200	A					
V _T	10 A, T _J = 25 °C	1.4	V					
dV/dt		500	V/µs					
dl/dt		150	A/µs					
TJ	Range	- 40 to 125	°C					

VOLTAGE RATINGS							
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA				
VS-16TTS08FPPbF, VS-16TTS08FP-M3	800	800	10				
VS-16TTS12FPPbF, VS-16TTS12FP-M3	1200	1200	10				

Revision: 26-Jul-13

Document Number: 94381

For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

1



VS-16TTS..FPPbF Series, VS-16TTS...FP-M3 Series

www.vishay.com

Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL		TEST CONDITIONS			UNITS
PANAMEIEN	STMBOL		TEST CONDITIONS	TYP.	MAX.	on in o
Maximum average on-state current	I _{T(AV)}	T _C = 70 °C, ⁻	180° conduction, half sine wave	10		
Maximum RMS on-state current	I _{RMS}			1	6	А
Maximum peak, one-cycle,	1	10 ms sine p	oulse, rated V _{RRM} applied	1	70	A
non-repetitive surge current	I _{TSM}	10 ms sine p	ulse, no voltage reapplied	2	00	
Movimum 12t for fusion	l ² t	10 ms sine p	oulse, rated V _{RRM} applied	144		A ² s
Maximum I ² t for fusing	141	10 ms sine pulse, no voltage reapplied			200	
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 to 10	ms, no voltage reapplied	20	00	A²√s
Maximum on-state voltage drop	V _{TM}	10 A, T _J = 25	10 A, T _J = 25 °C		.4	V
On-state slope resistance	r _t	T 105.00		24	1.0	mΩ
Threshold voltage	V _{T(TO)}	T _J = 125 °C		1	.1	V
	1 /1	T _J = 25 °C		0	.5	
Maximum reverse and direct leakage current	I _{RM} /I _{DM}	T _J = 125 °C	V _R = Rated V _{RRM} /V _{DRM}	1	0	
Holding current	Ι _Η	Anode supply = 6 V, resistive load, initial I_T = 1 A 16TTS08FP, 16TTS12FP, T_J = 25 °C		-	150	mA
Maximum latching current	١L	Anode supply = 6 V, resistive load, $T_J = 25 \text{ °C}$		2	00	
Maximum rate of rise of off-state voltage	dV/dt	$T_J = T_J max.$, linear to 80 %, $V_{DRM} = R_g - k = Open$	5	00	V/µs
Maximum rate of rise of turned-on current	dl/dt			1	50	A/µs

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P _{GM}		8.0	w	
Maximum average gate power	P _{G(AV)}		2.0	~~	
Maximum peak positive gate current	+ I _{GM}		1.5	А	
Maximum peak negative gate voltage	- V _{GM}		10	V	
	I _{GT}	Anode supply = 6 V, resistive load, T_J = - 10 °C	90	mA	
Maximum required DC gate current to trigger		Anode supply = 6 V, resistive load, T_J = 25 °C	60		
		Anode supply = 6 V, resistive load, T_J = 125 °C	35		
		Anode supply = 6 V, resistive load, T_J = - 10 °C	3.0		
Maximum required DC gate voltage to trigger	V_{GT}	Anode supply = 6 V, resistive load, T_J = 25 °C	2.0	v	
		Anode supply = 6 V, resistive load, T_J = 125 °C	1.0		
Maximum DC gate voltage not to trigger V _{GD}		T 105 00 M Detail of a	0.25		
Maximum DC gate current not to trigger I _{GD}		T _J = 125 °C, V _{DRM} = Rated value	2.0	mA	

SWITCHING							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Typical turn-on time	t _{gt}	T _J = 25 °C	0.9				
Typical reverse recovery time	t _{rr}	T 105 %C	4	μs			
Typical turn-off time	tq	T _J = 125 °C	110				

Revision: 26-Jul-13

2

Document Number: 94381

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

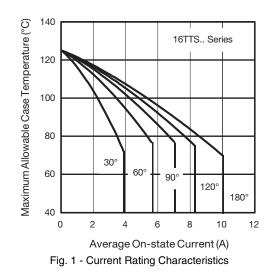


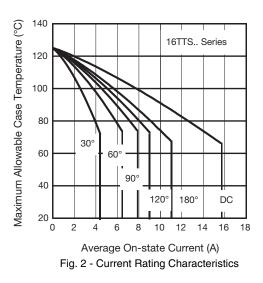
VS-16TTS..FPPbF Series, VS-16TTS...FP-M3 Series

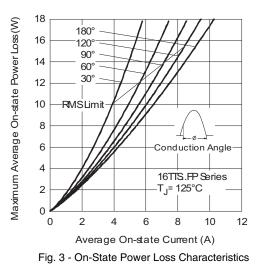
www.vishay.com

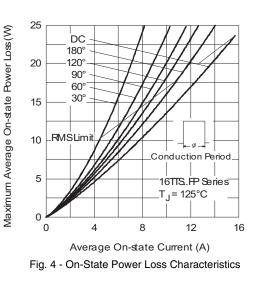
Vishay Semiconductors

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 125	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	2.5	
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5	
Approximate weight				2	g
Approximate weight				0.07	oz.
Mounting torgue	minimum			6 (5)	kgf ⋅ cm
	maximum			12 (10)	(lbf ⋅ in)
•• •• •				16TTS08FP	
Marking device			Case style TO-220AB FULL-PAK (94/V0)	16TTS12FP	





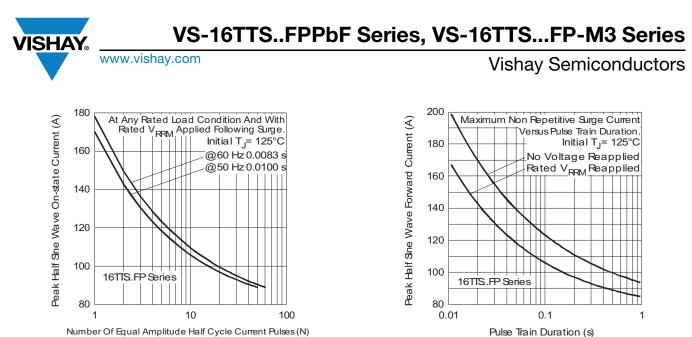


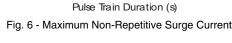


3

Document Number: 94381

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>





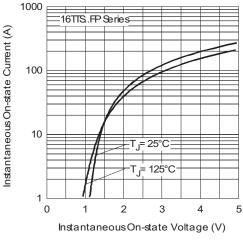


Fig. 5 - Maximum Non-Repetitive Surge Current

Fig. 7 - On-State Voltage Drop Characteristics

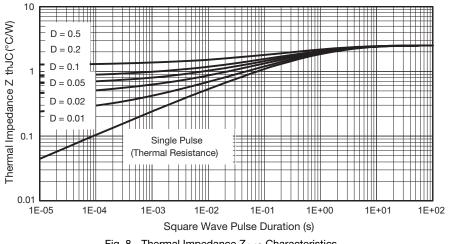
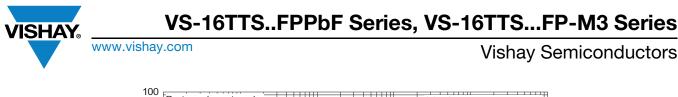


Fig. 8 - Thermal Impedance ZthJC Characteristics

Document Number: 94381

For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



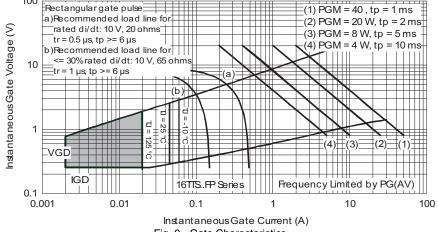


Fig. 9 - Gate Characteristics

ORDERING INFORMATION TABLE

Devi

ice code	VS-	16	т	т	S	12	FP	PbF	
		2	3	4	5	6	7	8	
	1 - 2 - 3 -	Curr	rent rati	ng, RMS guratior		duct			
	4 -	Pac	kage: TO-220	-					
	5 -		e of silic	on: ter grade	2	_			_
	6 - 7 -	Volt		-	= V _{RRM}		08 = 8 12 = 1		
	8 -	PbF	= Lead	. ,	e and R		•		tions lead (Pb)

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-16TTS08FPPbF	50	1000	Antistatic plastic tubes				
VS-16TTS08FP-M3	50	1000	Antistatic plastic tubes				
VS-16TTS12FPPbF	50	1000	Antistatic plastic tubes				
VS-16TTS12FP-M3	50	1000	Antistatic plastic tubes				

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95072					
Part marking information	TO-220FP PbF	www.vishay.com/doc?95069			
Part marking information	TO-220FP -M3	www.vishay.com/doc?95456			

Revision: 26-Jul-13

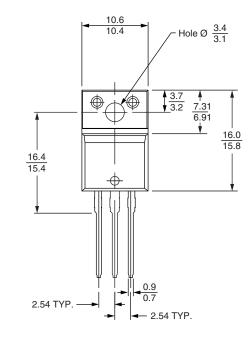
5

Document Number: 94381

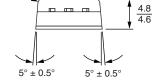
For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



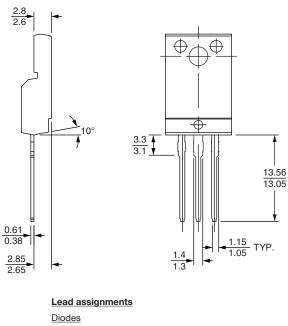
DIMENSIONS in millimeters



R 0.7 (2 places)



m m **Vishay Semiconductors**



- 1. Anode/open
- 2. Cathode
- 3. Anode

Conforms to JEDEC outline TO-220 FULL-PAK



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.