Si1036X

RoHS

COMPLIANT

HALOGEN

FREE

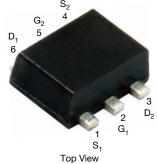


Vishay Siliconix

Dual N-Channel 30 V (D-S) MOSFET

| PRODU | CT SUMMARY | | |
|---------------------|----------------------------------|--------------------|-----------------------|
| V _{DS} (V) | R _{DS(on)} (Ω) MAX. | I _D (A) | Q _g (TYP.) |
| | 0.540 at V _{GS} = 4.5 V | 0.5 | |
| 30 | 0.600 at V _{GS} = 2.5 V | 0.2 | 0.72 nC |
| 30 | 0.700 at V _{GS} = 1.8 V | 0.2 | 0.72110 |
| | 1.100 at V _{GS} = 1.5 V | 0.05 | |

SC-89 Dual (6 leads)



Marking Code: B

Ordering Information:

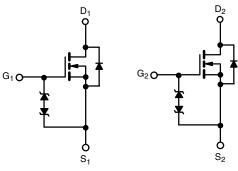
Si1036X-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- TrenchFET[®] Power MOSFET
- 100 % Rg tested
- Gate-source ESD protected: 1000 V
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Load switch
- High speed switching
- DC/DC converters / boost converters
- · For smart phones, tablet PCs and mobile computing



N-Channel MOSFET

N-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS (| T _A = 25 °C, unles | s otherwise i | noted) | |
|---|-------------------------------|-----------------------------------|---------------------|------|
| PARAMETER | | SYMBOL | LIMIT | UNIT |
| Drain-Source Voltage | | V _{DS} | 30 | V |
| Gate-Source Voltage | | V _{GS} | ± 8 | v |
| Continuous Drain Current (T. 150 °C) 3 | T _A = 25 °C | | 0.61 ^{a,b} | |
| Continuous Drain Current (T _J = 150 °C) ^a | T _A = 70 °C | I _D | 0.49 ^{a,b} | A |
| Pulsed Drain Current (t = 100 µs) | | I _{DM} | 2 | |
| Continuous Source-Drain Diode Current | T _A = 25 °C | I _S | 0.18 ^{a,b} | A |
| Maximum Dawar Dissinction a | T _A = 25 °C | D | 0.22 ^{a,b} | W |
| Maximum Power Dissipation ^a | T _A = 70 °C | PD | 0.14 ^{a,b} | vv |
| Operating Junction and Storage Temperature R | ange | T _J , T _{stg} | -55 to 150 | °C |

| THERMAL RESISTANCE RATINGS | | | | | |
|--|--------------|-------------------|------|------|------|
| PARAMETER | | SYMBOL | TYP. | MAX. | UNIT |
| Maximum Junction-to-Ambient ^b | t≤5 s | R _{thJA} | 470 | 565 | °C/W |
| | Steady State | nthJA | 560 | 675 | 0/10 |

Notes

a. Surface mounted on 1" x 1" FR4 board.

b. t = 5 s.

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Si1036X

| SPECIFICATIONS ($T_J = 25 \degree C$, | | | | TYP | | | |
|--|-------------------------|---|------|----------|-------|-------|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNIT | |
| Static | | | 1 | r | r | | |
| Drain-Source Breakdown Voltage | V _{DS} | $V_{GS} = 0 V, I_D = 250 \mu A$ | 30 | - | - | V | |
| V _{DS} Temperature Coefficient | $\Delta V_{DS}/T_{J}$ | I _D = 250 μΑ | - | 29 | - | mV/°C | |
| V _{GS(th)} Temperature Coefficient | $\Delta V_{GS(th)}/T_J$ | | - | -1.8 | - | | |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = 250 \ \mu A$ | 0.4 | - | 1 | V | |
| Gate-Source Leakage | IGSS | $V_{DS} = 0 V$, $V_{GS} = \pm 8 V$ | - | - | ± 30 | | |
| Guie Course Loundge | 1922 | $V_{DS} = 0 V, V_{GS} = \pm 4.5 V$ | - | - | ± 1 | μA | |
| Zero Gate Voltage Drain Current | I _{DSS} | $V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$ | - | - | 1 | μΛ | |
| Zero date voltage Drain ourrent | USS | V_{DS} = 30 V, V_{GS} = 0 V, T_{J} = 85 °C | - | - | 3 | | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} = \geq 5 \text{ V}, V_{GS} = 4.5 \text{ V}$ | 2 | - | - | Α | |
| | | $V_{GS} = 4.5 \text{ V}, I_D = 0.5 \text{ A}$ | - | 0.450 | 0.540 | | |
| Drain-Source On-State Resistance ^a | D | $V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 0.2 \text{ A}$ | - | 0.500 | 0.600 | Ω | |
| Drain-Source On-State Resistance | R _{DS(on)} | V _{GS} = 1.8 V, I _D = 0.2 A | - | 0.560 | 0.700 | 52 | |
| | | $V_{GS} = 1.5 \text{ V}, \text{ I}_{D} = 0.05 \text{ A}$ | - | 0.647 | 1.100 | | |
| Forward Transconductance | g _{fs} | V _{DS} = 15 V, I _D = 0.5 A | - | 7.5 | - | S | |
| Dynamic ^b | | | • | • | | | |
| Input Capacitance | Ciss | | - | 36 | - | | |
| Output Capacitance | C _{oss} | V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz | - | 9 | - | pF | |
| Reverse Transfer Capacitance | C _{rss} | | - | 5 | - | | |
| | 0 | $V_{DS} = 15 \text{ V}, V_{GS} = 8 \text{ V}, I_D = 0.5 \text{ A}$ | - | 1.2 | 2 | | |
| Total Gate Charge | Qg | | - | 0.72 | 1.2 | | |
| Gate-Source Charge | Q _{qs} | V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 0.5 A | - | 0.1 | - | nC | |
| Gate-Drain Charge | Q _{gd} | | - | 0.16 | - | | |
| Gate Resistance | R _g | f = 1 MHz | 2.4 | 12.2 | 24.4 | Ω | |
| Turn-On Delay Time | t _{d(on)} | | - | 6 | 15 | | |
| Rise Time | tr | V _{DD} = 15 V, R _I = 37.5 Ω | - | 13 | 24 | ns | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong 0.4 \text{ A}, V_{\text{GEN}} = 4.5 \text{ V}, R_g = 1 \Omega$ | - | 20 | 30 | | |
| Fall Time | t _f | | - | 11 | 20 | 1 | |
| Drain-Source Body Diode Characterist | | | 1 | <u> </u> | | | |
| Pulse Diode Forward Current ^a | I _{SM} | | - | - | 2 | А | |
| Body Diode Voltage | V _{SD} | I _S = 0.5 A | - | 0.8 | 1.2 | V | |
| Body Diode Reverse Recovery Time | t _{rr} | ~ | - | 8 | 15 | ns | |
| Body Diode Reverse Recovery Charge | Q _{rr} | | - | 2 | 4 | nC | |
| Reverse Recovery Fall Time | t _a | I _F = 0.4 A, dl/dt = 100 A/μs | - | 4 | - | | |
| Reverse Recovery Rise Time | t _b | | - | 4 | _ | ns | |

Notes

a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$

b. Guaranteed by design, not subject to production testing.

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Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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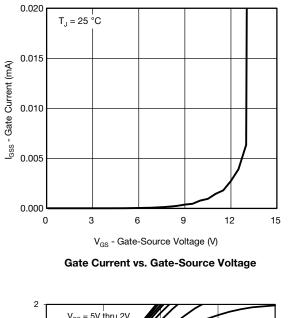
T_{.1} = 25 °C

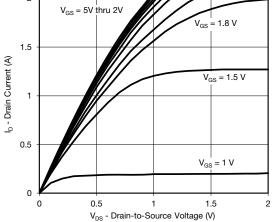
12

15

9

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Output Characteristics

1

On-Resistance vs. Drain Current

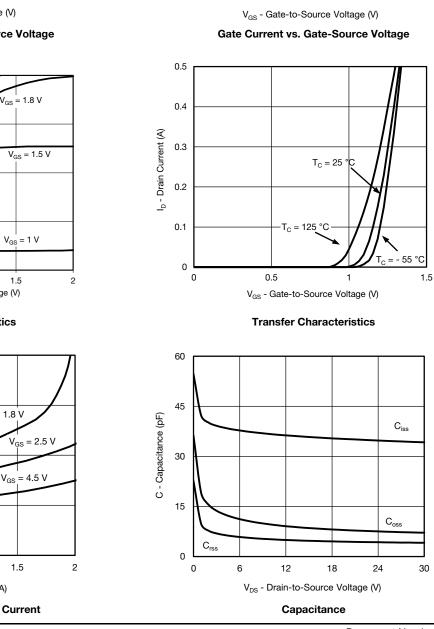
I_D - Drain Current (A)

V_{GS} = 1.5 V

0.5

V_{GS} = 1.8 V

1.5



10⁻³

10⁻⁴

10⁻⁵

10⁻⁶

10⁻⁷

10⁻⁸

10⁻⁹

0

3

6

 $T_{J} = 150$

I_{GSS} - Gate Current (A)

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1

0.8

0.6

0.4

0.2

0

 $R_{DS(on)}$ - On-Resistance (Ω)

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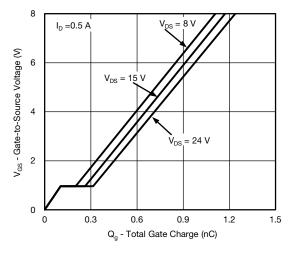
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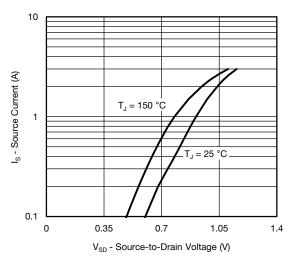


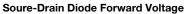
Vishay Siliconix

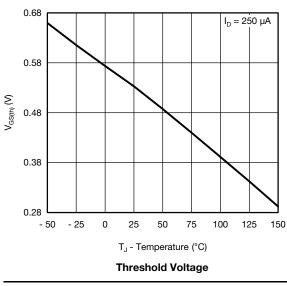
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)







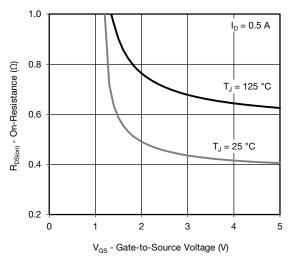




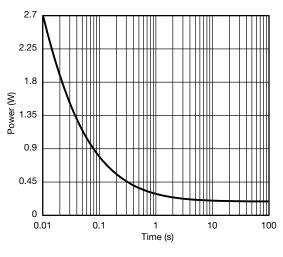
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1.8 $V_{GS} = 4.5$ 2.5 R_{DS(on)} - On-Resistance (Normalized) 1.5 V_{GS} = 1.8 V, 1.5 V 1.2 0.9 0.6 25 50 150 - 25 0 75 100 125 - 50 T_J - Junction Temperature (°C)

On-Resistance vs. Junction Temperature







Single Pulse Power, Junction-to-Ambient

4

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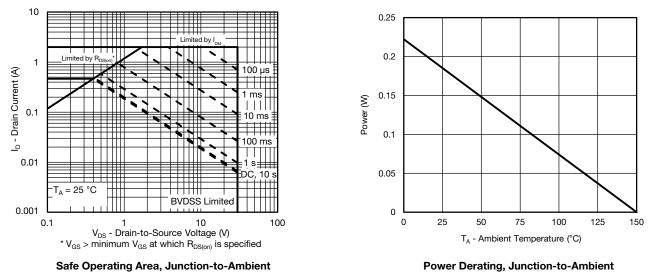
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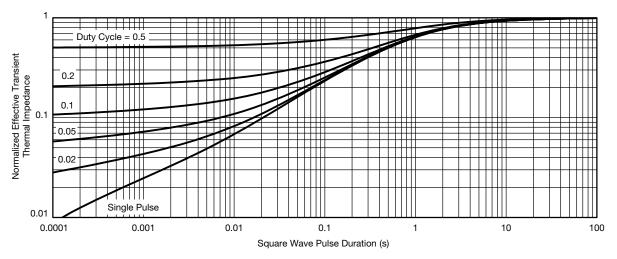


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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



* The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



Normalized Thermal Transient Impedance, Junction-to-Ambient

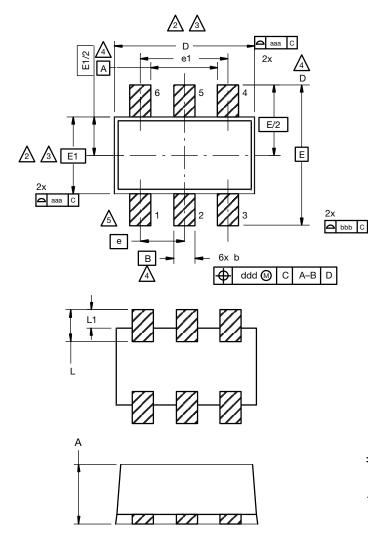
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Vishay Siliconix

SC-89 6-Leads (SOT-563F)



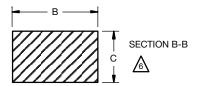
Notes

- 1. Dimensions in millimeters.
- Dimension D does not include mold flash, protrusions or gate burrs. Mold flush, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.
- Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.

A Datums A, B and D to be determined 0.10 mm from the lead tip.

 \triangle Terminal numbers are shown for reference only.

These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.









| 0.56 0 0.15 | NOM. 0.58 0.02 0.22 | MAX. 0.60 0.10 0.30 |
|-------------------|------------------------------------|---|
| 0 | 0.02 | 0.10 |
|).15 | | |
| | 0.22 | 0.20 |
| | | 0.30 |
|).10 | 0.14 | 0.18 |
| .50 | 1.60 | 1.70 |
| .50 | 1.60 | 1.70 |
| .15 | 1.20 | 1.25 |
|).45 | 0.50 | 0.55 |
|).95 | 1.00 | 1.05 |
|).25 | 0.35 | 0.50 |
|).10 | 0.20 | 0.30 |
| | .50 .15 0.45 0.95 0.25 | .50 1.60 .15 1.20 0.45 0.50 0.95 1.00 0.25 0.35 0.10 0.20 |

Document Number: 71612

Revision: 11-Aug-14

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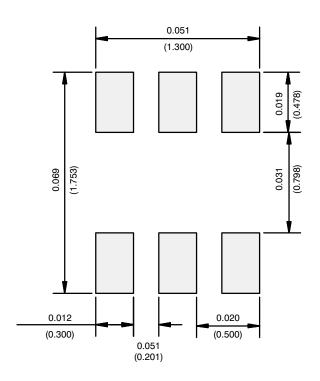
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Application Note 826

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RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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