

Vishay Siliconix

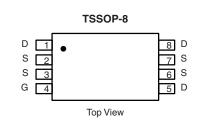
P-Channel 2.5-V (G-S) MOSFET

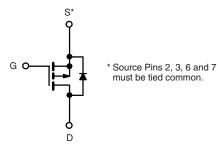
PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)			
- 12	0.040 at V_{GS} = - 4.5 V	- 4.8			
	0.070 at V _{GS} = - 2.5 V	- 3.6			

FEATURES

• Halogen-free







Ordering Information: Si6433BDQ-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25 \text{ °C}$, unless otherwise noted							
Parameter		Symbol	10 s	Steady State	Unit		
Drain-Source Voltage		V _{DS}	- 12		v		
Gate-Source Voltage		V _{GS}	± 8				
Continuous Drain Current (T _{.1} = 150 °C) ^a	T _A = 25 °C	- I _D	- 4.8	- 4.0			
Continuous Drain Current $(T_j = 150^{\circ} \text{ C})^{\circ}$	T _A = 70 °C		- 3.9	- 3.2	•		
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	- 20		A		
Continuous Source Current (Diode Conduction) ^a		۱ _S	- 1.35	- 0.95			
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	1.5	1.05	W		
	T _A = 70 °C		1.0	0.67			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum lumation to Ambienta	t ≤ 10 s	R _{thJA}	65	83		
Maximum Junction-to-Ambient ^a	Steady State		100	120	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State		43	52		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	<u>, - </u>						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.6		- 1.5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0$ V, $V_{GS} = \pm 8$ V		± 100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -12 V, V_{GS} = 0 V$			- 1		
		V_{DS} = - 12 V, V_{GS} = 0 V, T_{J} = 70 °C			- 25	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 20			А	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -4.8 \text{ A}$		0.032	0.040	Ω	
		V _{GS} = - 2.5 V, I _D = - 3.6 A		0.053	0.070		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -5 V, I_{D} = -4.8 A$		14		S	
Diode Forward Voltage ^a	V _{SD}	I _S = - 1.35 A, V _{GS} = 0 V		- 0.77	- 1.1	V	
Dynamic ^b				•			
Total Gate Charge	Qg			10	15		
Gate-Source Charge	Q _{gs}	V_{DS} = - 6 V, V_{GS} = - 4.5 V, I_{D} = - 4.8 A		1.8		nC	
Gate-Drain Charge	Q _{gd}			3			
Gate Resistance	R _g	f = 1 MHz		7.7		Ω	
Turn-On Delay Time	t _{d(on)}			45	70		
Rise Time	t _r	V_{DD} = - 6 V, R_L = 6 Ω		60	90		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 1 A, V_GEN = - 4.5 V, R_g = 6 Ω		70	110	ns	
Fall Time	t _f			35	55		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.35 A, di/dt = 100 A/μs		65			

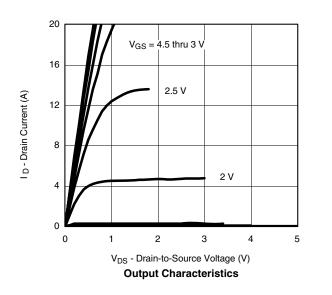
Notes:

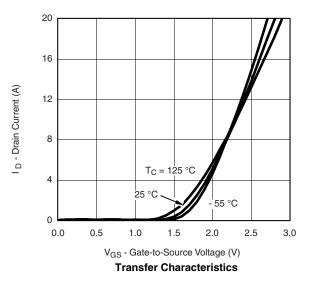
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







Si6433BDQ

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 C_{iss}

8

10

12

Coss

6

50

75

4.8 A

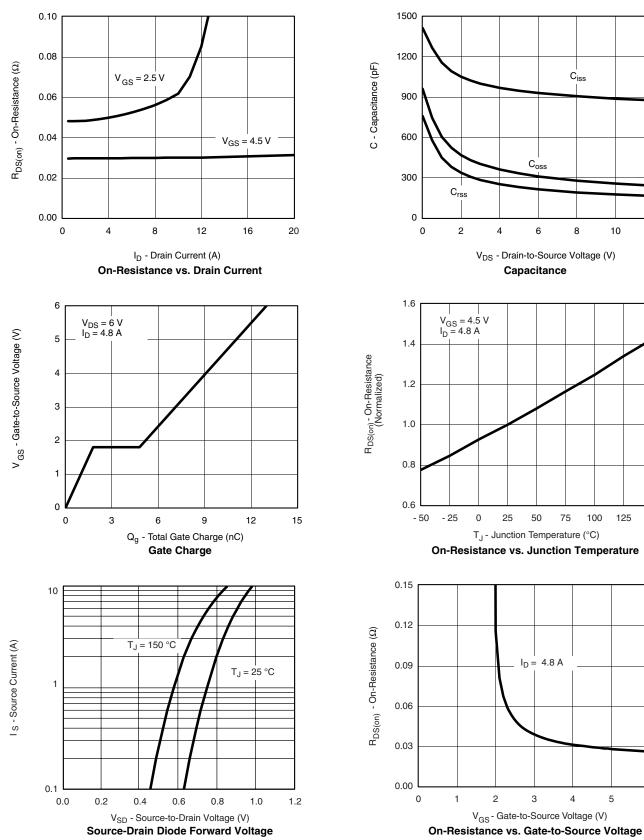
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4

100

125 150

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



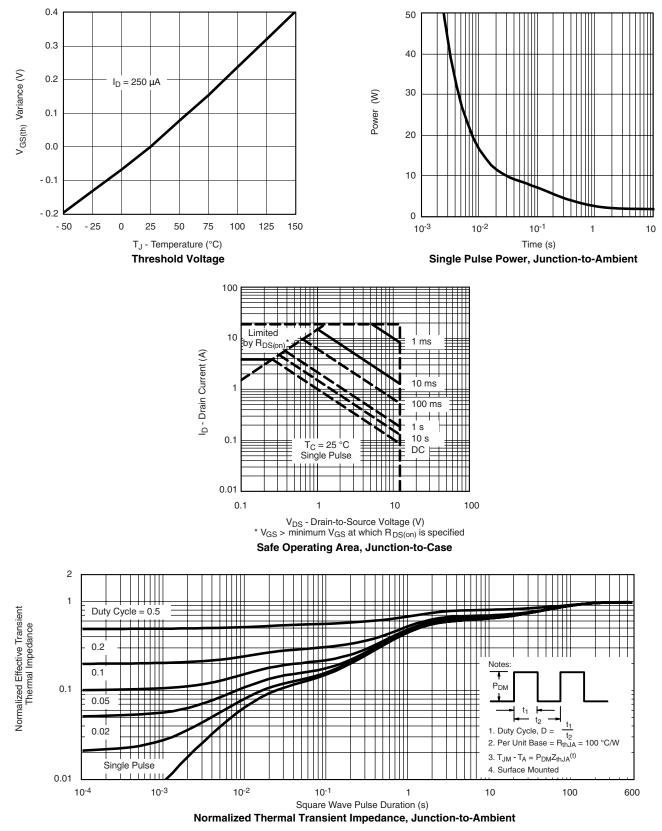
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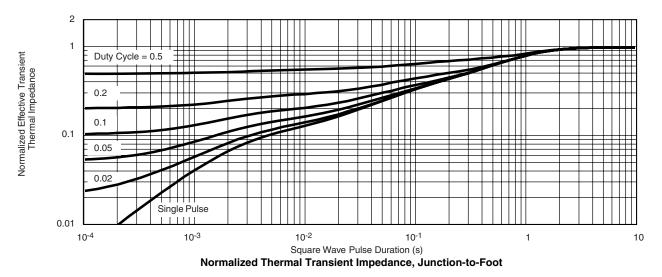
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72511.



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