VS-GT105LA120UX

Vishay Semiconductors



"Low Side Chopper" IGBT SOT-227 (Trench IGBT), 100 A



PRODUCT SUMMARY						
V _{CES}	1200 V					
I _C DC	100 A at 71 °C					
V _{CE(on)} typical at 100 A, 25 °C	2.45 V					
Package	SOT-227					
Circuit	Chopper low side switch					

FEATURES

- Trench IGBT technology
- Very low V_{CE(on)}
- Square RBSOA
- HEXFRED[®] clamping diode
- 10 µs short circuit capability
- · Fully isolated package
- Speed 4 kHz to 30 kHz
- Very low internal inductance (≤ 5 nH typical)
- · Industry standard outline
- UL approved file E78996
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- · Designed for increased operating efficiency in power conversion: UPS, SMPS, welding, induction heating
- Easy to assemble and parallel
- Direct mounting on heatsink
- Plug-in compatible with other SOT-227 packages
- · Low EMI, requires less snubbing

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
		T _C = 25 °C	134		
Continuous collector current	I _C	T _C = 80 °C	92		
Pulsed collector current	I _{CM}		270		
Clamped inductive load current	I _{LM}		270	А	
Diode continuous forward current		T _C = 25 °C	87		
	I _F	T _C = 80 °C	59	1	
Single pulse forward current	I _{FSM}	10 ms sine or 6 ms rectangular pulse, T_J = 25 °C	360		
Gate to emitter voltage	V _{GE}		± 30	V	
Power dissipation, IGBT	D	T _C = 25 °C	463		
	PD	T _C = 80 °C	260	\A/	
Power dissipation, diode		T _C = 25 °C	338	W	
	PD	T _C = 80 °C	190	1	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V	

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ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{BR(CES)}	$V_{GE} = 0 \text{ V}, \text{ I}_{C} = 250 \mu\text{A}$	1200	-	-		
		V _{GE} = 15 V, I _C = 50 A	-	1.73	2.33	V	
Collector to omitter voltage	V	V _{GE} = 15 V, I _C = 100 A	-	2.26	-		
Collector to emitter voltage	V _{CE(on)}	V_{GE} = 15 V, I_{C} = 50 A, T_{J} = 125 °C	-	2.02	-		
		V_{GE} = 15 V, I _C = 100 A, T _J = 125 °C	-	2.77	-		
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 3.5$ mA	4.6	5.8	8.0		
Temperature coefficient of threshold voltage	$V_{GE(th)}/\Delta T_J$	V_{CE} = V_{GE} , I_C = 3.5 mA (25 °C to 125 °C)	-	-14.5	-	mV/°C	
	I _{CES}	V _{GE} = 0 V, V _{CE} = 1200 V	-	0.5	75	μA	
Collector to emitter leakage current		$V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}, \text{T}_{\text{J}} = 125 ^{\circ}\text{C}$	-	0.12	-	mA	
Diode reverse breakdown voltage	V _{BR}	I _R = 1 mA	1200	-	-	V	
	V _{FM}	$I_F = 50 \text{ A}, V_{GE} = 0 \text{ V}$	-	2.65	3.55	- V	
Diada famuard valtage drag		$I_F = 100 \text{ A}, V_{GE} = 0 \text{ V}$	-	3.5	-		
Diode forward voltage drop		$I_F = 50 \text{ A}, V_{GE} = 0 \text{ V}, T_J = 125 \text{ °C}$	-	2.82	-		
		I _F = 100 A, V _{GE} = 0 V, T _J = 125 °C	-	3.9	-		
	I _{RM}	V _R = 1200 V	-	4	50	μA	
Diode reverse leakage current		T _J = 125 °C, V _R = 1200 V	-	0.8	-	mA	
Gate to emitter leakage current	I _{GES}	$V_{GE} = \pm 30 \text{ V}$	-	-	± 600	nA	

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg			-	400	-	
Gate to emitter charge (turn-on)	Q _{ge}	$I_{\rm C} = 100$ A, $V_{\rm CC} = 600$ V,	V _{GE} = 15 V	-	120	-	nC
Gate to collector charge (turn-on)	Q _{gc}			-	170	-	
Turn-on switching loss	Eon	$I_{\rm C} = 100 \text{A}, V_{\rm CC} = 600 \text{V},$		-	4.76	-	- mJ
Turn-off switching loss	E _{off}	$V_{GE} = 15 V, R_{g} = 2.2 \Omega,$		-	3.64	-	
Total switching loss	E _{tot}	L = 500 µH, T _J = 25 °C		-	8.4	-	
Turn-on switching loss	Eon			-	6.88	-	
Turn-off switching loss	E _{off}		Energy losses	-	5.66	-	
Total switching loss	E _{tot}	$\label{eq:lc} \begin{array}{l} {\sf I}_{C} = 100 \; {\sf A}, {\sf V}_{CC} = 600 \; {\sf V}, \\ {\sf V}_{GE} = 15 \; {\sf V}, {\sf R}_{g} = 2.2 \; \Omega, \\ {\sf L} = 500 \; \mu {\sf H}, {\sf T}_{\sf J} = 125 \; ^{\circ}{\rm C} \end{array}$	include tail and diode recovery	-	12.54	-	
Turn-on delay time	t _{d(on)}			-	150	-	- ns
Rise time	t _r			-	55	-	
Turn-off delay time	t _{d(off)}			-	164	-	
Fall time	t _f			-	167	-	
Reverse bias safe operating area	RBSOA			Fullsquare		1	
Short circuit safe operating area	SCSOA				10		μs
Diode reverse recovery time	t _{rr}			-	129	-	ns
Diode peak reverse current	I _{rr}	$I_F = 50 \text{ A}, dI_F/dt = 200 \text{ A}/\mu \text{s}, V_R = 200 \text{ V}$ -		-	11	-	А
Diode recovery charge	Q _{rr}			-	710	-	nC
Diode reverse recovery time	t _{rr}			-	208	-	ns
Diode peak reverse current	I _{rr}	I _F = 50 A, dI _F /dt = 200 A/µs, V _R = 200 V, T _J = 125 °C		-	17	-	A
Diode recovery charge	Q _{rr}			-	1768	-	nC

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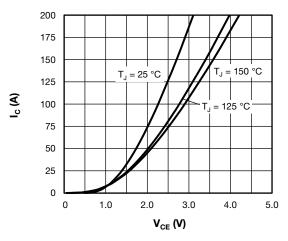
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THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL		MIN.	TYP.	MAX.	UNITS
Junction and storage tem	perature range	T _J , T _{Stg}		-40	-	+150	°C
Junction to case	IGBT	- R _{thJC}		-	-	0.27	
	Diode			-	-	0.37	°C/W
Case to heatsink		R _{thCS}	Flat, greased surface	-	0.05	-	
Weight				-	30	-	g
Mounting torque			Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
			Torque to heatsink	-	-	1.3 (11.5)	Nm (lbf.in)
Case style			SC	DT-227			



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Fig. 1 - Typical IGBT Output Characteristics, $V_{GE} = 15 V$

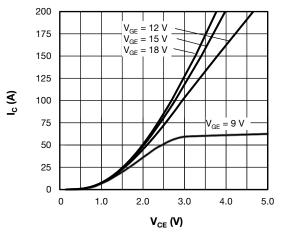
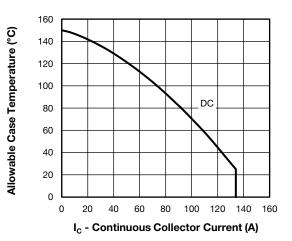
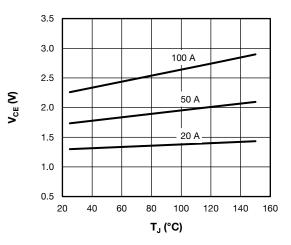


Fig. 2 - Typical IGBT Output Characteristics, $T_J = 125 \text{ }^{\circ}\text{C}$







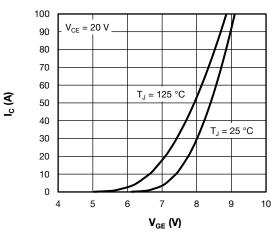


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Fig. 5 - Typical IGBT Transfer Characteristics

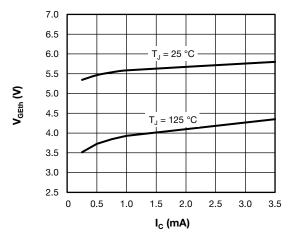


Fig. 6 - Typical IGBT Gate Threshold Voltage

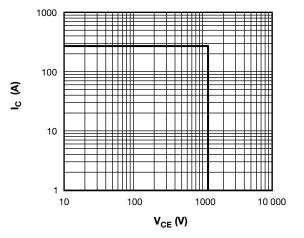


Fig. 7 - IGBT Reverse BIAS SOA T_J = 150 $^\circ\text{C},\,V_{GE}$ = 15 V

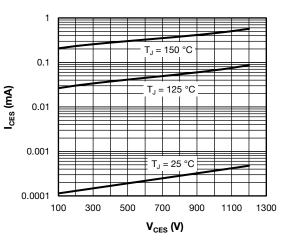


Fig. 8 - Typical IGBT Zero Gate Voltage Collector Current

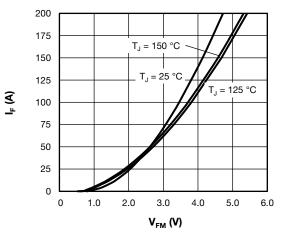
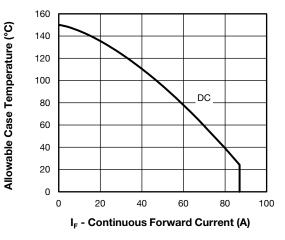


Fig. 9 - Typical Diode Forward Characteristics





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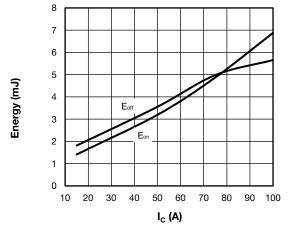


Fig. 11 - Typical IGBT Energy Loss vs. I_C T_J = 125 °C, V_{CC} = 600 V, R_g = 2.2 $\Omega,$ V_{GE} = 15 V, L = 500 μH

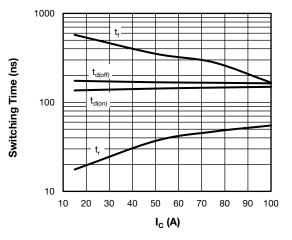


Fig. 12 - Typical IGBT Switching Time vs. I_C T_J = 125 °C, V_{CC} = 600 V, R_g = 2.2 $\Omega,$ V_{GE} = 15 V, L = 500 μH

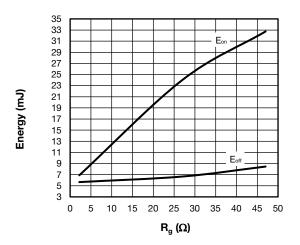


Fig. 13 - Typical IGBT Energy Loss vs. R_g T_J = 125 °C, V_{CC} = 600 V, I_C = 100 A, V_{GE} = 15 V, L = 500 μH

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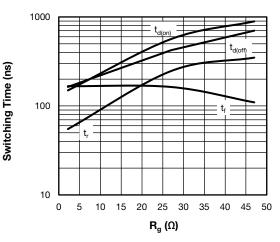


Fig. 14 - Typical IGBT Switching Time vs. R_g T_J = 125 °C, V_{CC} = 600 V, I_C = 100 A, V_{GE} = 15 V, L = 500 μH

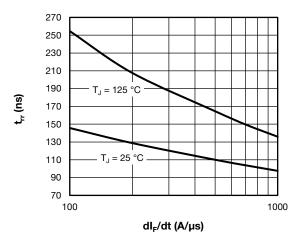


Fig. 15 - Typical Diode Reverse Recovery Time vs. dl_F/dt $V_{rr}=200$ V, l_F=50 A

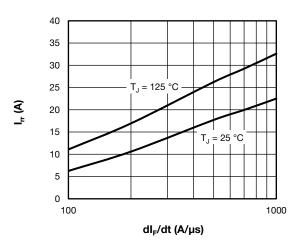


Fig. 16 - Typical Diode Reverse Recovery Current vs. dl_/dt $V_{rr} = 200 V$, l_F = 50 A

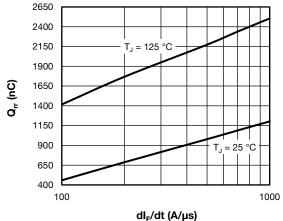
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ai_F/at (A/µS)

Fig. 17 - Typical Diode Reverse Recovery Charge vs. dl_F/dt V_{rr} = 200 V, l_F = 50 A

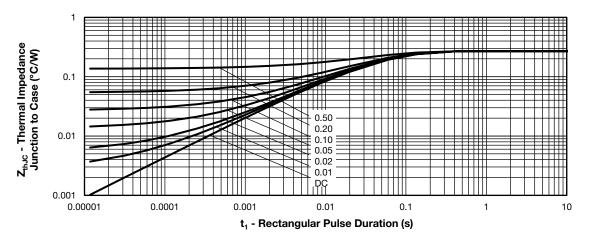
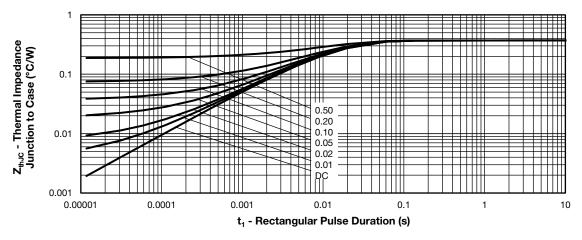


Fig. 18 - Maximum Thermal Impedance Z_{thJC} Characteristics - (IGBT)





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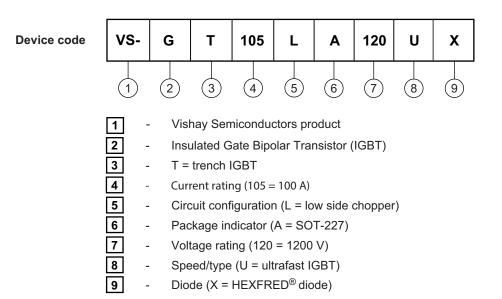
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ORDERING INFORMATION TABLE



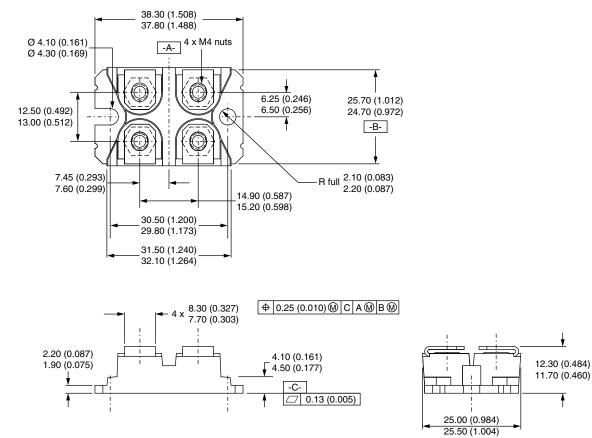
CIRCUIT CONFIGURATION				
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING		
Low side chopper IGBT	L	20-10-03	Lead Assignment 4 1 2	

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				

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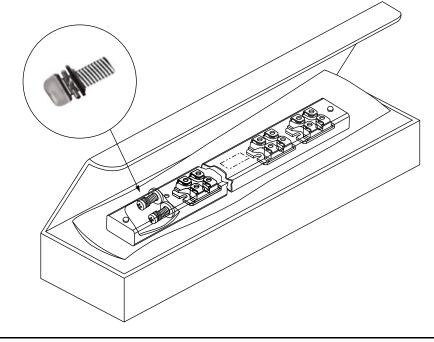
DIMENSIONS in millimeters (inches)



Note

٠ Controlling dimension: millimeter

PACKAGING INFORMATION SOT-227 GENERATION II



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