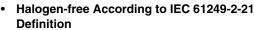




N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ)		
20	0.050 at V _{GS} = 10 V	4.5	3.16 nC		
30	0.080 at V _{GS} = 4.5 V	3.4	3.10110		

FEATURES





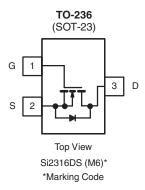
- PWM Optimized
- 100 % R_q tested
- Compliant to RoHS Directive 2002/95/EC



ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

- · Battery Switch
- DC/DC Converter



Ordering Information: Si2316BDS-T1-E3 (Lead (Pb)-free)
Si2316BDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A =$	25 °C, unless oth	erwise noted		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V_{DS}	30	V	
Gate-Source Voltage	Gate-Source Voltage			v
	T _C = 25 °C		4.5	
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C	I _D	3.6	
Continuous Diam Current (1) = 150°C)	T _A = 25 °C		3.9 ^{b, c}	
	T _A = 70 °C		3.13 ^{b, c}	Α
Pulsed Drain Current	I _{DM}	20		
Continuous Source-Drain Diode Current	T _C = 25 °C	1	1.39	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	1.04 ^{b, c}	
	T _C = 25 °C		1.66	
Maximum Power Dissipation	T _C = 70 °C	ь	1.06	w
Maximum Fower Dissipation	T _A = 25 °C	P_{D}	1.25 ^{b, c}	v
	T _A = 70 °C		0.8 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	≤ 5 s	R _{thJA}	80	100	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{th.IF}	60	75] 5/٧٧	

Notes:

- a. Based on T_C = 25 °C.
- b. Surface mounted on 1" x 1" FR4 moard.
- c. t = 5 s.
- d. Maximum under Steady State conditions is 130 °C/W.

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Si2316BDS

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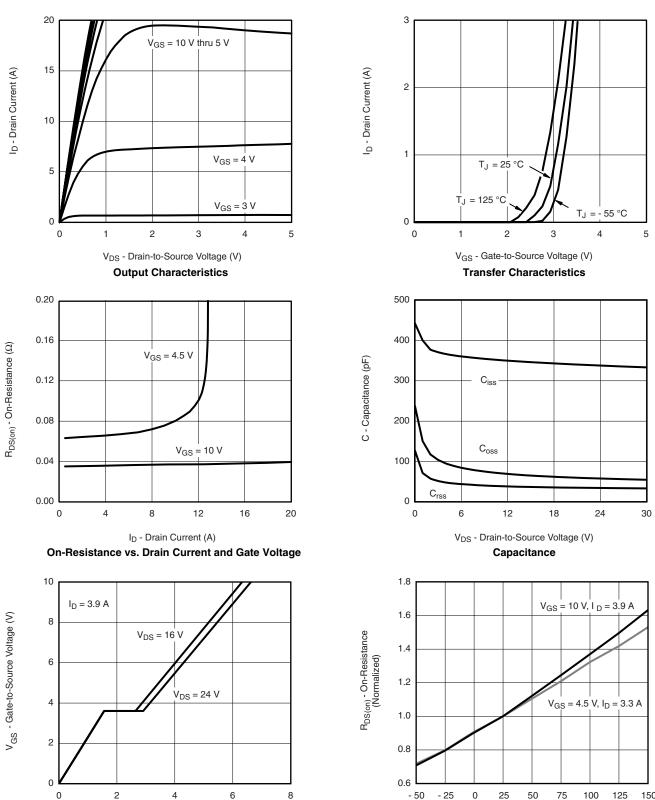
MOSFET SPECIFICATIONS $T_J = 25$ °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{DS} = 0 \text{ V, } I_{D} = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$			23.92		m\//°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		5.2		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1		3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zava Cata Valtaga Drain Current		V _{DS} = 30 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Due to Course On Otata Basistana		$V_{GS} = 10 \text{ V}, I_D = 3.9 \text{ A}$		0.041	1 0.050		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 3.3 \text{ A}$		0.064	0.080	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15V, I _D = 3.9 A		6		S	
Dynamic ^b	•						
Input Capacitance	C _{iss}			350			
Output Capacitance	C _{oss}	.,,,,,		65		1 _	
Reverse Transfer Capacitance	C _{rss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		37		pF	
T. 10 1 0		$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.9 \text{ A}$		6.35	9.6		
Total Gate Charge	Q_g			3.16	4.8		
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 3.9 \text{ A}$		1.56			
Gate-Drain Charge	Q_{gd}			1.1			
Gate Resistance	R _q	f = 1 MHz		2.6	3.9	Ω	
Turn-On Delay Time	t _{d(on)}			4.5	6.75		
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_1 = 4.8 \Omega$		11	16.5	- ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 3.13 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 1 \Omega$		12	18		
Fall Time	t _f			7	10.5		
Turn-On Delay Time	t _{d(on)}			20	30		
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_{I} = 6.25 \Omega$		65	98	1	
Turn-Off Delay Time	t _{d(off)}	$I_D = 2.4 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		11	17	ns	
Fall Time	t _f	-		23	35		
Drain-Source Body Diode Characteristic					l.		
Continuous Source-Drain Diode Current	Is	T _C = 25 °C			1.39	Ι.	
Pulse Diode Forward Current ^a	I _{SM}	-			20	Α	
Body Diode Voltage	V _{SD}	I _S = 2.0 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			10	15	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	1 00A 41/41 400A/ T 07.00		4	6	nC	
Reverse Recovery Fall Time	t _a	$I_F = 2.0 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		6.6			
Reverse Recovery Rise Time	t _b			3.5		ns	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Notes: a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Q_a - Total Gate Charge (nC)

Gate Charge

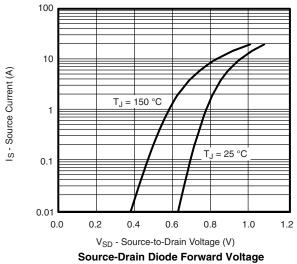
T_J - Junction Temperature (°C)

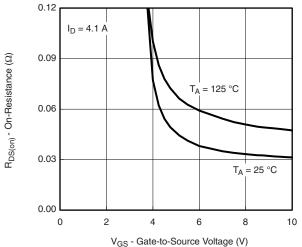
On-Resistance vs. Junction Temperature

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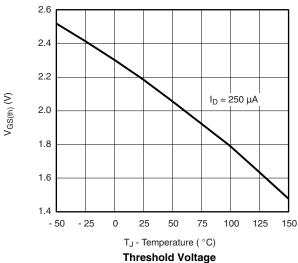
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

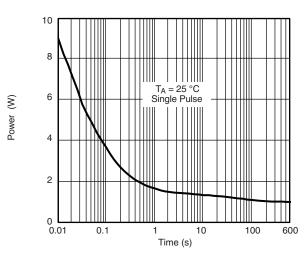




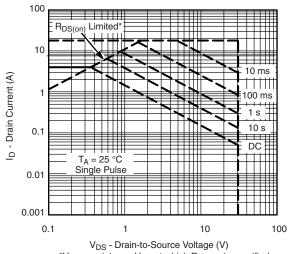




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power

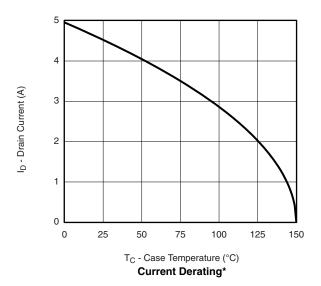


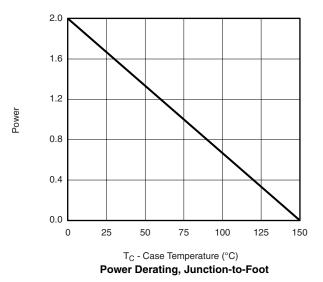
 $^{*}V_{GS}$ - Drain-to-Source voltage (V) $^{*}V_{GS}$ > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area

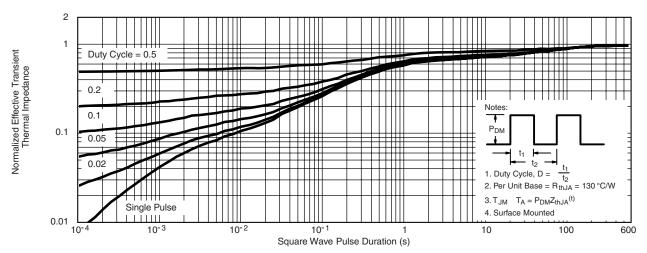


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





*The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



Normalized Thermal Transient Impedance, Junction-to-Ambient

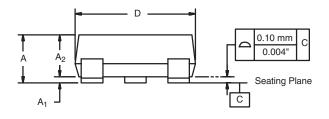
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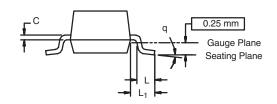
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SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	IETERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95	0.95 BSC		4 Ref	
e ₁	1.90	BSC	0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64	Ref	0.025	S Ref	
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K. 09-	Jul-01				

DWG: 5479

Document Number: 71196 www.vishay.com



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE

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