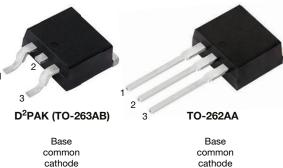
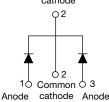
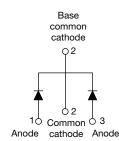


Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 20 A







VS-42CTQ030S-M3

VS-42CTQ030-1-M3

PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 20 A				
V _R	30 V				
V _F at I _F	0.38 V				
I _{RM}	183 mA at 125 °C				
T _J max.	150 °C				
E _{AS}	13 mJ				
Package	D ² PAK (TO-263AB), TO-262AA				
Circuit configuration	Common cathode				

FEATURES

- 150 °C TJ operation
- Center tap configuration
- Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 $^{\circ}\mathrm{C}$
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS A	MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	40	А			
V _{RRM}		30	V			
I _{FSM}	t _p = 5 μs sine	1100	А			
V _F	20 A _{pk} , T _J = 125 °C (per leg)	0.38	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-42CTQ030S-M3 VS-42CTQ030-1-M3	UNITS
Maximum DC reverse voltage	V _R	30	V
Maximum working peak reverse voltage	V _{RWM}	30	V

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ABSOLUTE MAXIMU	JM RATING	GS				
PARAMETER		SYMBOL	TEST COND	ITIONS	VALUES	UNITS
Maximum average	per leg				20	
forward current See fig. 5	per device	I _{F(AV)}	50 % duty cycle at T _C = 121 °(C, rectangular waveform	40	А
Maximum peak one cycle no	on-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load	1100	A
surge current per leg See fig. 7		I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	360	
Non-repetitive avalanche en	ergy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 2.90 mH		13	mJ
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3	А

ELECTRICAL SPECIFICATION	S				
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
		20 A	T _J = 25 °C	0.48	
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A	1j=25 0	0.57	v
See fig. 1	VFM ("	20 A	T _{.1} = 125 °C	0.38	
		40 A	1j = 125 C	0.51	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C		3	mA
See fig. 2	IRM (')	T _J = 125 °C	V _R = Rated V _R	183	
Threshold Voltage	V _{F(TO)}	T _T movimum		0.22	V
Forward slope resistance	r _t	T _J =T _J maximum		6.76	mΩ
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal rang	2840	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 m	8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs	

Note

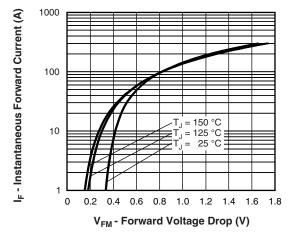
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

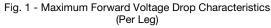
THERMAL - MECHA	NICAL SP	ECIFICAT	IONS		
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS
Maximum junction and stora temperature range	ige	T _J , T _{Stg}		-55 to 150	°C
Maximum thermal resistance, junction to case per leg		Р	DC operation	2.0	
Maximum thermal resistance, junction to case per package		R _{thJC}	Do operation	1.0	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
Approximate weight				0.07	oz.
Mounting torque	minimum			6 (5)	kgf ⋅ cm
Mounting torque	maximum			12 (10)	(lbf · in)
Marking dovice			Case style D ² PAK (TO-263AB)	42CTC	2030S
Marking device			Case style TO-262AA	42CTC	030-1

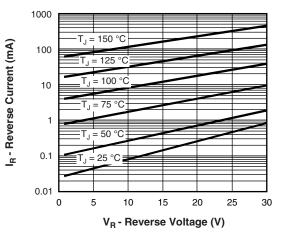
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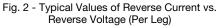


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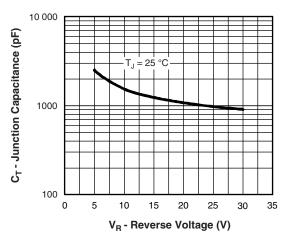
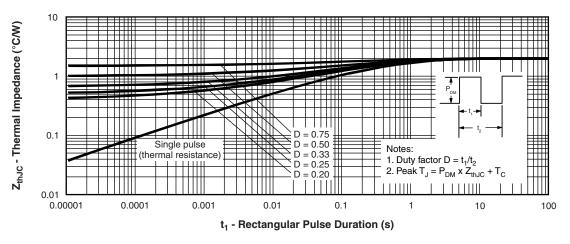


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)



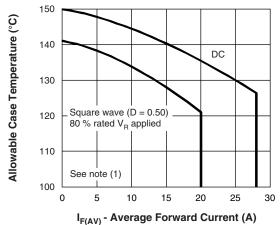


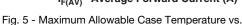
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Average Forward Current (Per Leg)

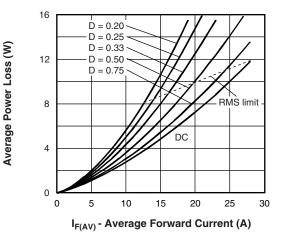


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

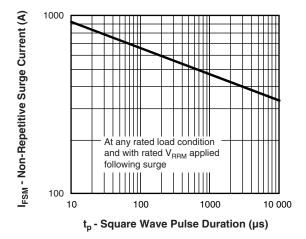


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

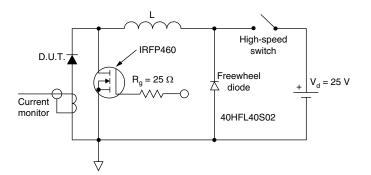


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 10 V

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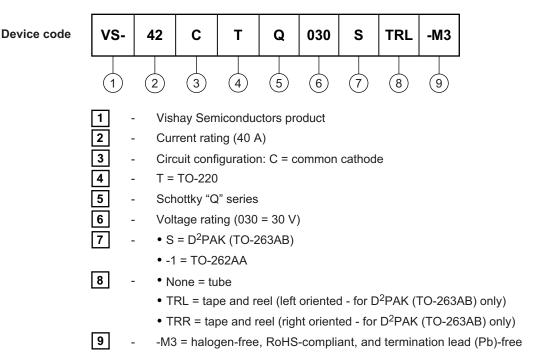
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Vishay Semiconductors

ORDERING INFORMATION TABLE



ORDERING INFORMATION						
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION				
VS-42CTQ030S-M3	50	Antistatic plastic tubes				
VS-42CTQ030STRL-M3	800	13" diameter plastic tape and reel				
VS-42CTQ030STRR-M3	800	13" diameter plastic tape and reel				
VS-42CTQ030-1-M3	50	Antistatic plastic tubes				

	LINKS TO RELATED	DOCUMENTS
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164
Dimensions	TO-262AA	www.vishay.com/doc?96165
Port marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444
Part marking information	TO-262AA	www.vishay.com/doc?95443
Packaging information		www.vishay.com/doc?96424

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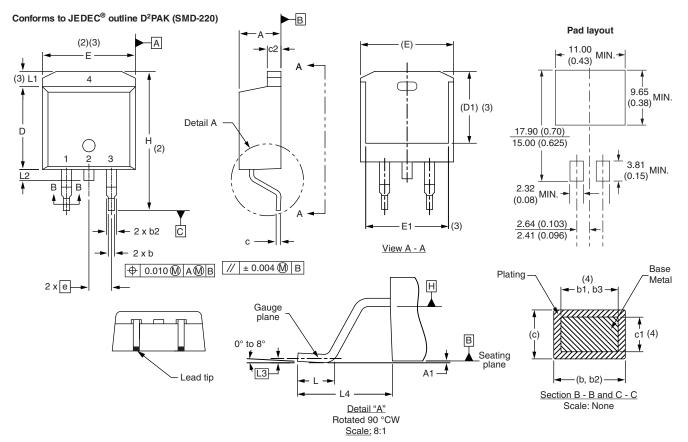


Outline Dimensions

Vishay Semiconductors



DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STINDUL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
с	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010) BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

Notes

 $^{(1)}\,$ Dimensioning and tolerancing per ASME Y14.5 M-1994 $\,$

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

1

Document Number: 95046

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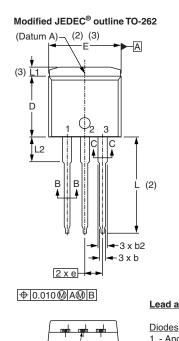


Outline Dimensions

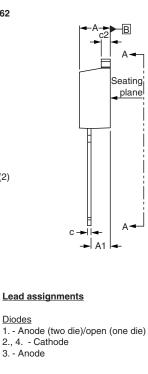
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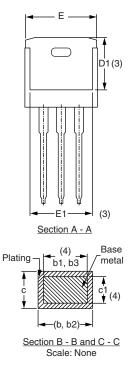
TO-262

DIMENSIONS in millimeters and inches



Lead tip





SYMBOL	MILLIN	IETERS	INC	INCHES			
STINDUL	MIN.	MAX.	MIN.	MAX.	NOTES		
А	4.06	4.83	0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2 1.14	2 1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54 BSC		0.100	BSC			
L	13.46	14.10	0.530	0.555			
L1	-	1.65	-	0.065	3		
L2	3.36	3.71	0.132	0.146			

3. - Anode

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

(4) Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

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Document Number: 95419

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