



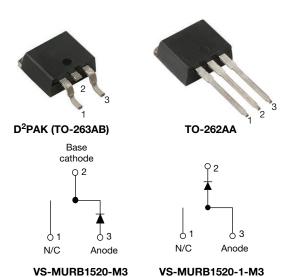
www.vishay.com

Vishay Semiconductors

COMPLIANT

**HALOGEN** 

# Ultrafast Rectifier, 15 A FRED Pt®



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	15 A				
$V_{R}$	200 V				
V <sub>F</sub> at I <sub>F</sub>	0.85 V				
t <sub>rr</sub>	35 ns				
T <sub>J</sub> max.	175 °C				
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA				
Circuit configuration	Single				

#### **FEATURES**

- Ultrafast recovery time
- · Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION / APPLICATIONS**

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Peak repetitive reverse voltage	$V_{RRM}$		200	V		
Average rectified forward current	I <sub>F(AV)</sub>	Total device, rated V <sub>R</sub> , T <sub>C</sub> = 150 °C	15			
Non-repetitive peak surge current	I <sub>FSM</sub>		200	Α		
Peak repetitive forward current	I <sub>FM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 150 °C	30			
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C		

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	200	-	-	V
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 15 A	-	-	1.05	V
Forward voitage	V <sub>F</sub>	I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	-	0.85	
Reverse leakage current	ı	$V_R = V_R$ rated	-	-	10	
neverse leakage current	I <sub>R</sub>	$T_J = 150 ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	1	500	μΑ
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	-	55	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nΗ

Revision: 21-Dec-2021 1 Document Number: 96316



www.vishay.com

# VS-MURB1520-M3, VS-MURB1520-1-M3

# Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
		$I_F = 1.0 \text{ A}, dI_F/dt =$	50 A/μs, V <sub>R</sub> = 30 V	-	-	35			
Reverse recovery time	t <sub>ee</sub>	T <sub>1</sub> = 25 °C		_	22	_	ns		

PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 A, dI_F/dt =$	50 A/μs, V <sub>R</sub> = 30 V	-	ı	35	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		=	22	-	ns
		T <sub>J</sub> = 125 °C		-	39	-	
Peak recovery current	I	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 15 A dI <sub>F</sub> /dt = 200 A/μs	=	1.6	1	Α
reak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = 160 V	=	4.1	-	Α
Reverse recovery charge	$Q_{rr}$	T <sub>J</sub> = 25 °C	VR = 100 V	-	19	-	nC
neverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	90	ı	TIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	-	1.5	
Thermal resistance, junction-to-ambient	R <sub>thJA</sub>		-	-	50	°C/W
Thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2.0	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style D <sup>2</sup> PAK (TO-263AB)	MURB1520			
Ivial Killig Gevice		Case style TO-262		MURB	1520-1	·

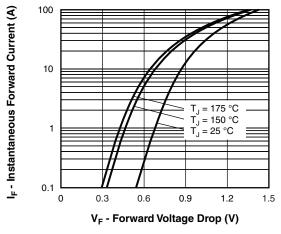


Fig. 1 - Typical Forward Voltage Drop Characteristics

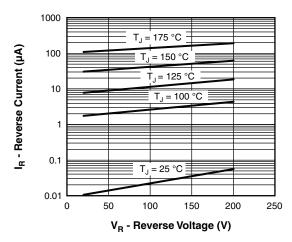


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

Revision: 21-Dec-2021 Document Number: 96316 www.vishay.com

Vishay Semiconductors

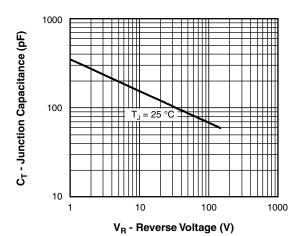


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

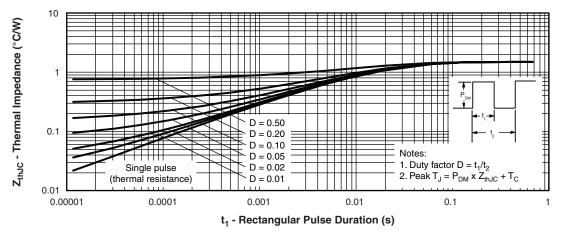


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

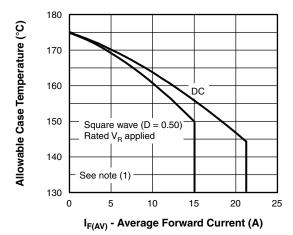


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

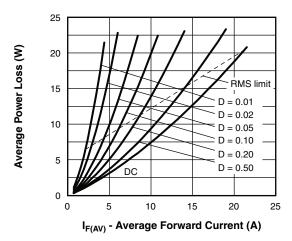


Fig. 6 - Forward Power Loss Characteristics

Revision: 21-Dec-2021 3 Document Number: 96316



# VS-MURB1520-M3, VS-MURB1520-1-M3

## Vishay Semiconductors

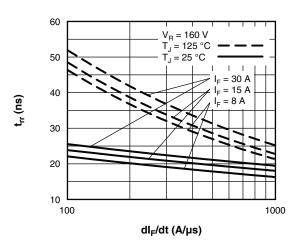


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

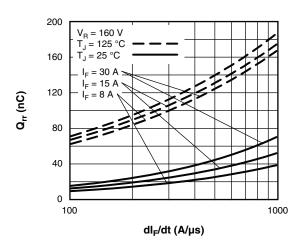
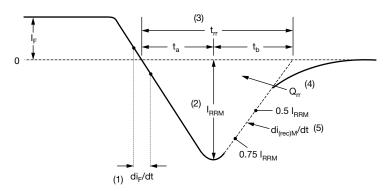


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$ 



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_{r}$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 9 - Reverse Recovery Waveform and Definitions

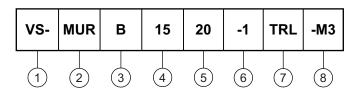


## VS-MURB1520-M3, VS-MURB1520-1-M3

Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

2 - Ultrafast MUR series

3 - B = D<sup>2</sup>PAK (TO-263AB) / TO-262AA

4 - Current rating (15 = 15 A)

5 - Voltage rating (20 = 200 V)

• None = D<sup>2</sup>PAK (TO-263AB)

• -1 = TO-262AA

7 - • None = tube (50 pieces)

• TRL = tape and reel (left oriented, for D2PAK (TO-263AB) package)

• TRR = tape and reel (right oriented, for D<sup>2</sup>PAK (TO-263AB) package)

8 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION			
VS-MURB1520-M3	50	Antistatic plastic tubes			
VS-MURB1520TRR-M3	800	13" diameter plastic tape and reel			
VS-MURB1520TRL-M3	800	13" diameter plastic tape and reel			
VS-MURB1520-1-M3	50	Antistatic plastic tubes			

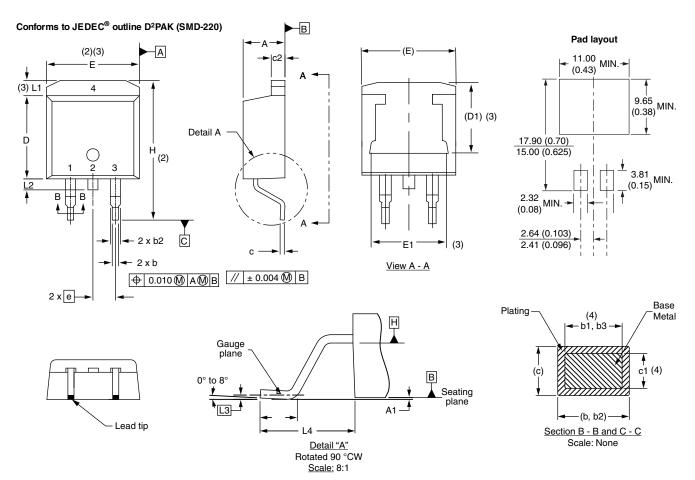
LINKS TO RELATED DOCUMENTS					
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164			
Differisions	TO-262AA	www.vishay.com/doc?96165			
Part marking information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444			
Fait marking imormation	TO-262AA	www.vishay.com/doc?95443			
Packaging information		www.vishay.com/doc?96424			
SPICE model		www.vishay.com/doc?95271			



### Vishay Semiconductors

## D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	2.54 BSC		BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	0.25 BSC		BSC	
L4	4.78	5.28	0.188	0.208	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inches
- (7) Outline conforms to JEDEC® outline TO-263AB

Revision: 13-Jul-17 1 Document Number: 96164

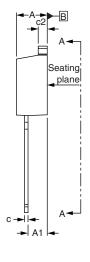


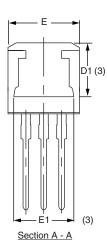
### Vishay Semiconductors

### **TO-262AA**

#### **DIMENSIONS** in millimeters and inches

# Modified JEDEC® outline TO-262 (2) (3) (3) Ď L2 В (2)





0.010 M AM B

2 x e

#### Lead assignments



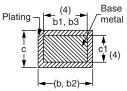
**Diodes** 1. - Anode (two die)/open (one die)

2., 4. - Cathode

3. - Anode

-3 x b2

**-**3 x b



Section B - B and C - C Scale: None

SYMBOL	MILLIN	METERS	INC	INCHES		
	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
Е	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.100	) BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

#### **Notes**

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
  (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- Controlling dimension: inches

  Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)

Revision: 30-Nov-17 Document Number: 96165

## **Legal Disclaimer Notice**



Vishay

### **Disclaimer**

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2022 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED