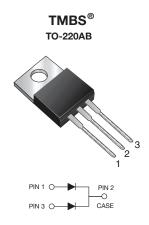


Dual High Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.41 \text{ V}$ at $I_F = 5 \text{ A}$



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 20 A			
V _{RRM}	120 V			
I _{FSM}	250 A			
V _F at I _F = 20 A (125 °C)	0.62 V			
T _J max.	150 °C			
Package	TO-220AB			
Diode variation	Common cathode			

FEATURES

Trench MOS Schottky technology

· Low forward voltage drop, low power losses

· High efficiency operation

HALOGEN • Solder bath temperature 275 °C maximum, 10 s, FREE

per JESD 22-B106 · Material categorization: for definitions of compliance

please see www.vishav.com/doc?99912 TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER		SYMBOL	V40120CI	UNIT
Maximum repetitive peak reverse voltage		V _{RRM}	120	V
Maximum average forward rectified current (fig. 1)	per device		40	А
	per diode	I _F (AV)	20	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	250	А
Operating junction temperature range		T _J ⁽¹⁾	-40 to +150	°C
Storage temperature range		T _{STG}	-55 to +150	

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction to ambient: dP_D/dT_J <1/R_{b,IA}



ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.48	-	V	
	I _F = 10 A			0.57	-		
	I _F = 20 A			0.74	0.82		
	I _F = 5 A	T _A = 125 °C		0.41	-		
	I _F = 10 A			0.52	-		
	I _F = 20 A			0.62	0.70		
Reverse current per diode	V - 00 V	T _A = 25 °C	T _A = 25 °C		0.01	-	
	$V_R = 90 \text{ V}$	T _A = 125 °C	I _R ⁽²⁾	9.0	-	A	
	V _R = 120 V	T _A = 25 °C		-	0.7	mA	
		T _A = 125 °C		20.0	38		
Junction capacitance	4 V, 1MHz		CJ	2400	-	pF	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	V40120CI	UNIT		
Typical thermal resistance per device	$R_{ heta JC}$	1.7	°C/W		

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V40120CI-M3/P	1.88	Р	50/tube	Tube		



RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

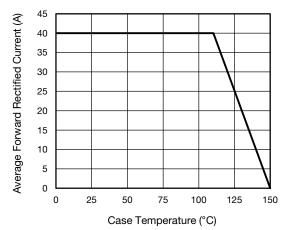


Fig. 1 - Forward Current Derating Curve

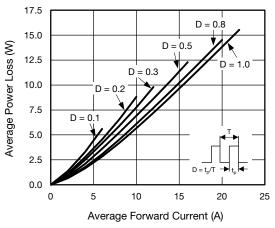


Fig. 2 - Forward Power Loss Characteristics Per Diode

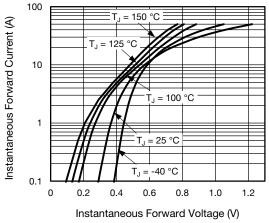


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

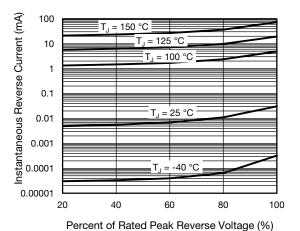


Fig. 4 - Typical Reverse Characteristics Per Diode

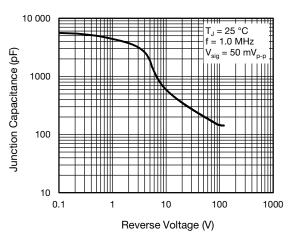


Fig. 5 - Typical Junction Capacitance

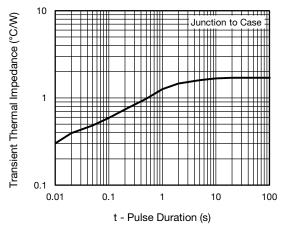
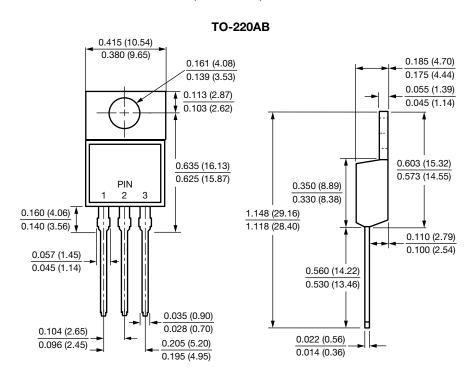


Fig. 6 - Typical Transient Thermal Impedance Per Device



PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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