

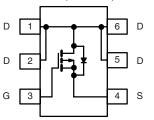
Si1417DH

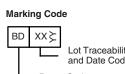
**Vishay Siliconix** 

# P-Channel 12-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	r <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
	0.085 at V <sub>GS</sub> = - 4.5 V	- 3.3		
- 12	0.115 at V <sub>GS</sub> = - 2.5 V	- 2.9		
	0.160 at V <sub>GS</sub> = - 1.8 V	- 2.4		







Top View Ordering Information: Si1417DH-T1 Si1417DH-T1-E3 (Lead (Pb)-free)

#### **FEATURES**

- TrenchFET<sup>®</sup> Power MOSFETS: 1.8 V Rated
- Thermally Enhanced SC-70 Package

#### **APPLICATIONS**

- · Load Switching
- PA Switch
- Level Switch



COMPLIANT

### Lot Traceability and Date Code Part # Code

ABSOLUTE MAXIMUM RATINGS	T <sub>A</sub> = 25 °C, unle	ess otherwise	noted			
Parameter		Symbol	5 sec	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 12		V	
Gate-Source Voltage		V <sub>GS</sub>	± 8			
	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 3.3	- 2.7		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 85 °C		- 2.4	- 1.9	•	
Pulsed Drain Current		I <sub>DM</sub>	- 8		A	
Continuous Diode Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	- 1.4	- 0.9		
	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	1.56	1.0	14/	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 85 °C		0.81	0.52	W	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum lumation to Ambienta	$t \le 5 \text{ sec}$	- R <sub>thJA</sub>	60	80	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		100	125	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	34	45	

Notes:

a. Surface Mounted on 1" x 1" FR4 Board.

\* Pb containing terminations are not RoHS compliant, exemptions may apply.



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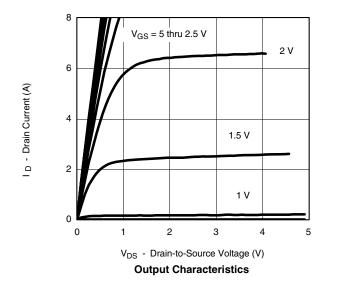
<b>SPECIFICATIONS</b> $T_J = 25$ Parameter	Symbol	Test Conditions	Тур	Max	Unit		
Static	5,		Min	-76	IIIux	Unit	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.45		- 0.8	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -9.6 \text{ V}, V_{GS} = 0 \text{ V}$			- 1		
		$V_{DS}$ = - 9.6 V, $V_{GS}$ = 0 V, $T_{J}$ = 85 °C			- 5	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 4			А	
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	$V_{GS}$ = - 4.5 V, I <sub>D</sub> = - 3.3 A		0.070	0.085	Ω	
		$V_{GS} = -2.5 \text{ V}, \text{ I}_{D} = -2.9 \text{ A}$		0.095	0.115		
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 1.0 A		0.133	0.160		
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -3.3 \text{ A}$		8		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = - 1.4 A, V <sub>GS</sub> = 0 V		- 0.80	- 1.1	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			7.0	10.5		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 6 V, $V_{GS}$ = - 4.5 V, $I_D$ = - 3.3 A		1.3		nC	
Gate-Drain Charge	Q <sub>gd</sub>			1.5			
Turn-On Delay Time	t <sub>d(on)</sub>			18	30		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 6 V, $R_L$ = 6 $\Omega$		28	45		
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong$ - 1 A, $\text{V}_\text{GEN}$ = - 4.5 V, $\text{R}_\text{G}$ = 6 $\Omega$		41	65	ns	
Fall Time	t <sub>f</sub>			60	90		

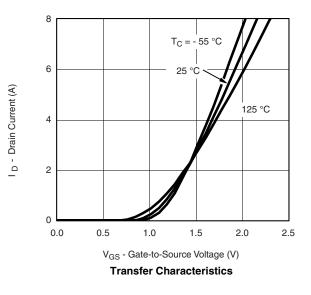
Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





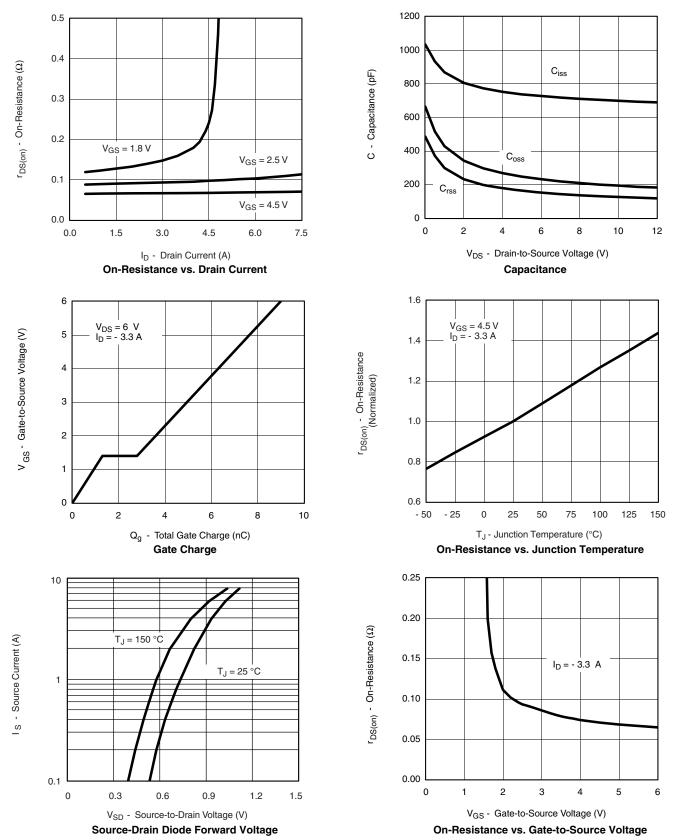




# Si1417DH

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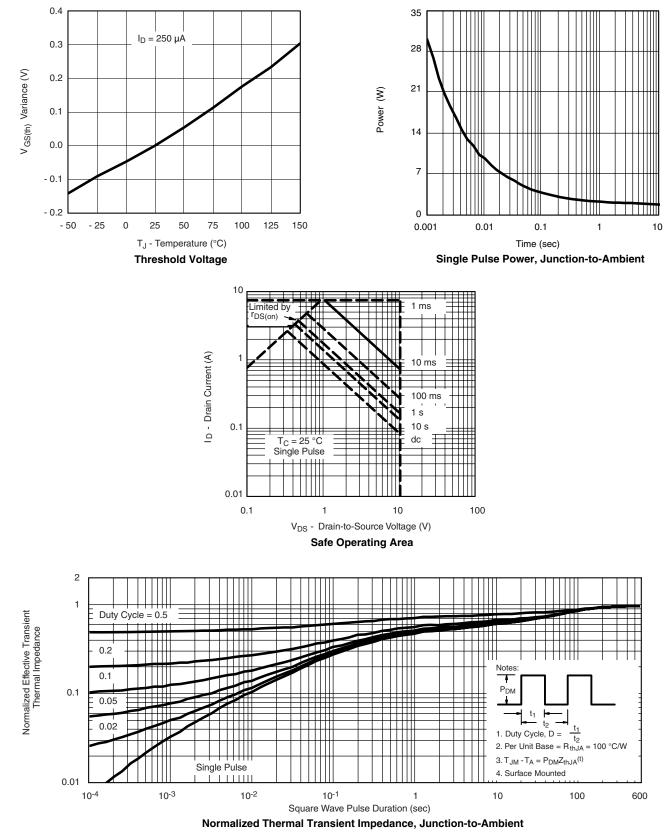
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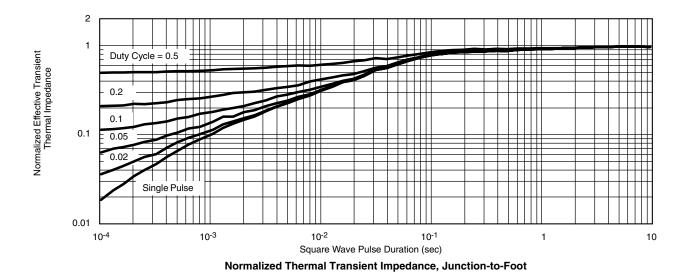






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