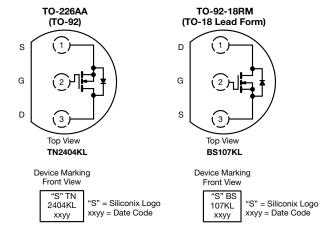


Vishay Siliconix

## N-Channel 240 V (D-S) MOSFET

| PRODUCT SUMMARY     |                        |                             |                         |                       |                          |  |
|---------------------|------------------------|-----------------------------|-------------------------|-----------------------|--------------------------|--|
| Part<br>Number      | V <sub>DS</sub><br>(V) | R <sub>DS(on)</sub><br>(Ω)  | V <sub>GS(th)</sub> (A) | I <sub>D</sub><br>(A) | Q <sub>g</sub><br>(Typ.) |  |
| TN2404K             |                        |                             |                         | 0.2                   |                          |  |
| TN2404K,<br>BS107KL | 240                    | 4 at V <sub>GS</sub> = 10 V | 0.8 to 2                | 0.3                   | 4.87 nC                  |  |



#### **FEATURES**

Low On-Resistance: 4  $\Omega$ 

Secondary Breakdown Free: 260 V

Low Power/Voltage Driven

Low Input and Output Leakage

**Excellent Thermal Stability** 

Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

## COMPLIANT HALOGEN FREE

#### **APPLICATIONS**

- · High-Voltage Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Transistors, etc.
- Telephone Mute Switches, Ringer Circuits
- Power Supply, Converters
- Motor Control

### **BENEFITS**

- · Low Offset Voltage
- **Full-Voltage Operation**
- Easily Driven Without Buffer
- Low Error Voltage
- No High-Temperature "Run-Away"

|     | (SOT-23)            |     |
|-----|---------------------|-----|
| G 1 |                     | 3 D |
|     | Top View<br>TN2404K |     |

TO-236

Marking Code: K1ywl K1 = Part Number Code for TN2404K y = Year Code w = Week Code I = Lot Traceability

| ORDRING INFORMATION |                      |                                 |  |  |  |
|---------------------|----------------------|---------------------------------|--|--|--|
| Standard Partnumber | Ordering Part Number | Option                          |  |  |  |
| TN2404K             | TN2404K-T1-E3        | Lead (Pb) free                  |  |  |  |
|                     | TN2404K-T1-GE3       | Lead (Pb) free and Halogen free |  |  |  |
| TN2404KL            | TN2404KL-TR1-E3      | With Tape and Reel              |  |  |  |
| BS107KL             | BS107KL-TR1-E3       | Spool Option                    |  |  |  |

| <b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted) |                        |                                   |                  |                  |        |  |
|--|------------------------|-----------------------------------|------------------|------------------|--------|--|
| Parameter  |                        | Symbol                            | TN2404K          | TN2404KL/BS107KL | Symbol |  |
| Drain-Source Voltage   |                        | $V_{DS}$                          | 24               | 240              |        |  |
| Gate-Source Voltage  |                        | V <sub>GS</sub>                   | ±                | V                |        |  |
| Continuous Drain Current (T <sub>.I</sub> = 150 °C)                              | T <sub>A</sub> = 25 °C | - I <sub>D</sub>                  | 0.2              | 0.3              |        |  |
| Continuous Brain Carron (1) = 100 °C)  | T <sub>A</sub> = 70 °C |                                   | 0.16             | 0.25             | Α      |  |
| Pulsed Drain Current (t = 300 μs)  |                        | I <sub>DM</sub>                   | 0.8              | 1.4              |        |  |
| Maximum Power Dissipation  | T <sub>A</sub> = 25 °C | P <sub>D</sub>                    | 0.36             | 0.8              | W      |  |
| Waximum Fower Dissipation  | T <sub>A</sub> = 70 °C | ' D                               | 0.23             | 0.51             | **     |  |
| Thermal Resistance Junction-to-Ambient   |                        | $R_{thJA}$                        | 350 <sup>b</sup> | 156              | °C/W   |  |
| Operating Junction and Storage Temperature Range                                 |                        | T <sub>J</sub> , T <sub>stg</sub> | - 55 t           | - 55 to 150      |        |  |

#### Notes:

a. Pulse width limited by maximum junction temperature.

b. Surface mounted on an FR4 board.

Document Number: 72225 S12-1767-Rev. C, 23-Jul-12 For technical questions, contact: pmostechsupport@vishav.com

## TN2404K/TN2404KL/BS107KL

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| <b>SPECIFICATIONS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted) |                     |  |   |      |       |      |
|--|---------------------|--|---|------|-------|------|
| Parameter  | Symbol              | Test Conditions  | Conditions Limits     Min.   Typ. <sup>a</sup> Max. |      | nits  |      |
| Parameter  | Symbol              | rest Conditions  |   |      | Max.  | Unit |
| Static   |                     |  |   |      |       |      |
| Drain-Source Breakdown Voltage   | V <sub>DS</sub>     | $V_{GS} = 0 \text{ V}, I_{D} = 100 \mu\text{A}$                        | 240   | 257  |       | V    |
| Gate-Source Threshold Voltage  | V <sub>GS(th)</sub> | $V_{DS} = V_{GS}, I_{D} = 250 \mu A$                                   | 0.8   | 1.65 | 2     | V    |
| Gate-Source Leakage  | I <sub>GSS</sub>    | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$                      |   |      | ± 100 | nA   |
| Zero Gate Voltage Drain Current  | 1                   | V <sub>DS</sub> = 192 V, V <sub>GS</sub> = 0 V                         |   |      | 1     |      |
| Zero Gate Voltage Drain Current  | I <sub>DSS</sub>    | V <sub>DS</sub> = 192 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C |   |      | 10    | _ v  |
| On Olate Dunin Comment   | le co               | V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 10 V                         | 0.8   |      |       | ۸    |
| On-State Drain Current <sup>a</sup>                                    | I <sub>D(on)</sub>  | V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 4.5 V                        | 0.5   |      |       | A    |
|  |                     | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.3 A                         |   | 2.2  | 4     |      |
| Drain-Source On-State Resistance <sup>a</sup>                          | R <sub>DS(on)</sub> | $V_{GS} = 4.5 \text{ V}, I_D = 0.2 \text{ A}$                          |   | 2.3  | 4     | Ω    |
|  |                     | $V_{GS} = 2.5 \text{ V}, I_D = 0.1 \text{ A}$                          |   | 2.4  | 6     |      |
| Forward Transconductance <sup>a</sup>                                  | 9 <sub>fs</sub>     | V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.3 A                         |   | 1.6  |       | S    |
| Diode Forward Voltage  | V <sub>SD</sub>     | $V_{GS} = 0 \text{ V, I}_{S} = 0.3 \text{ A}$                          |   | 0.8  | 1.2   | V    |
| Dynamic <sup>b</sup>   |                     |  |   |      |       |      |
| Total Gate Charge  | Qg                  |  |   | 4.87 | 8     |      |
| Gate-Source Charge   | Q <sub>gs</sub>     | $V_{DS} = 192 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 0.5 \text{ A}$   |   | 0.56 |       | nC   |
| Gate-Drain Charge  | Q <sub>gd</sub>     |  |   | 1.53 |       |      |
| Turn-On Delay Time   | t <sub>d(on)</sub>  |  |   | 5    | 10    |      |
| Rise Time  | t <sub>r</sub>      | $V_{DD} = 60 \text{ V}, R_{L} = 200 \Omega$                            |   | 12   | 20    | no   |
| Turn-Off Delay Time  | t <sub>d(off)</sub> | $I_D \cong 0.3 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 25 \Omega$     |   | 35   | 60    | ns   |
| Fall Time  | t <sub>f</sub>      |  |   | 16   | 25    |      |

#### Notes:

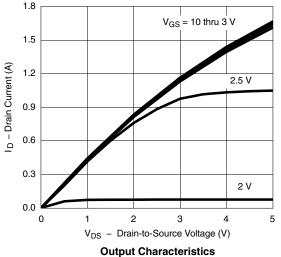
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

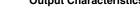
a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%$ 

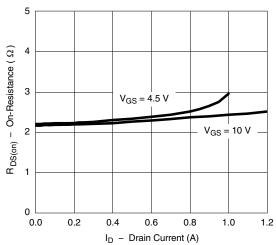
b. Guaranteed by design, not subject to production testing.



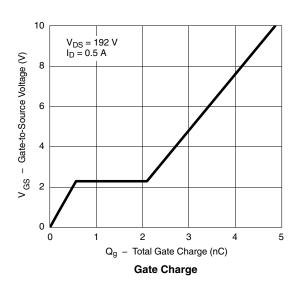
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

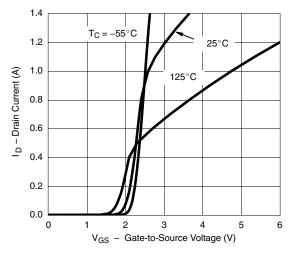




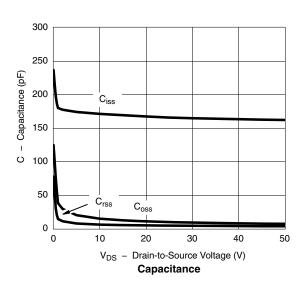


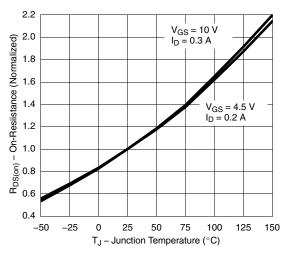
On-Resistance vs. Drain Current





**Transfer Characteristics** 





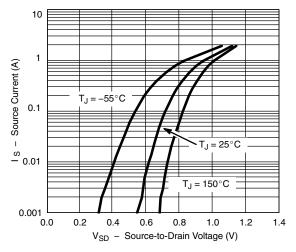
On-Resistance vs. Junction Temperature

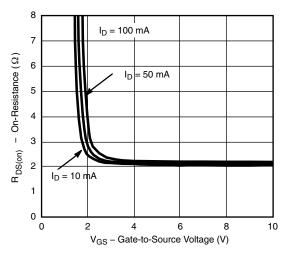
## TN2404K/TN2404KL/BS107KL

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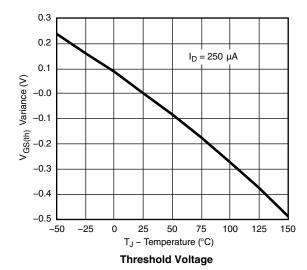
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





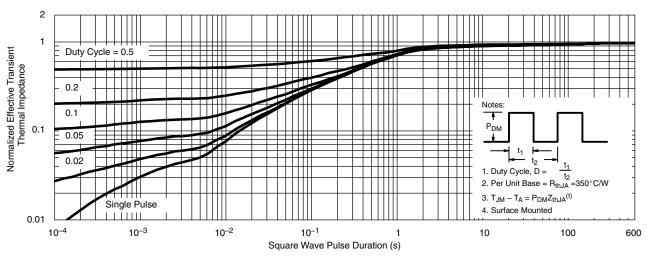
Source-Drain Diode Forward Voltage

On-Resistance vs. Gate-to-Source Voltage

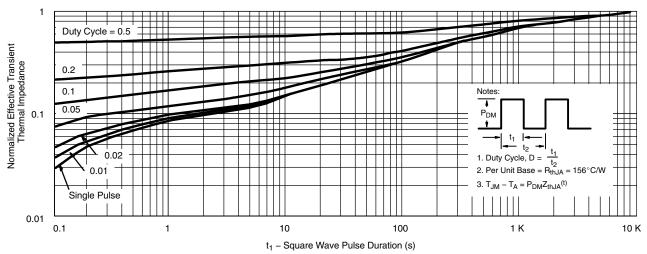




#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient (TO-236, TN2404K only)



Normalized Thermal Transient Impedance, Junction-to-Ambient (TO-226AA, TN2404KL and TO-92-18RM, BS107KL only)

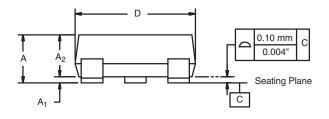
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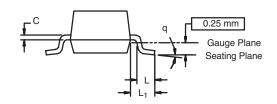
Document Number: 72225 S12-1767-Rev. C, 23-Jul-12

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## SOT-23 (TO-236): 3-LEAD







| Dim                      | MILLIMETERS |                     | INCHES |         |  |  |
|--------------------------|-------------|---------------------|--------|---------|--|--|
|                          | Min         | Max                 | Min    | Max     |  |  |
| Α                        | 0.89        | 1.12                | 0.035  | 0.044   |  |  |
| A <sub>1</sub>           | 0.01        | 0.10                | 0.0004 | 0.004   |  |  |
| A <sub>2</sub>           | 0.88        | 1.02                | 0.0346 | 0.040   |  |  |
| b                        | 0.35        | 0.50                | 0.014  | 0.020   |  |  |
| С                        | 0.085       | 0.18                | 0.003  | 0.007   |  |  |
| D                        | 2.80        | 3.04                | 0.110  | 0.120   |  |  |
| E                        | 2.10        | 2.64                | 0.083  | 0.104   |  |  |
| E <sub>1</sub>           | 1.20        | 1.40                | 0.047  | 0.055   |  |  |
| е                        | 0.95        | 0.95 BSC 0.0374 Ref |        | 4 Ref   |  |  |
| e <sub>1</sub>           | 1.90 BSC    |                     | 0.074  | 748 Ref |  |  |
| L                        | 0.40        | 0.60                | 0.016  | 0.024   |  |  |
| L <sub>1</sub>           | 0.64 Ref    |                     | 0.025  | i Ref   |  |  |
| S                        | 0.50 Ref    |                     | 0.020  | ) Ref   |  |  |
| q                        | 3°          | 8°                  | 3°     | 8°      |  |  |
| ECN: S-03946-Rev. K. 09- | Jul-01      |                     |        |         |  |  |

DWG: 5479

Document Number: 71196 www.vishay.com



#### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE

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