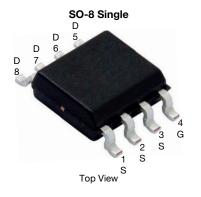


Vishay Siliconix

Automotive P-Channel 30 V (D-S) 175 °C MOSFET

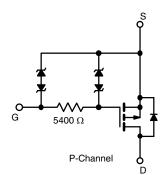
PRODUCT SUMMARY				
V _{DS} (V)	-30			
$R_{DS(on)} (\Omega)$ at $V_{GS} = -10 V$	0.0085			
$R_{DS(on)} (\Omega)$ at $V_{GS} = -4.5 V$	0.0200			
I _D (A)	-22			
Configuration	Single			



FEATURES

- TrenchFET[®] power MOSFET
- AEC-Q101 qualified
- ESD Protection: 3000 V
- 100 % UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and Halogen-free	SQ4483BEEY-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	-30	V	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current	T _C = 25 °C	- I _D -	-22		
	T _C = 125 °C		-13		
Continuous Source Current (Diode Conduction)		IS	-6	А	
Pulsed Drain Current ^a		I _{DM}	-84		
Single Pulse Avalanche Current	L = 10 mH	I _{AS}	-7		
Single Pulse Avalanche Energy	L = 10 mH	E _{AS}	245	mJ	
Maximum Power Dissipation ^a	T _C = 25 °C	PD	7	W	
	T _C = 125 °C	гD	2	VV	
Operating Junction and Storage Temperature Range	ge	T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^b	R _{thJA}	85	°C/W
Junction-to-Foot (Drain)		R _{thJF}	21	0/₩

Notes

a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$

b. When mounted on 1" square PCB (FR4 material).

SPending-Rev. C, 24-Jul-15

Document Number: 67097

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SQ4483BEEY

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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static	-	-					
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = -250 μA		-30	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$		-2.0	-2.5	
Gate-Source Leakage	1	V _{DS} =	$V_{DS} = 0 V, V_{GS} = \pm 20 V$ $V_{DS} = 0 V, V_{GS} = \pm 12 V$		-	± 1	mA
Gale-Source Leakage	I _{GSS}	V _{DS} =			-	± 2	
Zero Gate Voltage Drain Current		$V_{GS} = 0 V$	$V_{DS} = -30 V$	I	-	-1	μA
	I _{DSS}	$V_{GS} = 0 V$	V_{DS} = -30 V, T_J = 125 °C	-	-	-50	
		$V_{GS} = 0 V$	V_{DS} = -30 V, T_J = 175 °C	I	-	-150	
On-State Drain Current ^a	I _{D(on)}	$V_{GS} = -10 V$	$V_{DS} \le -5 V$	-30	-	-	А
Drain-Source On-State Resistance ^a		$V_{GS} = -10 V$	I _D = -10 A	-	0.0070	0.0085	Ω
	P	$V_{GS} = -10 V$	$I_D = -10 \text{ A}, \text{ T}_J = 125 ^\circ\text{C}$	I	-	0.0130	
	R _{DS(on)}	$V_{GS} = -10 V$	I _D = -10 A, T _J = 175 °C	-	-	0.0150	
		$V_{GS} = -4.5 V$	I _D = -7 A	-	0.0160	0.0200	
Forward Transconductance ^b	g _{fs}	V _{DS} = -10 V, I _D = -10 A		I	32	-	S
Dynamic ^b							
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	$V_{DS} = -15 V, f = 1 MHz$	I	712	890	pF
Total Gate Charge ^c	Qg			I	75	113	nC
Gate-Source Charge ^c	Q _{gs}	$V_{GS} = -10 V$	$V_{DS} = -15 \text{ V}, I_{D} = -10 \text{ A}$	-	9.5	-	
Gate-Drain Charge ^c	Q _{gd}			-	19	-	
Turn-On Delay Time ^c	t _{d(on)}	$V_{DD} = -15 \text{ V}, \text{ R}_{\text{L}} = 1.5 \Omega$ $\text{I}_{\text{D}} \cong -10 \text{ A}, \text{ V}_{\text{GEN}} = -10 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		I	38	57	μs
Rise Time ^c	t _r			-	82	123	
Turn-Off Delay Time ^c	t _{d(off)}			-	134	201	
Fall Time ^c	t _f			-	178	214	
Source-Drain Diode Ratings and Chara	acteristics ^b						
Pulsed Current ^a	I _{SM}			-	-	-84	А
Forward Voltage	V _{SD}	I _F = -3 A, V _{GS} = 0 V		-	-0.75	-1.2	V

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

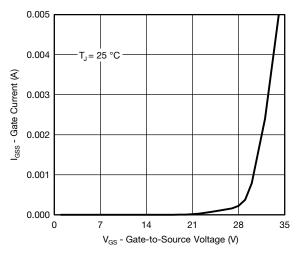
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

2

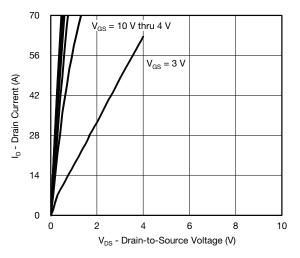


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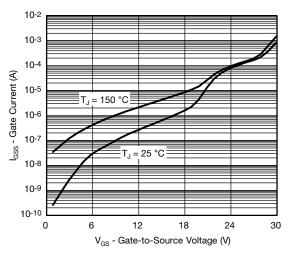
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



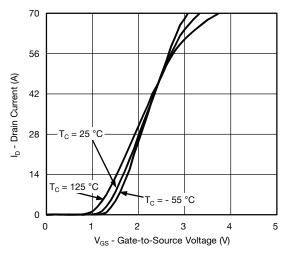
Gate Current vs. Gate-Source Voltage



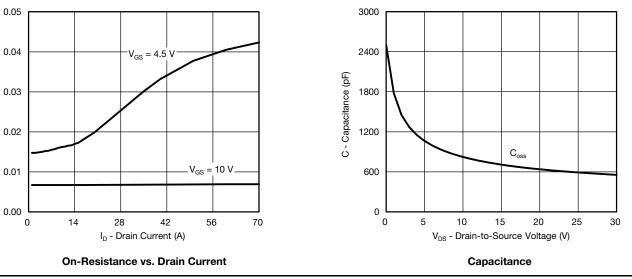
Output Characteristics



Gate Current vs. Gate-Source Voltage







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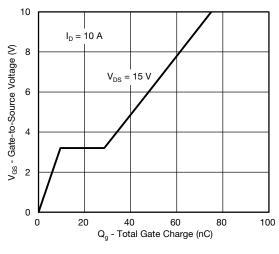
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 $R_{DS(on)}$ - On-Resistance (Ω)

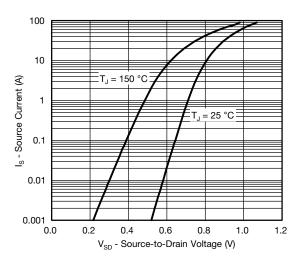


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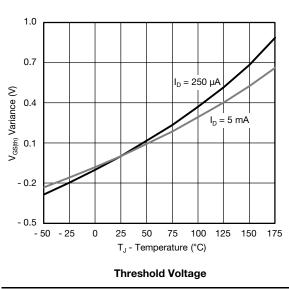
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





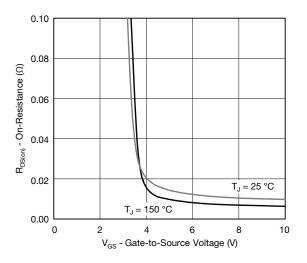




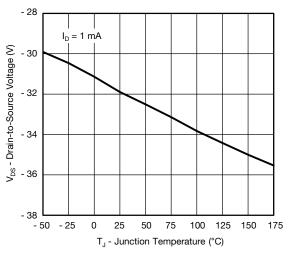


2.0 I_D = 10 A R_{DS(on)} - On-Resistance (Normalized) 1.7 V_{GS} = 10 V 1.4 V_{GS} = 4.5 V 1.1 0.8 0.5 - 25 - 50 0 25 50 75 100 125 150 175 T_J - Junction Temperature (°C)

On-Resistance vs. Junction Temperature







Drain Source Breakdown vs. Junction Temperature

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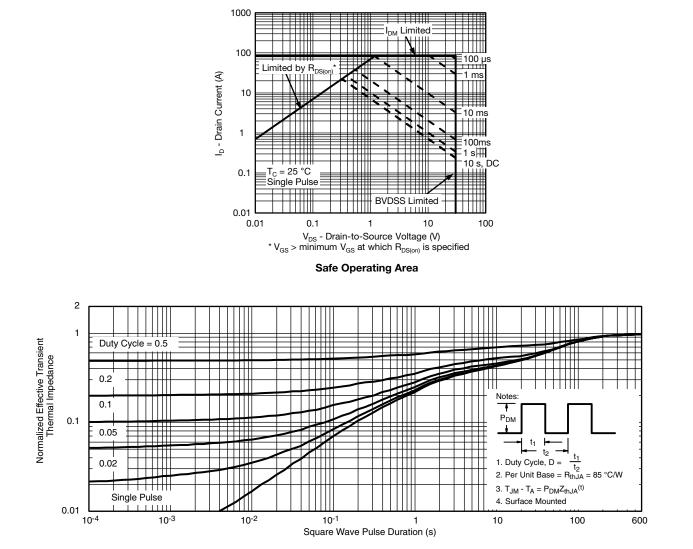
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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



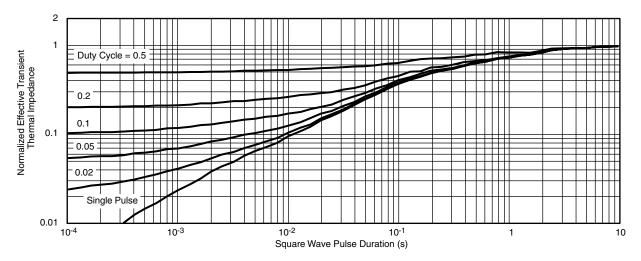
Normalized Thermal Transient Impedance, Junction-to-Ambient

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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

· The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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