

## P-Channel 12 V (D-S) MOSFET

PRODUCT SUMMARY			
$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ.)
- 12	0.156 at $V_{GS} = - 4.5$ V	1.18	6.7 nC
	0.190 at $V_{GS} = - 2.5$ V	1.07	
	0.245 at $V_{GS} = - 1.8$ V	0.49	

### FEATURES

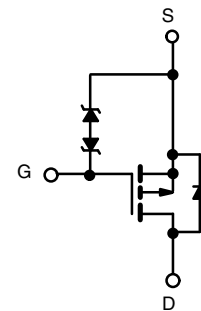
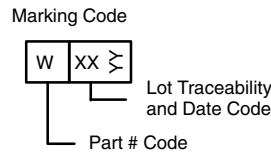
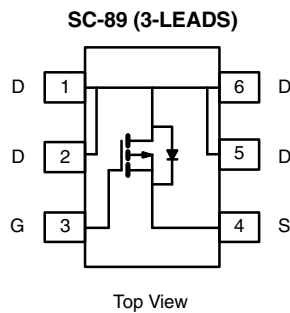
- TrenchFET<sup>®</sup> Power MOSFET
- 100 %  $R_g$  Tested
- Material categorization:  
For definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

### APPLICATIONS

- Load Switch for Portable Devices



Ordering Information: Si1065X-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise noted)				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	$V_{DS}$	- 12	V	
Gate-Source Voltage	$V_{GS}$	$\pm 8$		
Continuous Drain Current ( $T_J = 150$ °C)	$I_D$	$T_A = 25$ °C	- 1.18 <sup>b, c</sup>	A
		$T_A = 70$ °C	- 0.94 <sup>b, c</sup>	
Pulsed Drain Current	$I_{DM}$	- 8		
Continuous Source-Drain Diode Current	$I_S$	- 0.2 <sup>b, c</sup>		
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25$ °C	0.236 <sup>b, c</sup>	W
		$T_A = 70$ °C	0.151 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a, b</sup>	$R_{thJA}$	$t \leq 5$ s	440	530	°C/W
		Steady State State	540	650	

Notes:

- Maximum under steady state conditions is 650 °C/W.
- Surface mounted on 1" x 1" FR4 board.
- $t = 5$  s.

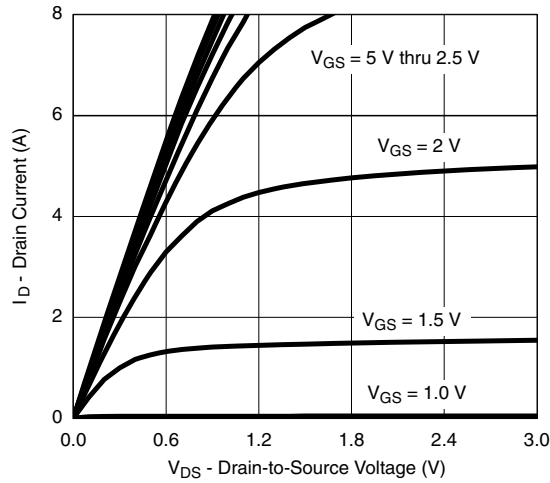
SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 12			V
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = - 250 μA		- 8.47		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>			2.33		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 0.45		- 0.95	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 8 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 12 V, V <sub>GS</sub> = 0 V			- 1	nA
		V <sub>DS</sub> = - 12 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C			- 10	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = - 4.5 V	- 8			A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 1.18 A		0.108	0.156	Ω
		V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 1.07 A		0.131	0.190	
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 0.49 A		0.158	0.245	
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> = - 6 V, I <sub>D</sub> = - 1.18 A		5.18		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = - 6 V, V <sub>GS</sub> = 0 V, f = 1 MHz		480		pF
Output Capacitance	C <sub>oss</sub>			190		
Reverse Transfer Capacitance	C <sub>rss</sub>			145		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = - 6 V, V <sub>GS</sub> = - 5 V, I <sub>D</sub> = - 1.18 A		7.2	10.8	nC
				6.7	10.1	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = - 6 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 1.18		0.84		
Gate-Drain Charge	Q <sub>gd</sub>			2.7		
Gate Resistance	R <sub>g</sub>	f = 1 MHz		10	15	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = - 6 V, R <sub>L</sub> = 6.32 Ω I <sub>D</sub> ≅ - 0.95 A, V <sub>GEN</sub> = - 4.5 V, R <sub>g</sub> = 1 Ω		13	19.5	ns
Rise Time	t <sub>r</sub>			27	40.5	
Turn-Off Delay Time	t <sub>d(off)</sub>			45	67.5	
Fall Time	t <sub>f</sub>			27	40.5	
<b>Drain-Source Body Diode Characteristics</b>						
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				8	A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 0.63 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 0.7 A, di/dt = 100 A/μs		29.2	44	nC
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			10.22	15.3	ns
Reverse Recovery Fall Time	t <sub>a</sub>			13.7		
Reverse Recovery Rise Time	t <sub>b</sub>			15.5		

Notes:

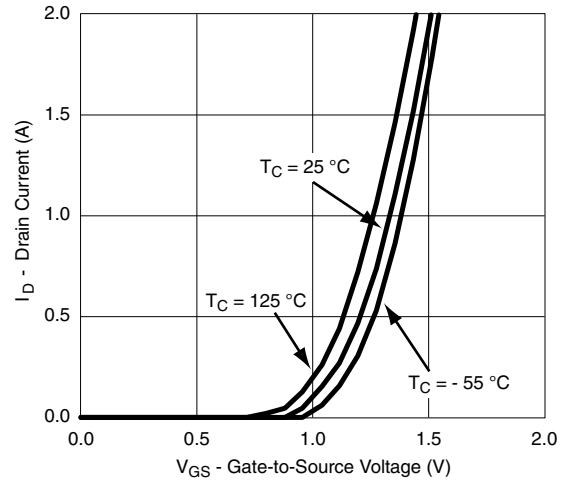
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.  
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

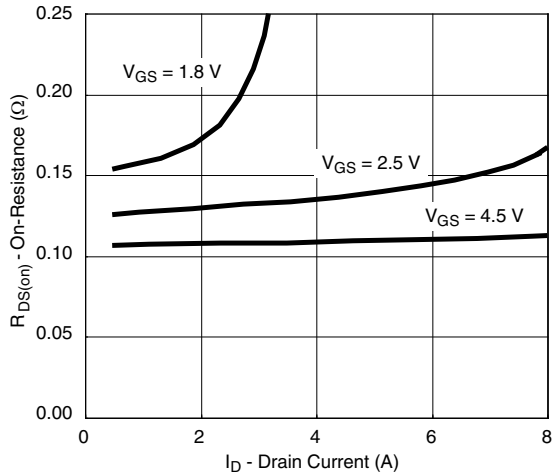
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



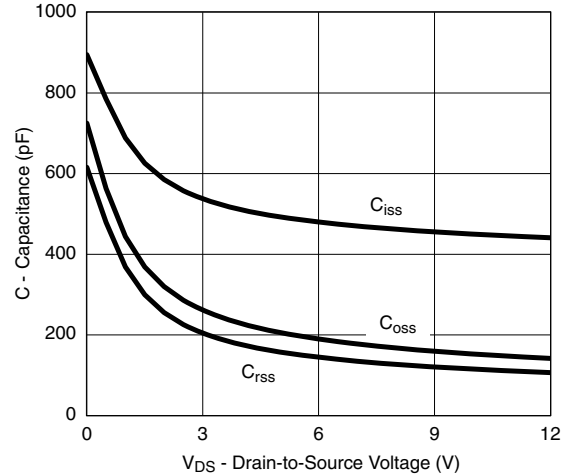
**Output Characteristics**



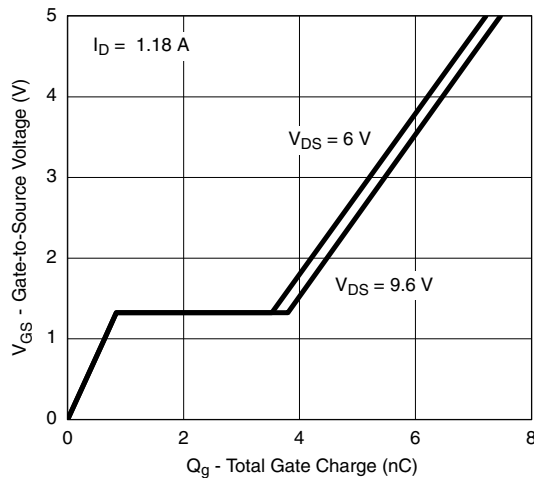
**Transfer Characteristics Curves vs. Temp.**



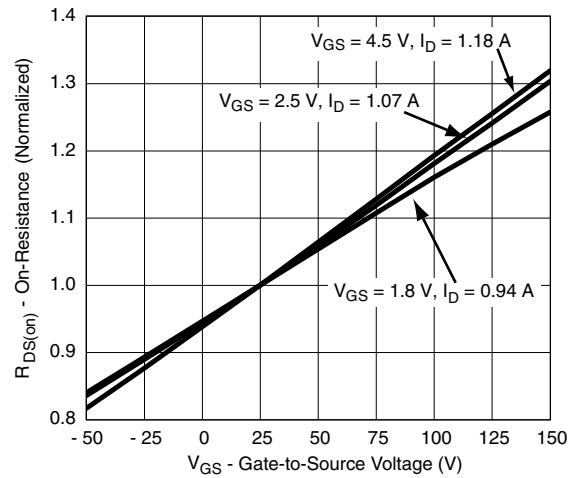
**On-Resistance vs. Drain Current**



**Capacitance**

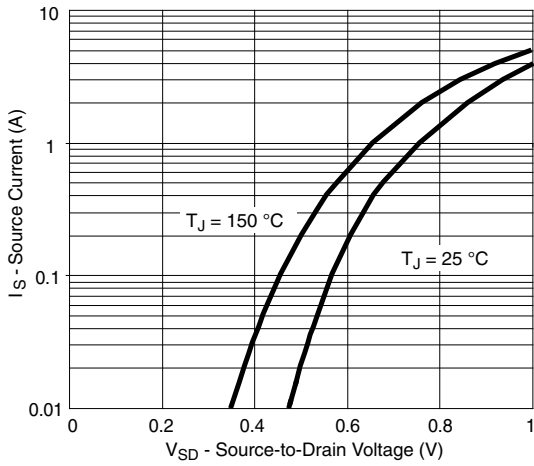


**Gate Charge**

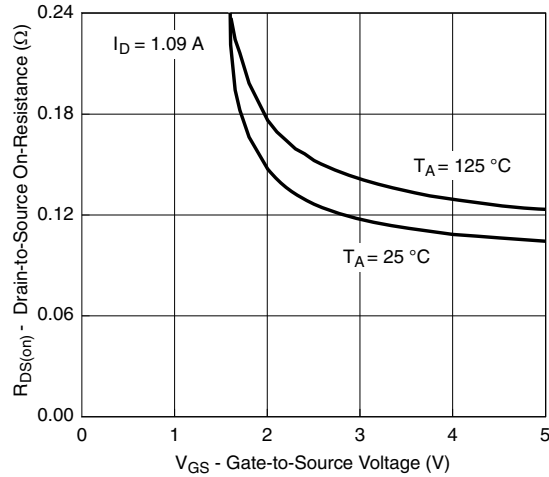


**On-Resistance vs. Junction Temperature**

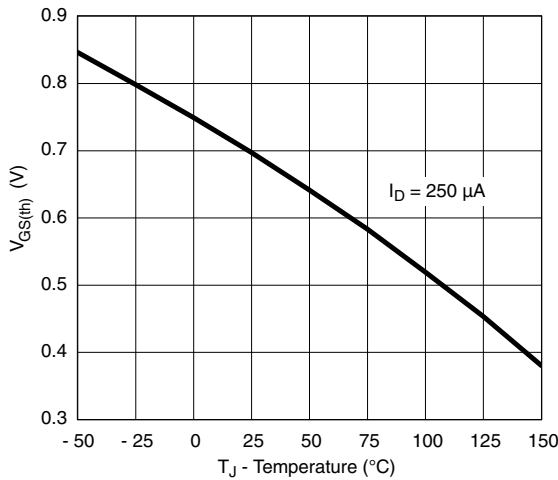
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



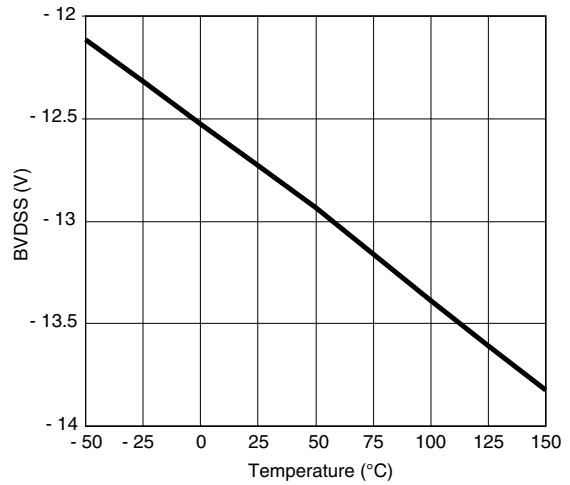
Source-Drain Diode Forward Voltage



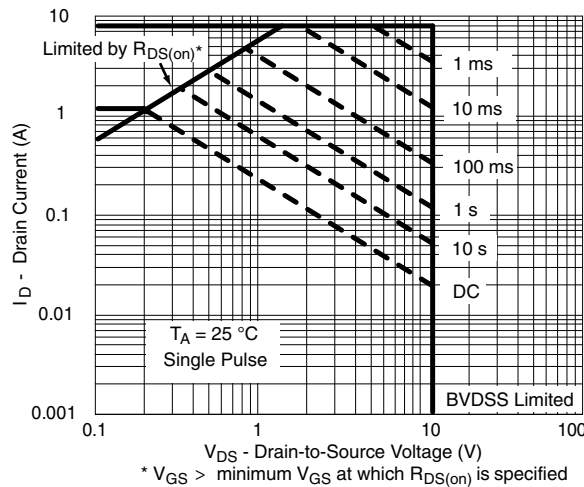
$R_{DS(on)}$  vs.  $V_{GS}$  vs. Temperature



Threshold Voltage

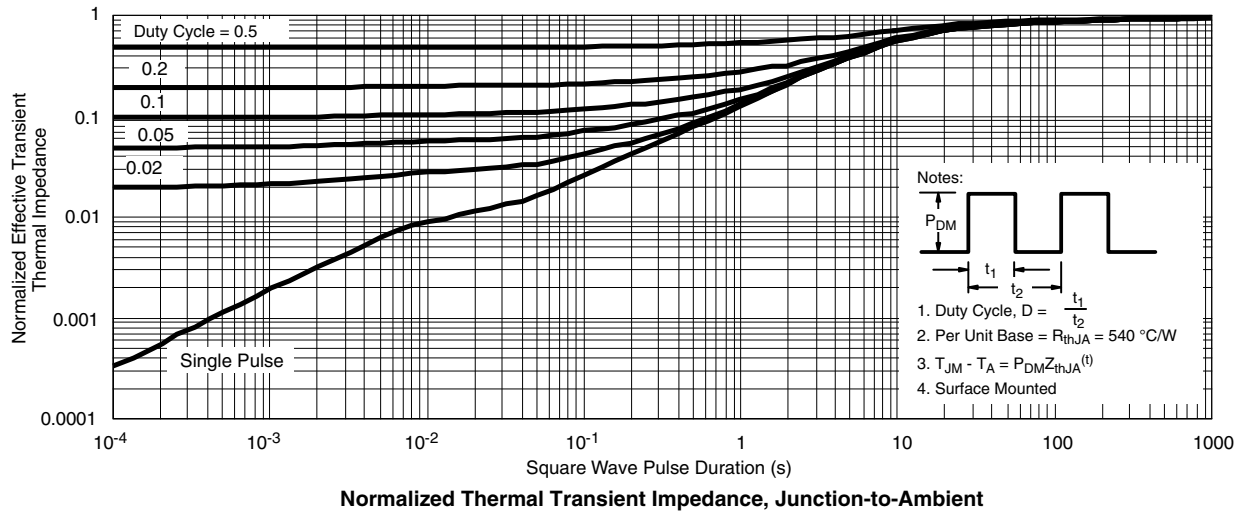


$BV_{DSS}$  vs. Temperature



Safe Operating Area, Junction-to-Ambient

**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see [www.vishay.com/ppg?74320](http://www.vishay.com/ppg?74320).

## SC-89 6-Leads (SOT-563F)



### Notes

- Dimensions in millimeters.
- Dimension D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.
- Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.
- Datums A, B and D to be determined 0.10 mm from the lead tip.
- Terminal numbers are shown for reference only.
- These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.56	0.58	0.60
A1	0	0.02	0.10
b	0.15	0.22	0.30
c	0.10	0.14	0.18
D	1.50	1.60	1.70
E	1.50	1.60	1.70
E1	1.15	1.20	1.25
e	0.45	0.50	0.55
e1	0.95	1.00	1.05
L	0.25	0.35	0.50
L1	0.10	0.20	0.30

C14-0439-Rev. C, 11-Aug-14  
DWG: 5880

## RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads  
Dimensions in Inches/(mm)

[Return to Index](#)



## Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.