

Vishay Siliconix

# N-Channel Reduced Q<sub>g</sub>, Fast Switching MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
20	0.0028 at V <sub>GS</sub> = 10 V	25		
	0.0040 at $V_{GS}$ = 4.5 V	22		

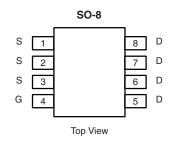
#### FEATURES

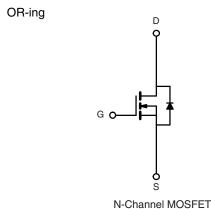
- Halogen-free According to IEC 61249-2-21
  Definition
- Extremely Low Q<sub>gd</sub> for Switching Losses
- Ultra-Low On-Resistance
- 100 % R<sub>g</sub> and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

- Synchronous Rectifier in Low Power DC/DC Converters
- POL

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Ordering Information: Si4398DY-T1-E3 (Lead (Pb)-free) Si4398DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b>	<b>S</b> (T <sub>A</sub> = 25 °C, unle	ess otherwise r	noted)		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	20		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	25	19	
Continuous Drain Current $(T_j = 150^{\circ}C)^{*}$	T <sub>A</sub> = 70 °C		20	13	
Pulsed Drain Current (10 µs Pulse Width)		I <sub>DM</sub>	70		А
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	2.9	1.3	
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	40		
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	80		mJ
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	3.5	1.6	W
maximum Fower Dissipation*	T <sub>A</sub> = 70 °C		2.2	1.0	vv
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stq</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 s	R <sub>thJA</sub>	29	35	
Maximum Sunction-to-Amblent	Steady State		67	80	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	13	16	

Notes:

Downloaded from Arrow.com.

a. Surface mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$	1.0		3.0	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
Zara Cata Valtaga Drain Current	I <sub>DSS</sub>	$V_{DS} = 20 V, V_{GS} = 0 V$ $V_{DS} = 20 V, V_{GS} = 0 V, T_{J} = 55 °C$			1	μΑ	
Zero Gate Voltage Drain Current					5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 V$ , $V_{GS} = 10 V$	50			А	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 25 A	0.0023 0.002		0.0028	Ω	
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 22 \text{ A}$		0.0033	0.0040	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 15 A		95		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = 2.9 \text{ A}, V_{GS} = 0 \text{ V}$		0.72	1.1	V	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			5620		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1340			
Reverse Transfer Capacitance	C <sub>rss</sub>			540			
Total Gate Charge	Qg			34	50		
Gate-Source Charge	Q <sub>gs</sub>	$Q_{gs}$ $V_{DS} = 10 V, V_{GS} = 4.5 V, I_{D} = 20 A$		17.5		nC	
Gate-Drain Charge	Q <sub>gd</sub>			7.5			
Gate Resistance	R <sub>g</sub>		0.7	1.4	2.1	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			23	35		
Rise Time	t <sub>r</sub>	$V_{DD} = 10 \text{ V}, \text{ R}_{L} = 10 \Omega$		15	23		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 1 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 6 \Omega$		80	120	ns	
Fall Time	t <sub>f</sub>			23	35		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.9 A, dI/dt = 100 A/μs		50	80		

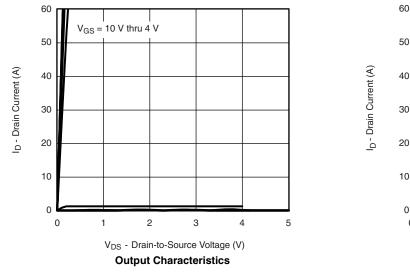
Notes:

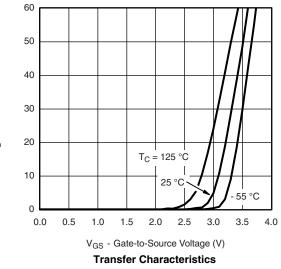
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





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# Si4398DY

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Ciss

Coss

8

25

50

I<sub>D</sub> = 25 A

4

6

75

100

125

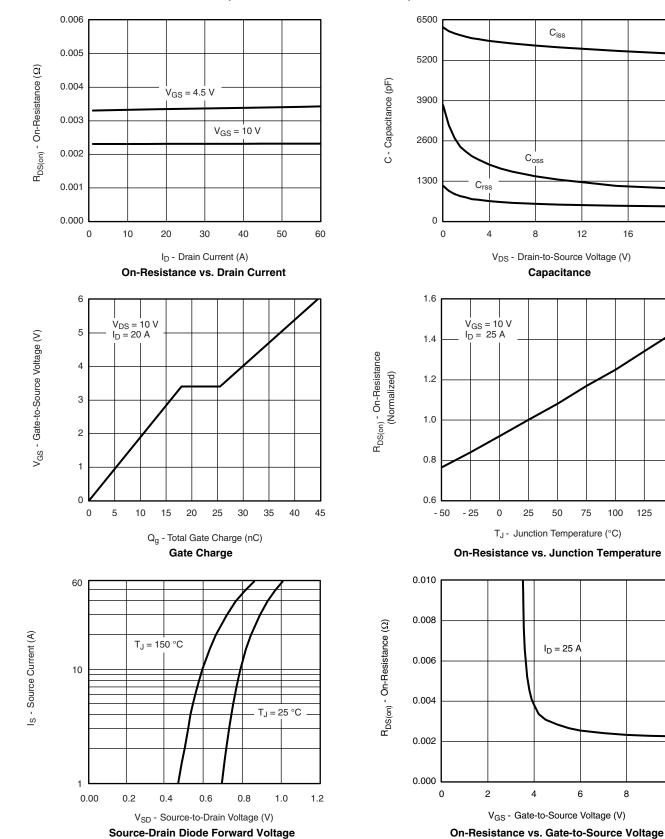
150

12

Capacitance

16

20



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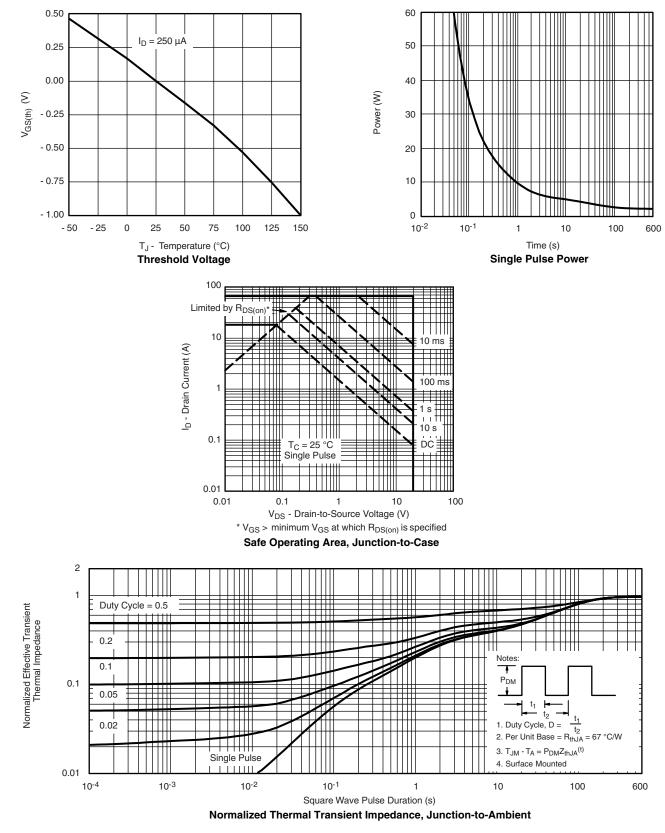
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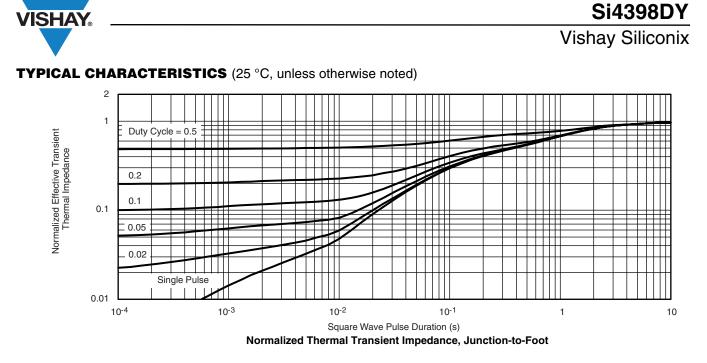
### Si4398DY

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#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?73018">www.vishay.com/ppg?73018</a>.



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