

Vishay Siliconix

P-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)	Q _g (Typ.)			
- 100	0.019 at V _{GS} = - 10 V	- 90	97 nC			
	0.021 at V _{GS} = - 4.5 V	- 85	97110			

FEATURES

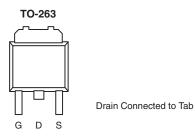
- TrenchFET[®] Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

GC

s

P-Channel MOSFET





Top View

Ordering Information: SUM90P10-19L-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATING	S T _A = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol Limit		Unit	
Drain-Source Voltage		V _{DS}	- 100	- V	
Gate-Source Voltage		V _{GS}	± 20		
	T _C = 25 °C		- 90		
Continuous Drain Current ($T_1 = 150 \ ^{\circ}C$)	T _C = 125 °C	I _D	- 52		
Continuous Drain Current (1j = 150°C)	T _A = 25 °C		- 17.2 ^{b, c}		
	T _A = 125 °C		- 9.9 ^{b, c}	A	
Pulsed Drain Current		I _{DM}	- 90	A	
Continuous Source-Drain Diode Current	T _C = 25 °C	I _S	- 250		
Continuous Source-Drain Diode Current	T _A = 25 °C		- 9 ^{b, c}		
Avalanche Current	L = 0.1 mH	I _{AS}	- 70		
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	245	mJ	
	T _C = 25 °C	Р	375		
Movimum Dower Discinction	T _C = 125 °C		125	w	
Maximum Power Dissipation	T _A = 25 °C	P _D	13.6 ^{b, c}	vv	
	T _A = 125 °C		4.5 ^{b, c}	1	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R _{thJA}	8	11	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	0.33	0.4	C/W	

Notes:

a. Package Limited.b. Surface Mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under Steady State conditions is 40 °C/W.

Document Number: 73474 S09-0659-Rev. E, 20-Apr-09

SUM90P10-19L

Vishay Siliconix



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = -250 \mu A$	- 100			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$			- 125			
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μA		5.9		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D =$ - 250 μ A	- 1		- 3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = -100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	IDSS	V_{DS} = - 100 V, V_{GS} = 0 V, T_{J} = 175 °C			- 500	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10$ V, $V_{GS} = -10$ V	- 90			А	
	Б	$V_{GS} = -10$ V, $I_{D} = -20$ A		0.0156	0.019	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 15 A		0.0173	0.021		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 20 A		80		S	
Dynamic ^b	1 1			<u> </u>		1	
Input Capacitance	C _{iss}			11100			
Output Capacitance	C _{oss}	V _{DS} = - 50 V, V _{GS} = 0 V, f = 1 MHz		700		pF	
Reverse Transfer Capacitance	C _{rss}	$v_{\rm DS} = -30$ v, $v_{\rm GS} = 0$ v, $r = 1.0012$		1690			
		$V_{DS} = -50 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -90 \text{ A}$ 217	326				
Total Gate Charge	Qg			97	146	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = - 50 V, V_{GS} = - 4.5 V, I_{D} = - 90 A		42			
Gate-Drain Charge	Q _{gd}			51			
Gate Resistance	R _g	f = 1 MHz		3.5		Ω	
Turn-On Delay Time	t _{d(on)}			20	30		
Rise Time	t _r	V_{DD} = - 50 V, R_L = 0.56 Ω		510	855	ns	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 90 A, V_GEN = - 10 V, R_g = 1 Ω		145	220		
Fall Time	t _f			870	1300		
Drain-Source Body Diode Characte	ristics						
Continous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 90	А	
Pulse Diode Forward Current ^a	I _{SM}				- 250		
Body Diode Voltage	V _{SD}	I _S = - 20 A		- 0.8	- 1.5	V	
Body Diode Reverse Recovery Time	t _{rr}			80	120	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = - 20 A, dl/dt = 100 A/μs, T _J = 25 °C		220	330	nC	
Reverse Recovery Fall Time	t _a			56			
Reverse Recovery Rise Time	t _b			24		ns	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

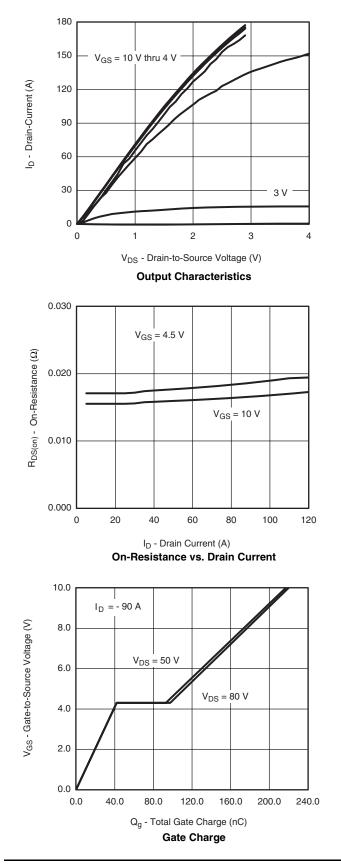
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

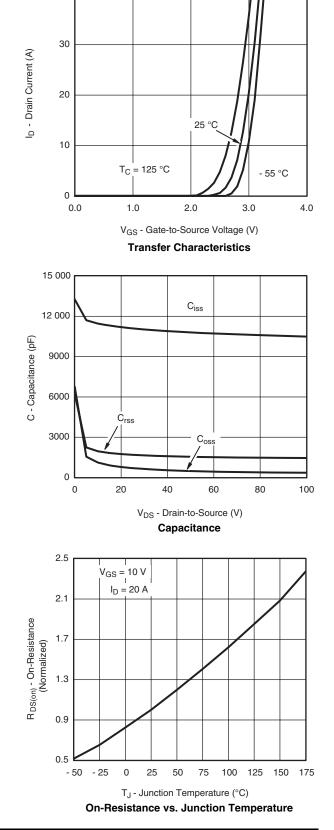


SUM90P10-19L

Vishay Siliconix

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





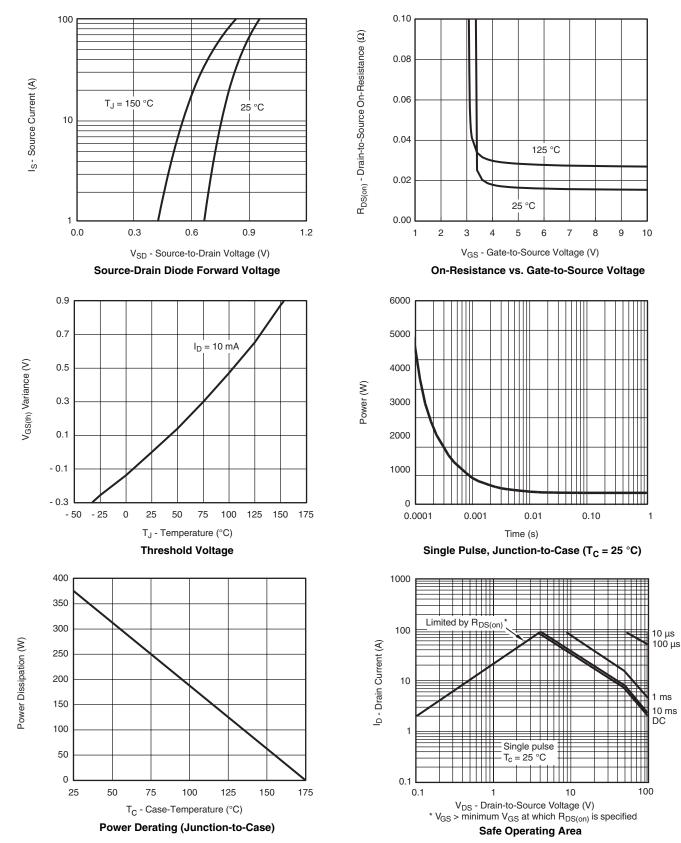
40

Document Number: 73474 S09-0659-Rev. E, 20-Apr-09

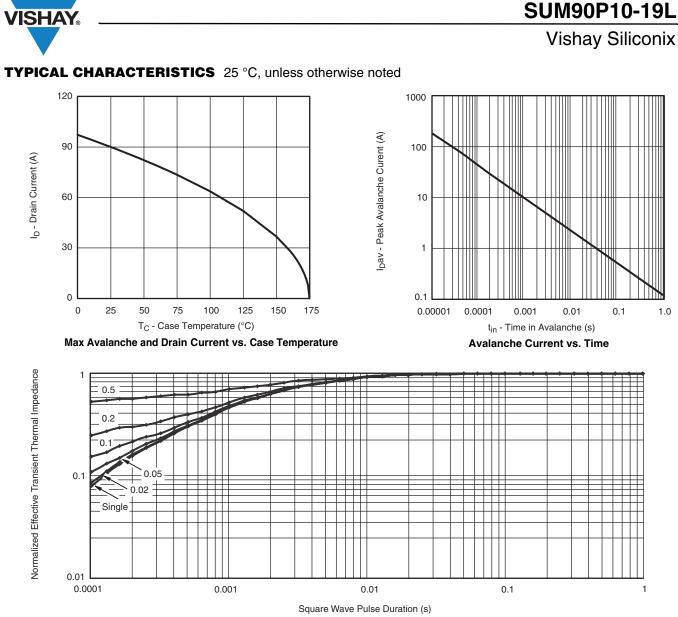
Vishay Siliconix



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Document Number: 73474 S09-0659-Rev. E, 20-Apr-09



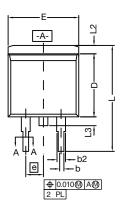
Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?73474.



Vishay Siliconix

TO-263 (D²PAK): 3-LEAD









DETAIL A (ROTATED 90°)



		INC	HES	MILLIMETERS			
DIM.		MIN.	MAX.	MIN.	MAX.		
A		0.160	0.190	4.064	4.826		
b		0.020	0.039	0.508	0.990		
b1		0.020	0.035	0.508	0.889		
b2		0.045	0.055	1.143	1.397		
с*	Thin lead	0.013	0.018	0.330	0.457		
C	Thick lead	0.023	0.028	0.584	0.711		
c1	Thin lead	0.013	0.017	0.330	0.431		
CI	Thick lead	0.023	0.027	0.584	0.685		
c2		0.045	0.055	1.143	1.397		
	D	0.340	0.380	8.636	9.652		
	D1	0.220	0.240	5.588	6.096		
	D2	0.038	0.042	0.965	1.067		
	D3	0.045	0.055	1.143	1.397		
	D4	0.044	0.052	1.118	1.321		
	E	0.380	0.410	9.652	10.414		
E1		0.245	-	6.223	-		
E2		0.355	0.375	9.017	9.525		
E3		0.072	0.078	1.829	1.981		
e		0.100) BSC	2.54	BSC		
K		0.045	0.055	1.143	1.397		
L		0.575	0.625	14.605	15.875		
	L1	0.090	0.110	2.286	2.794		
L2		0.040	0.055	1.016	1.397		
L3		0.050	0.070	1.270	1.778		
L4		0.010) BSC	0.254	.254 BSC		
М		-	0.002	-	0.050		
ECN: T13-0707-Rev. K, 30-Sep-13 DWG: 5843							

Notes

- 1. Plane B includes maximum features of heat sink tab and plastic. 2. No more than 25 % of L1 can fall above seating plane by
- max. 8 mils. 3. Pin-to-pin coplanarity max. 4 mils.
- 4. *: Thin lead is for SUB, SYB.
 - Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

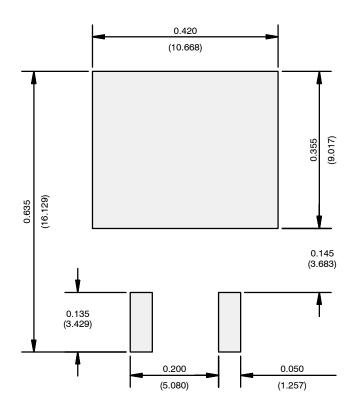
6. This feature is for thick lead.

Revison: 30-Sep-13

1



RECOMMENDED MINIMUM PADS FOR D²PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.