



# Dual P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)			
- 20	0.490 at $V_{GS} = -4.5 \text{ V}$	- 1.0			
	$0.750 \text{ at V}_{GS} = -2.5 \text{ V}$	- 0.81			
	1.1 at V <sub>GS</sub> = - 1.8 V	- 0.67			

#### **FEATURES**

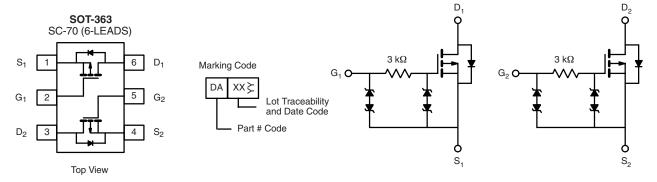
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFETs: 1.8 V Rated
- ESD Protected: 3000 V
- Thermally Enhanced SC-70 Package
- Compliant to RoHS Directive 2002/95/EC



ROHS COMPLIANT HALOGEN FREE

#### **APPLICATIONS**

- Load Switching
- PA Switch
- · Level Switch



Ordering Information: Si1913EDH-T1-E3 (Lead (Pb)-free)

Si1913EDH-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 20		- V	
Gate-Source Voltage		$V_{GS}$	± 12			
Continuous Drain Current (T <sub>.I</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 1.0	- 0.88		
Continuous Diam Current (1) = 150 °C)	T <sub>A</sub> = 85 °C		- 0.72	- 0.63	Α	
Pulsed Drain Current		I <sub>DM</sub>	- 3		A	
Continuous Diode Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 0.61	- 0.48	ı	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.74	0.57	W	
	T <sub>A</sub> = 85 °C		0.38	0.30	٧٧	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 s	- R <sub>thJA</sub>	130	170		
Maximum Junction-to-Ambient	Steady State		170	220	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	80	100		

#### Notes:

a. Surface mounted on 1" x 1" FR4 board.

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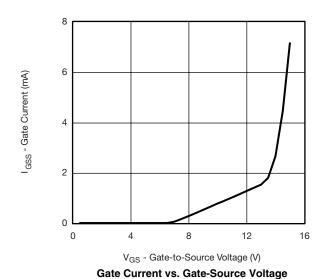
SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Тур.	Max.	Unit			
Static								
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -100 \mu A$	- 0.45			V		
Gate-Body Leakage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 1.5	μΑ		
	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 10	mA		
Zero Gate Voltage Drain Current		$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$			- 1.0	μΑ		
	I <sub>DSS</sub>	$V_{DS}$ = - 16 V, $V_{GS}$ = 0 V, $T_{J}$ = 85 °C			- 5.0			
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 4.5 V	- 2.0			Α		
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 0.88 A		0.400	0.490	Ω		
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 0.71 A		0.610	0.750			
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 0.2 A		0.850	1.10			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 0.88 A		1.5		S		
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = - 0.47 A, V <sub>GS</sub> = 0 V		- 0.85	- 1.2	V		
Dynamic <sup>b</sup>	_							
Total Gate Charge	Qg			1.2	1.8	nC		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -0.88 \text{ A}$		0.3				
Gate-Drain Charge	Q <sub>gd</sub>			0.3				
Turn-On Delay Time	t <sub>d(on)</sub>			0.15	0.23			
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, R_{L} = 20 \Omega$		0.48	0.72			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -0.5$ A, $V_{GEN} = -4.5$ V, $R_g = 6$ Ω		0.84	1.2	μs		
Fall Time	t <sub>f</sub>	1		0.85	1.2			

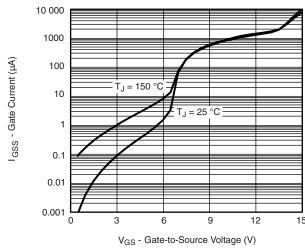
#### Notes

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



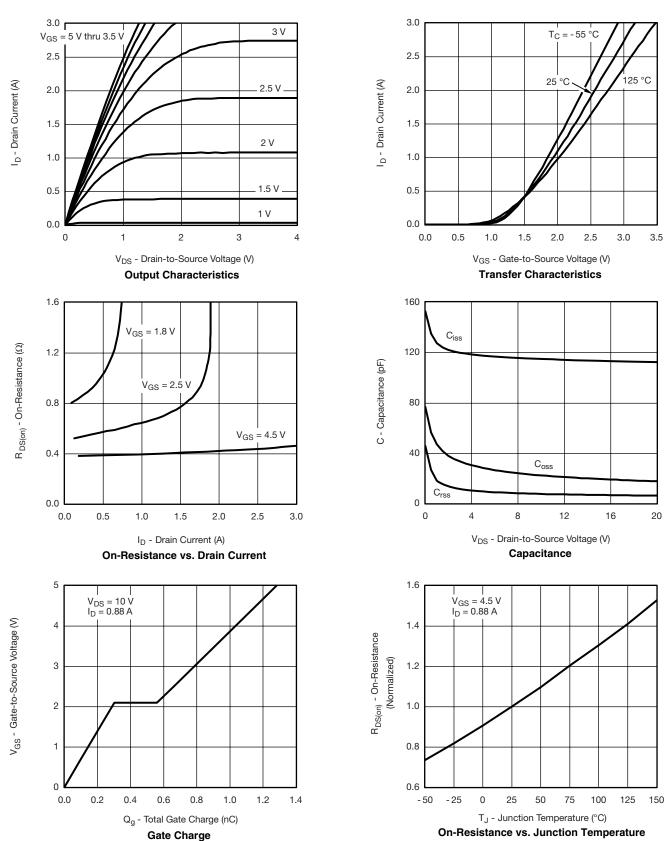


Gate Current vs. Gate-Source Voltage





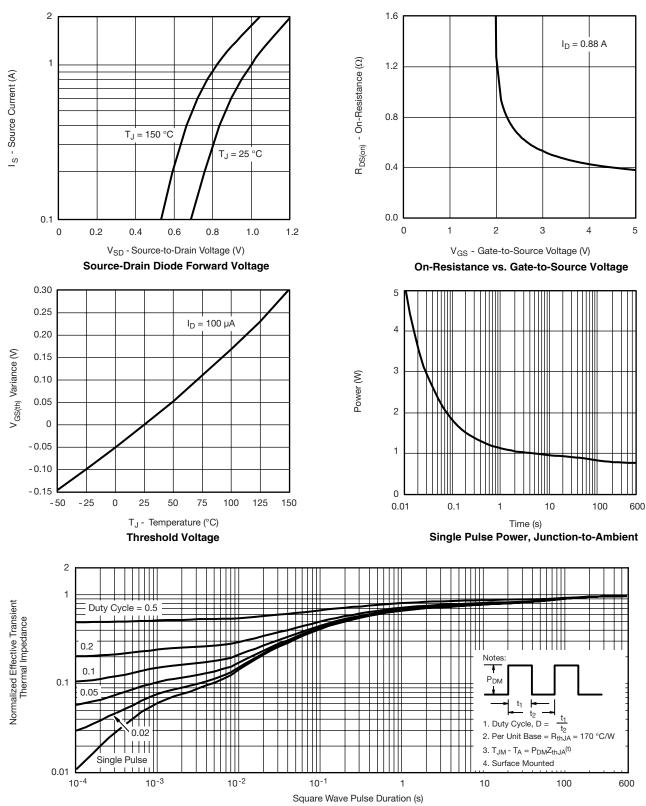
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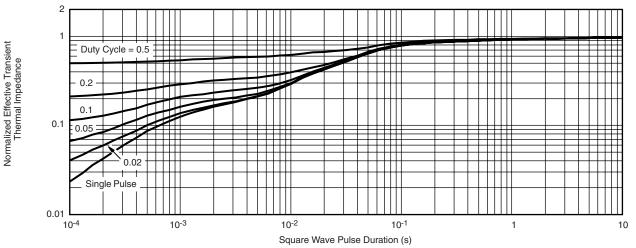
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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





# TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?71415">www.vishay.com/ppg?71415</a>.

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