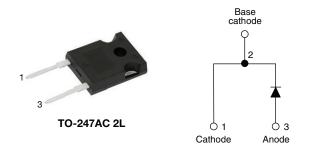


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High Voltage, Input Rectifier Diode, 40 A



PRIMARY CHARACTERISTICS				
I _{F(AV)}	40 A			
V_{R}	800 V to 1200 V			
V _F at I _F	1.1 V			
I _{FSM}	475 A			
T _J max.	150 °C			
Package	TO-247AC 2L			
Circuit configuration	Single			

FEATURES

- Very low forward voltage drop
- 150 °C max. operating junction temperature
- Glass passivated pellet chip junction
- Designed and qualified according to JEDEC®-JESD 47





APPLICATIONS

- Input rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Sinusoidal waveform	40	A			
V _{RRM}	Range	800/1200	V			
I _{FSM}		475	Α			
V _F	40 A, T _J = 25 °C	1.1	V			
T _J		-40 to +150	°C			

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA			
VS-40EPS08-M3	800	900	1			
VS-40EPS12-M3	1200	1300	1			

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	T _C = 105 °C, 180° conduction half sine wave	40			
Maximum peak one cycle		10 ms sine pulse, rated V _{RRM} applied	400	Α		
non-repetitive surge current	I _{FSM}	10 ms sine pulse, no voltage reapplied	475			
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	800	A ² s		
waximum i-t for fusing	I-t	10 ms sine pulse, no voltage reapplied	1131	A-S		
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied	11 310	A²√s		

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CON	IDITIONS	VALUES	UNITS	
Maximum forward voltage drop	V	20 A, T _J = 25 °C		1.0	V	
Maximum forward voltage drop	V_{FM}	40 A, T _J = 25 °C		1.1	V	
Forward slope resistance	r _t	T _{.1} = 150 °C		7.16	mΩ	
Threshold voltage	V _{F(TO)}	1 J = 150 C		0.74	V	
Maximum rayaraa laakaga ayrrant	1	T _J = 25 °C	V _R = Rated V _{RRM}	0.1	m A	
Maximum reverse leakage current	I _{RM}	T _J = 150 °C	v _R = nateu v _{RRM}	1.0	mA	

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storrage temperature range)	T _J , T _{Stg}		-40 to +150	°C
Maximum thermal resistance, junction to case		R_{thJC}	DC operation	0.6	
Maximum thermal resistance, junction to ambient		R _{thJA}		40	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, flat, smooth, and greased	0.2	
Approximate weight				6	g
Approximate weight				0.21	oz.
Mounting torque	minimum			6 (5)	kgf · cm
Mounting torque —	maximum			12 (10)	(lbf · in)
Maulina davia			Case style TO-247AC 2L	40EF	PS08
Marking device			Case style TO-247AC modified	40EF	PS12

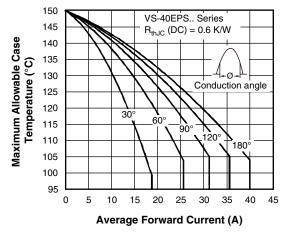


Fig. 1 - Current Rating Characteristics

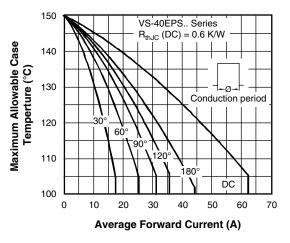


Fig. 2 - Current Rating Characteristics

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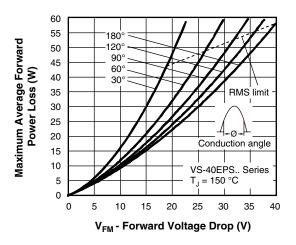


Fig. 3 - Forward Power Loss Characteristics

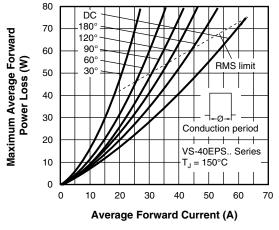


Fig. 4 - Forward Power Loss Characteristics

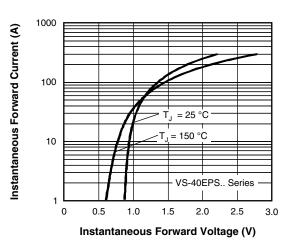


Fig. 5 - Forward Voltage Drop Chacteristics

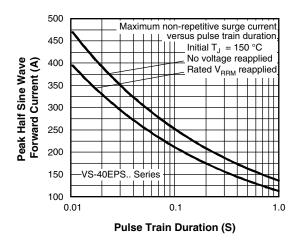


Fig. 6 - Maximum Non-Repetitive Surge Current

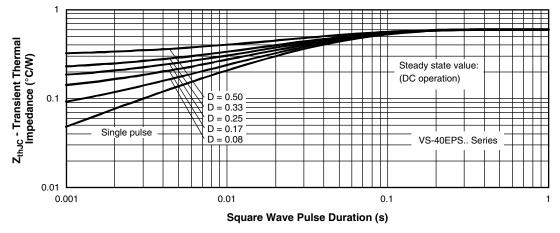


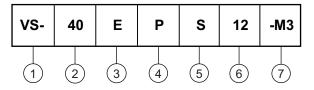
Fig. 7 - Thermal Impedance Z_{thJC} Characteristics



Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Current rating (40 = 40 A)

Circuit configuration:

E = single diode

4 - Package:

P = TO-247AC 2L

5 - Type of silicon:

S = standard recovery rectifier

08 = 800 V

Voltage rating

12 = 1200 V

7 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-40EPS08-M3	25	500	Antistatic plastic tubes			
VS-40EPS12-M3	25	500	Antistatic plastic tubes			

LINKS TO RELATED DOCUMENTS				
Dimensions —	TO-247AC 2L	www.vishay.com/doc?96144		
Difficusions	TO-247AC modified	www.vishay.com/doc?95541		
Port marking information	TO-247AC 2L	www.vishay.com/doc?95648		
Part marking information —	TO-247AC modified	www.vishay.com/doc?95442		
SPICE model		www.vishay.com/doc?96047		

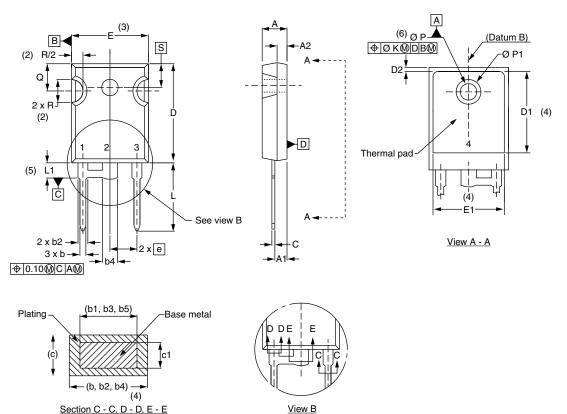
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Vishay Semiconductors

TO-247AC modified - 50 mils L/F

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.35	0.020	0.053	
Е	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØK	0.2	0.254)10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	'BSC	
	·		·	·	

Notes

- (1) Dimensioning and tolerance per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension c and Q

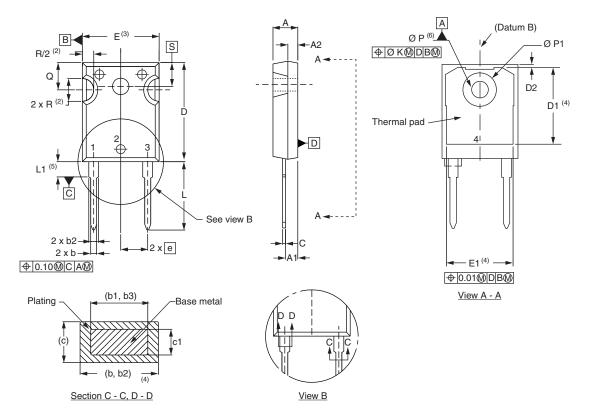
Revision: 20-Apr-17 1 Document Number: 95541



Vishay Semiconductors

TO-247AC 2L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	ETERS INCHES		NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
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