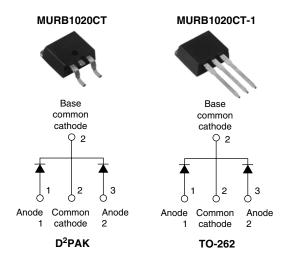


Vishay High Power Products

Ultrafast Rectifier, 2 x 5 A FRED Pt[®]



PRODUCT SUMMARY				
t _{rr}	25 ns			
I _{F(AV)}	2 x 5 A			
V _R	200 V			

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- · Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage		V _{RRM}		200	V
Average rectified forward current	per leg	I _{F(AV)}		5	•
Average rectined forward current	total device		Rated V _R , T _C = 149 °C	10	
Non-repetitive peak surge current per leg		I _{FSM}		50	A
Peak repetitive forward current per leg		I _{FM}	Rated V _R , square wave, 20 kHz, T _C = 149 °C	10	
Operating junction and storage temperatures		T _J , T _{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-	
		I _F = 5 A, T _J = 125 °C	-	0.87	0.99	v
Forward voltage V _F	V _F	I _F = 10 A, T _J = 125 °C	-	1.02	1.20	
		I _F = 10 A, T _J = 25 °C	-	1.12	1.25	
Reverse leakage current I _R		$V_{R} = V_{R}$ rated	-	-	10	
	^I R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250	μΑ
Junction capacitance	CT	V _R = 200 V		8	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8.0 -		nH		



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F=1.0~A,~dI_F/dt=50~A/\mu s,~V_R=30~V$		-	-	35	
Reverse recovery time t _{rr}	÷	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{REC} = 0.25 \text{ V}$		-	-	25	ns
	T _J = 25 °C		-	24	-		
		T _J = 125 °C	$I_F = 5 A$	-	35	-	
Pook recovery ourrent	l	T _J = 25 °C		-	- 3.3	-	А
Peak recovery current I _{RRM}	T _J = 125 °C	dI _F /dt = 200 A/μs V _B = 160 V	-	5.0	-	~	
Reverse recovery charge Q	$Q_{rr} \qquad \frac{T_J = 25 \text{ °C}}{T_J = 125 \text{ °C}}$	T _J = 25 °C		-	33	-	nC
			-	76	-		

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	-	5	
Thermal resistance, junction to ambient per leg	R _{thJA}		-	-	50	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking davias		Case style D ² PAK	MURB1020CT			•
Marking device		Case style TO-262	MURB1020CT-1			



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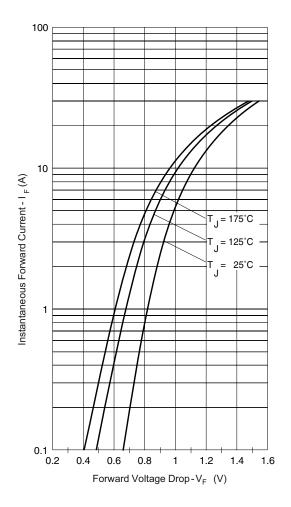


Fig. 1 - Typical Forward Voltage Drop Characteristics

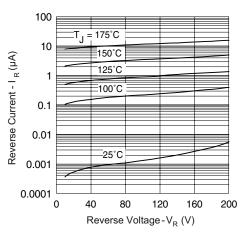


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

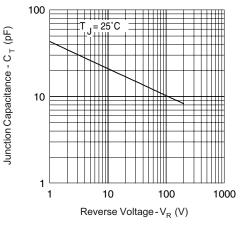


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

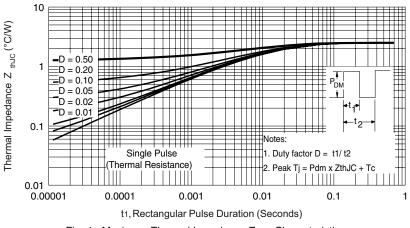


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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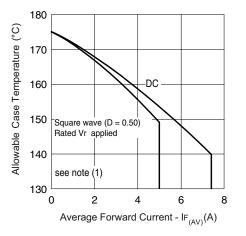


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

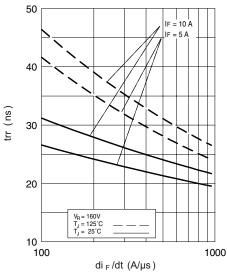


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

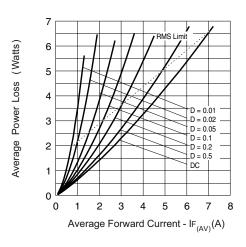
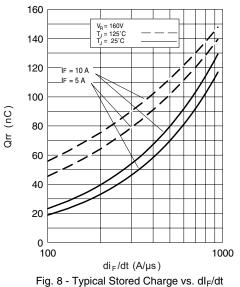


Fig. 6 - Forward Power Loss Characteristics

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; $I_R at V_{R1} = Rated V_R$



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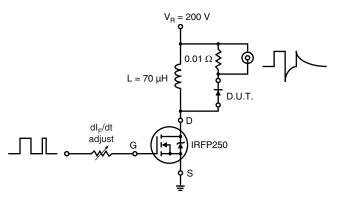


Fig. 9 - Reverse Recovery Parameter Test Circuit

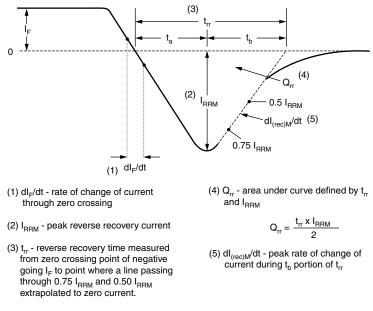


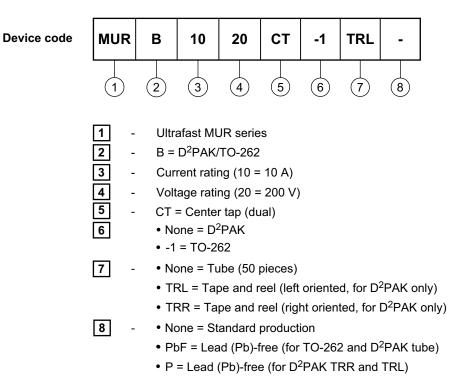
Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95014				
Part marking information	www.vishay.com/doc?95008			
Packaging information	www.vishay.com/doc?95032			



Vishay

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