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Vishay Siliconix

Automotive N-Channel 40 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	40		
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.040		
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.055		
I _D (A)	6		
Configuration	Single		

guration Single T0-236 (SOT-23) G G

SQ2318ES* * Marking Code: 8Gxxx

Top View

N-Channel MOSFET

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- AEC-Q101 Qualified^c
- 100 % Rq and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



ROHS COMPLIANT HALOGEN FREE

ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and Halogen-free	SQ2318ES-T1-GE3

ABSOLUTE MAXIMUM RATING	S ($T_C = 25 ^{\circ}C$, unless	s otherwise noted	(k	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	40	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	T _C = 25 °C	- I _D	6	
	T _C = 125 °C		3.5	
Continuous Source Current (Diode Conduction)		I _S	3.8	Α
Pulsed Drain Current ^a		I _{DM}	24	
Single Pulse Avalanche Current	1 0111	I _{AS}	11	
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	6	mJ
Maximum Power Dissipation ^a	T _C = 25 °C	- P _D	3	w
	T _C = 125 °C		1	
Operating Junction and Storage Temperatur	e Range	T _J , T _{stq}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient PC	CB Mount ^b	R _{thJA}	166	°C/W	
Junction-to-Foot (Drain)		R _{th IF}	50	C/VV	

Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. When mounted on 1" square PCB (FR-4 material).
- c. Parametric verification ongoing.



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PARAMETER	SYMBOL	TES	TEST CONDITIONS		TYP.	MAX.	UNIT	
Static							,	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		40	-	=.	V	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$		1.5	2.0	2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
Zero Gate Voltage Drain Current		V _{GS} = 0 V	V _{DS} = 40 V	-	-	1		
	I _{DSS}	V _{GS} = 0 V	V _{DS} = 40 V, T _J = 125 °C	-	-	50	μΑ	
		V _{GS} = 0 V	V _{DS} = 40 V, T _J = 175 °C	-	-	150		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	10	-		Α	
Drain-Source On-State Resistance ^a		V _{GS} = 10 V	I _D = 3.9 A	-	0.030	0.040	Ω	
	Б	V _{GS} = 10 V	I _D = 3.9 A, T _J = 125 °C	=.	-	0.064		
	R _{DS(on)}	V _{GS} = 10 V	I _D = 3.9 A, T _J = 175 °C	-	-	0.078		
		V _{GS} = 4.5 V	I _D = 3.5 A	-	0.041	0.055		
Forward Transconductanceb	9 _{fs}	V _{DS}	= 15 V, I _D = 2.5 A	-	10	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}		V _{DS} = 25 V, f = 1 MHz	=.	550	690	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		=.	69	75		
Reverse Transfer Capacitance	C _{rss}			=.	40	50		
Total Gate Charge ^c	Qg			-	12.5	19		
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V	$V_{GS} = 10 \text{ V}$ $V_{DS} = 20 \text{ V}, I_D = 3.9 \text{ A}$	-	1.9	-	nC	
Gate-Drain Charge ^c	Q _{gd}			-	2.7	-		
Gate Resistance	R_g	f = 1 MHz		0.84	1.67	2.50	Ω	
Turn-On Delay Time ^c	t _{d(on)}			-	7	11		
Rise Time ^c	t _r	V_{DD} = 20 V, R_L = 20 Ω $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω		-	13	20	ns	
Turn-Off Delay Time ^c	t _{d(off)}			-	18	27		
Fall Time ^c	t _f			-	5	8		
Source-Drain Diode Ratings and Chara	icteristics ^b							
Pulsed Current ^a	I _{SM}			-	-	24	Α	
Forward Voltage	V _{SD}	I _F = 2.5 A, V _{GS} = 0 V		_	0.8	1.2	V	

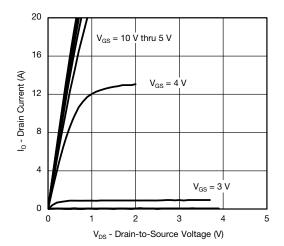
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

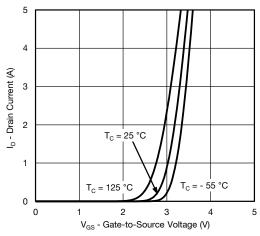
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



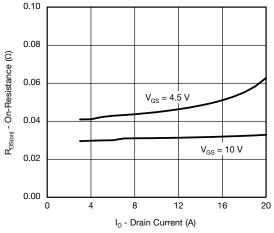
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



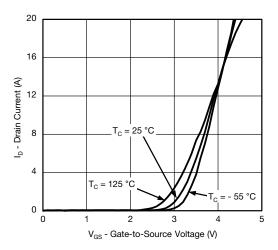
Output Characteristics



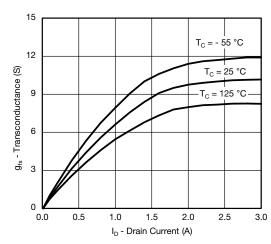
Transfer Characteristics



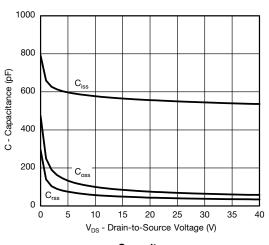
On-Resistance vs. Drain Current



Transfer Characteristics



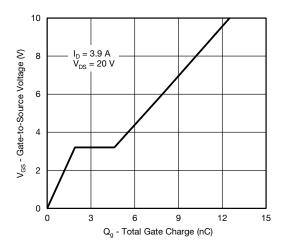
Transconductance



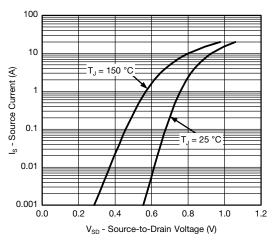
Capacitance



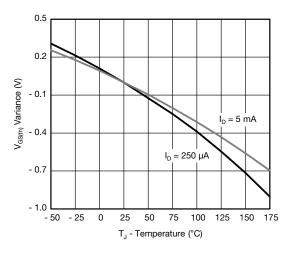
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



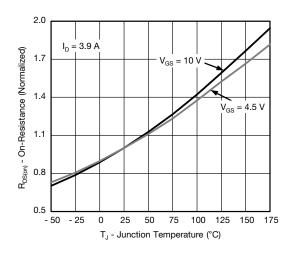
Gate Charge



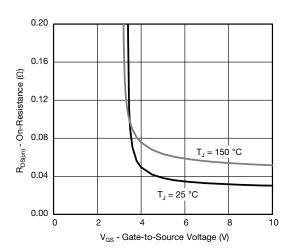
Source Drain Diode Forward Voltage



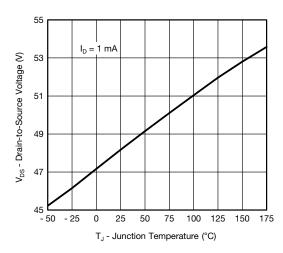
Threshold Voltage



On-Resistance vs. Junction Temperature



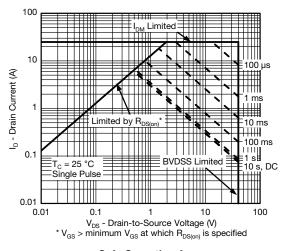
On-Resistance vs. Gate-to-Source Voltage



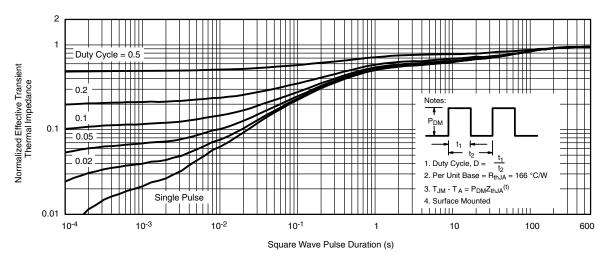
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



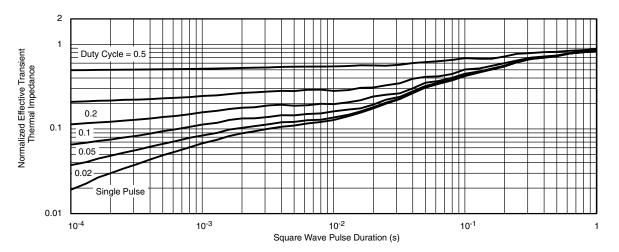
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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