HALOGEN

FREE

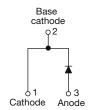


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Vishay Semiconductors

### Ultralow V<sub>F</sub> Hyperfast Rectifier for Discontinuous Mode PFC, 8 A FRED Pt<sup>®</sup>





PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	8 A							
V <sub>R</sub>	600 V							
V <sub>F</sub> at I <sub>F</sub>	0.81 V							
t <sub>rr</sub> typ.	60 ns							
T <sub>J</sub> max.	175 °C							
Package	TO-220AC 2L							
Circuit configuration	Single							

#### **FEATURES**

- · Hyperfast recovery time
- Benchmark ultralow forward voltage drop
- 175 °C operating junction temperature
- · Low leakage current
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

State of the art, ultralow  $V_F$ , soft-switching hyperfast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

#### **APPLICATIONS**

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Repetitive peak reverse voltage	$V_{RRM}$		600	V					
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 160 °C	8	Α					
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	175						
Repetitive peak forward current	I <sub>FM</sub>		16						
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C					

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	TEST CONDITIONS MIN. TYP. MA						
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	600	-	-	V			
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 8 A	-	0.96	1.05	V			
		I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	-	0.81	0.86				
Reverse leakage current	I <sub>R</sub>	$V_R = V_R$ rated	=.	0.05	5				
neverse leakage current		T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	20	100	μA			
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	17	-	pF			
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nΗ			

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>C</sub> = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time	t <sub>rr</sub>	$I_F = 1 A, dI_F/dt = 100$	$I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$			100				
		$I_F = 8 \text{ A}, dI_F/dt = 100 \text{ A}$	-	150	250					
		T <sub>J</sub> = 25 °C		-	170	-	ns - A			
		T <sub>J</sub> = 125 °C		-	250	-				
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 8 A	-	15	-				
		T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = 200 A/µs V <sub>R</sub> = 390 V	-	20	-				
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	] ''		1.3	-				
		T <sub>J</sub> = 125 °C		-	2.6	-	μC			

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	$T_J$ , $T_{Stg}$		-65	-	175	°C			
Thermal resistance, junction-to-case	$R_{thJC}$		-	1.4	2	°C/W			
Thermal resistance, junction-to-ambient per leg	$R_{thJA}$	Typical socket mount	-	-	70				
Thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-				
Weight			-	2.0	-	g			
vveignt			-	0.07	-	oz.			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Marking device		Case style TO-220AC 2L	8ETL06						

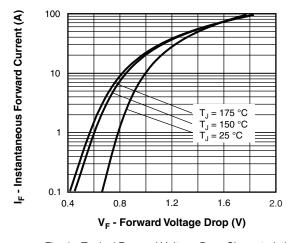


Fig. 1 - Typical Forward Voltage Drop Characteristics

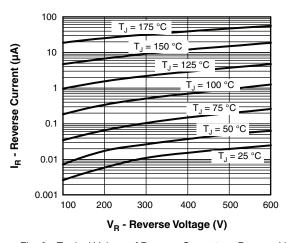


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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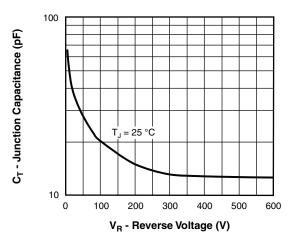


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

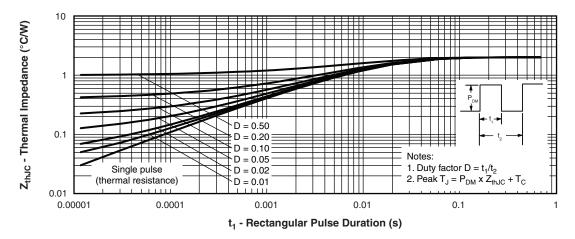


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

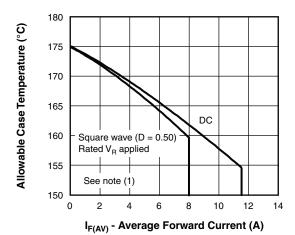


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

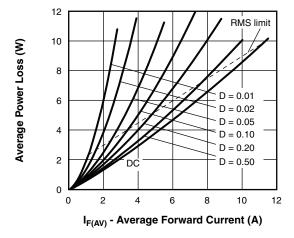


Fig. 6 - Forward Power Loss Characteristics

### Note

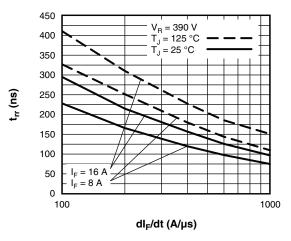
Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 5);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = rated V_R$ 

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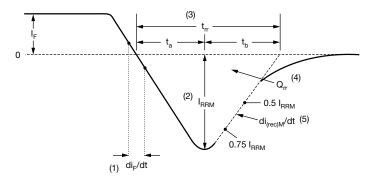
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5000  $_{\rm F} = 16 \, {\rm A}$ 4500  $I_{F} = 8 \text{ A}$ 4000 3500 Q<sub>rr</sub> (nC) 3000 2500 2000 1500  $V_{R} = 390 \text{ V}$  $T_J = 125 \,^{\circ}\text{C}$   $T_J = 25 \,^{\circ}\text{C}$ 1000 500 100 1000 dl<sub>F</sub>/dt (A/μs)

Fig. 7 - Typical Reverse Recovery Time vs. dI<sub>F</sub>/dt

Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

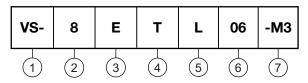
Fig. 9 - Reverse Recovery Waveform and Definitions



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### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

Current rating (8 = 8 A)

**3** - E = single diode

- T = TO-220, D<sup>2</sup>PAK (TO-263AB)

L = ultralow V<sub>F</sub> hyperfast recovery

6 - Voltage rating (06 = 600 V)

7 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

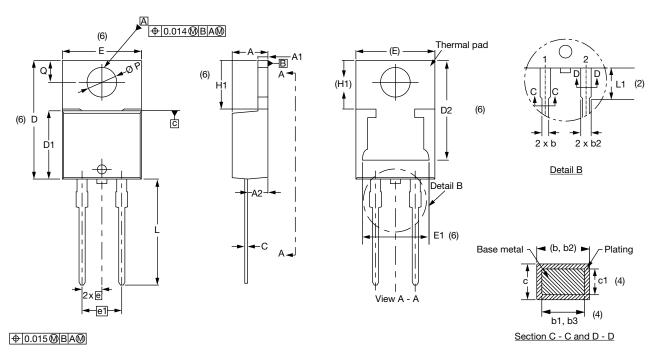
ORDERING INFORMATION (Example)									
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION							
VS-8ETL06-M3	50	Antistatic plastic tubes							

LINKS TO RELATED DOCUMENTS							
Dimensions <u>www.vishay.com/doc?96156</u>							
Part marking information	www.vishay.com/doc?95391						
SPICE model	www.vishay.com/doc?96053						

# Vishay Semiconductors

### **TO-220AC 2L**

### **DIMENSIONS** in millimeters and inches



Lead tip

Conforms to JEDEC® outline TO-220AC

SYMBOL	MILLIMETERS		INC	HES	NOTES	NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STIVIBOL		MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7	
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6	
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6	
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105		
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208		
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6	
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552		
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2	
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154		
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118		
D1	8.38	9.02	0.330	0.355							•		

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- $^{(7)}\,$  Outline conforms to JEDEC® TO-220, except D2

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