

VS-8TQ060-M3, VS-8TQ080-M3, VS-8TQ100-M3

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Vishay Semiconductors

COMPLIANT

HALOGEN

FREE

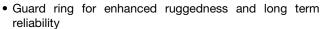
High Performance Schottky Rectifier, 8 A



PRIMARY CHARACTERISTICS							
I _{F(AV)}	8 A						
V _R	60 V, 80 V, 100 V						
V _F at I _F	0.58 V						
I _{RM} max.	7 mA at 125 °C						
T _J max.	175 °C						
E _{AS}	7.5 mJ						
Package	2L TO-220AC						
Circuit configuration	Single						

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-8TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL CHARACTERISTICS VALUES U							
I _{F(AV)}	Rectangular waveform	8	Α				
V_{RRM}	Range	60 to 100	V				
I _{FSM}	t _p = 5 μs sine	850	Α				
V _F	8 A _{pk} , T _J = 125 °C	0.58	V				
T _J	Range	-55 to +175	°C				

VOLTAGE RATINGS								
PARAMETER SYMBOL VS-8TQ060-M3 VS-8TQ080-M3 VS-8TQ100-								
Maximum DC reverse voltage	V_R	60	80	100	V			
Maximum working peak reverse voltage	V_{RWM}	60	60	100	V			

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS			
Maximum average forward current, see fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 157 °C	8	А				
Maximum peak one cycle non-repetitive	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	850	Α			
surge current, see fig. 7		10 ms sine or 6 ms rect. pulse	V _{RRM} applied	230				
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 0.50 A, L = 60 mH		7.50	mJ			
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		0.50	Α			

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		8 A	T = 25 °C	0.72	V			
Maximum forward voltage drop	V _{FM} ⁽¹⁾	16 A	T _J = 25 °C	0.88				
See fig. 1		8 A	T 105 00	0.58				
		16 A	T _J = 125 °C	0.69				
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	\/	0.55	mA			
See fig. 2		T _J = 125 °C	V_R = rated V_R	7				
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		500	pF			
Typical series inductance	L _S	Measured lead to lead 5 n	8	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range	T _J , T _{Stg}		-55 to +175	°C					
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	2.0	°C/W					
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, and greased	0.50						
Approximate weight			2	g					
Approximate weight			0.07	oz.					
Mounting torque minimum			6 (5)	kgf ⋅ cm					
Mounting torque maximum			12 (10)	(lbf \cdot in)					
			8TQ060						
Marking device		Case style 2L TO-220AC	8TQ080						
			8TQ100						



100

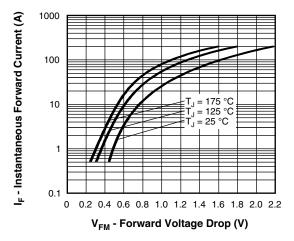
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 $T_J = 175$ °C



I_R - Reverse Current (mA) T₁ = 150 °C T_J = 125 °C T_J = 100 °C 0.1 T₁ = 75 °C 0.01 0.001 T_{.1} = 25 °C 0.0001 20 60 80 100 V_R - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs.Reverse Voltage

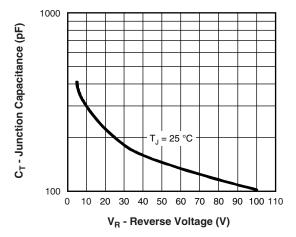


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

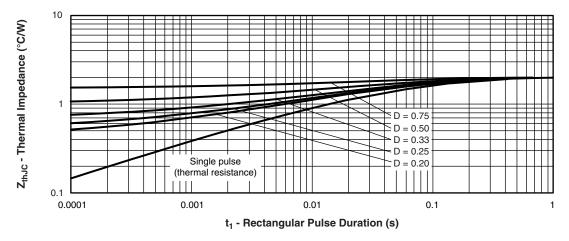


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics





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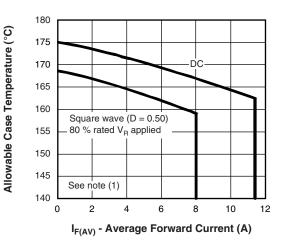


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

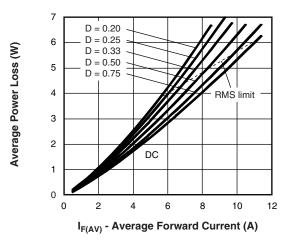


Fig. 6 - Forward Power Loss Characteristics

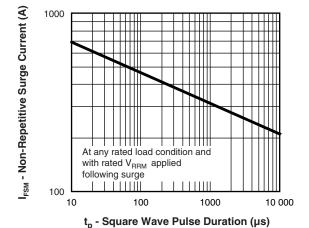


Fig. 7 - Maximum Non-Repetitive Surge Current

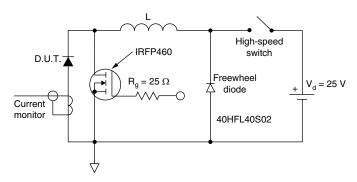


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{thJC}}; \\ \text{Pd} = & \text{forward power loss} = \text{I}_{\text{F(AV)}} \times \text{V}_{\text{FM}} \text{ at } (\text{I}_{\text{F(AV)}}/\text{D}) \text{ (see fig. 6)}; \\ \text{Pd}_{\text{REV}} = & \text{inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D)}; \text{I}_{\text{R}} \text{ at } \text{V}_{\text{R1}} = 80 \% \text{ rated V}_{\text{R}} \\ \end{array}$

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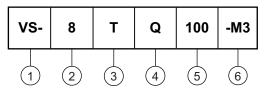


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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (8 = 8 A)

- Package:

T = TO-220

4 - Schottky "Q" series

Voltage ratings — 060 = 60 V 080 = 80 V

6 - Environmental digit

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

100 = 100 V

ORDERING INFORMATION (Example)								
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION						
VS-8TQ060-M3	50	Antistatic plastic tubes						
VS-8TQ080-M3	50	Antistatic plastic tubes						
VS-8TQ100-M3	50	Antistatic plastic tubes						

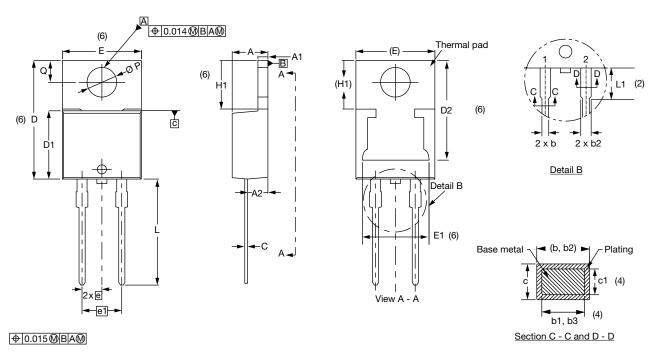
LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?96156					
Part marking information	www.vishay.com/doc?95391					
SPICE model	www.vishay.com/doc?96227					

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TO-220AC 2L

DIMENSIONS in millimeters and inches



Lead tip

Conforms to JEDEC® outline TO-220AC

SYMBOL	MILLIN	IETERS	INC	HES	NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355							•	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- $^{(7)}\,$ Outline conforms to JEDEC® TO-220, except D2

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